

**Maintenance Diagrams
for
MODELS T25/T50/T80**

**Maintenance Diagrams
for
MODELS T25/T50/T80**

March 1981

[illegible]

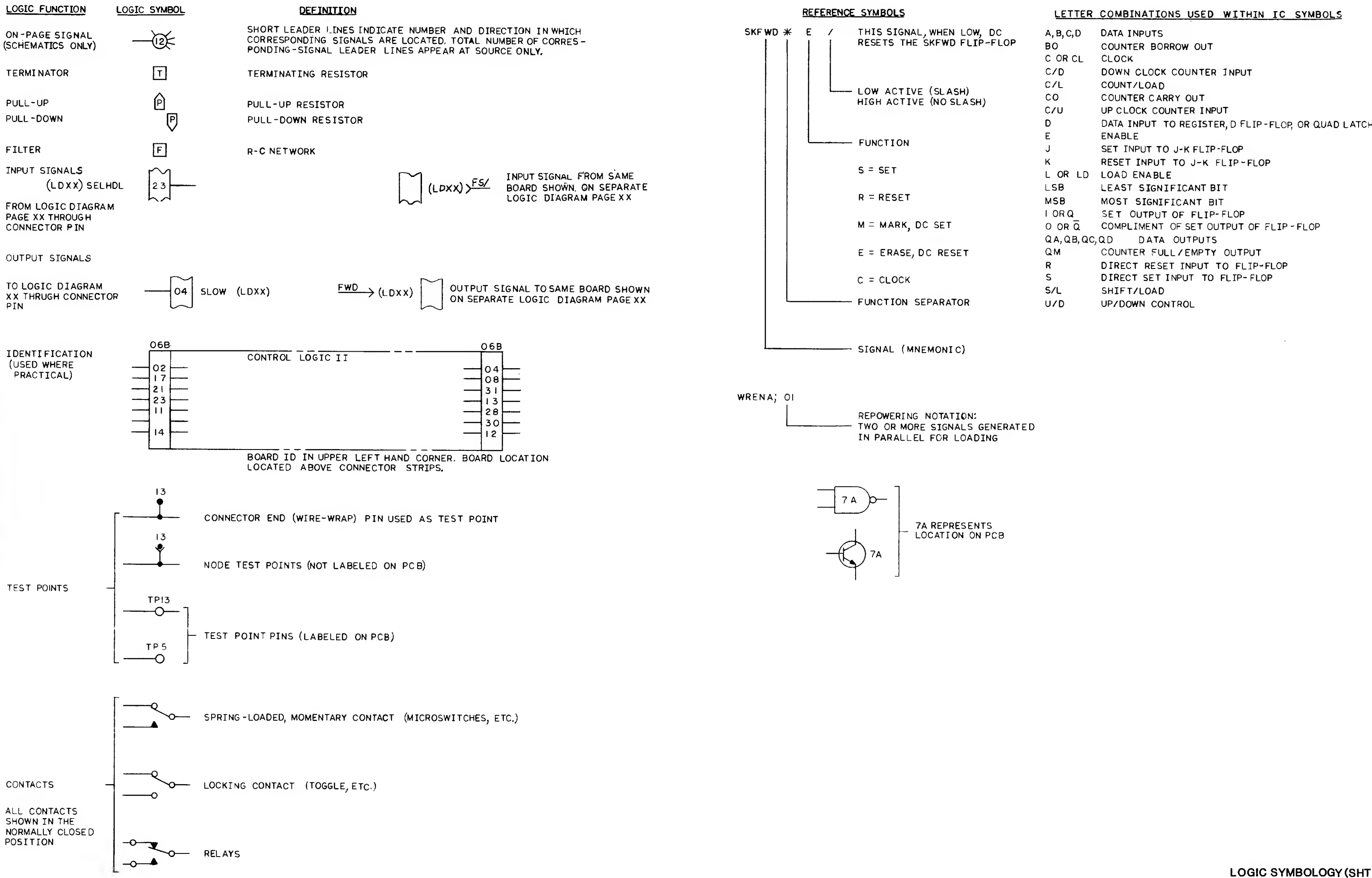
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*22985-001 USED ON T25 UNITS
 22985-002 USED ON T50 UNITS

LOGIC FUNCTION	LOGIC SYMBOL	DEFINITION	LOGIC FUNCTION	LOGIC SYMBOL	DEFINITION	LOGIC FUNCTION	LOGIC SYMBOL	DEFINITION
INVERTER NAT DM936		HIGH IN, LOW OUT	FETS		ARROW IN; 0 VGS TURNS ON	ONE-OF-EIGHT DATA SELECTOR 74151A		BINARY ADDRESS INPUTS SELECT ONE OF EIGHT DATA INPUTS WHEN E IS LOW. SELECTED DATA INPUT IS AVAILABLE AT I OUTPUT AND COMPLEMENT IS AVAILABLE AT O OUTPUT. WHEN E IS HIGH I OUTPUT IS LOW AND O OUTPUT IS HIGH,
3-INPUT NAND GATE (AND FUNCTION) 7410		LOW IN, HIGH OUT	ONE-SHOTS		ARROW OUT; 0 VGS TURNS ON			
MULTIPLE-INPUT 7410 7430		ALL HIGHS IN, LOW OUT ANY LOW IN, HIGH OUT PERFORMS AND FUNCTION	(OUTPUT PULSE DURATION DE- TERMINED BY EXTERNAL R/C) MOT MC8602		IC ONE-SHOT	DECODER, 4-TO-10 LINE 7442 7445 SELECT INPUTS		CAN BE USED AS EITHER 4-TO-10 LINE OR 3-TO-8 LINE DECODER. BINARY CONFIGURATION OF SELECT INPUTS DETERMINES WHICH OUTPUT GOES LOW. USING MSB AS LOW ENABLE INPUT CONVERTS FUNCTION TO 3-TO-8 LINE DECODER
3-INPUT NAND GATE (OR FUNCTION) 7410		ANY LOW IN, HIGH OUT ALL HIGHS IN, LOW OUT PERFORMS OR FUNCTION	AND-NOR GATE 7451		A INPUT LOW, OUTPUT PULSE TRIGGERED BY NEGATIVE-GOING B IN- PUT. B INPUT HIGH, OUTPUT PULSE TRIGG- ERED BY POSITIVE- GOING A INPUT LOW RESET INTERRUPTS TIMING CYCLE	COUNTER 74193		IF THE LOAD INPUT IS HIGH, THE COUNTER INCREMENTS WITH A POSITIVE TRANSITION AT THE C/U INPUT AND IT DECREMENTS WITH A POSITIVE TRANSITION AT THE C/D INPUT. THE OUTPUTS ARE SET TO THE DATA INPUTS WHEN THE LOAD IS LOW. THE COUNTER IS CLEARED WHEN THE R INPUT IS HIGH. THE CARRY OUT IS LOW ACTIVE WHEN THE COUNTER IS COUNTING UP AND THE DATA OUTPUTS ARE ALL HIGH AND THE C/U INPUT IS LOW. THE BORROW OUTPUT IS LOW ACTIVE WHEN THE DATA OUTPUTS ARE ALL LOW AND THE C/D INPUT IS LOW.
MULTIPLE-INPUT 7420 7430			CROSS-COUPLED LATCH		HIGH A AND B OR HIGH C AND D, LOW OUT LOW A OR B AND LOW C OR D, HIGH OUT	D FLIP-FLOP 7474		OPERATES AS A 4-BIT COUNTER WHEN A OUTPUT CONNECTED TO CLOCK C2 DATA INPUT. PRESETS COUNTER WHEN COUNT/LOAD INPUT GOES LOW. BINARY COUNT IS INCREMENTED EACH TIME CLOCK C1 GOES LOW. WITH COUNT/LOAD INPUT HIGH. ALL OUTPUTS GO LOW WHEN RESET GOES LOW REGARDLESS OF CLOCK INPUTS.
2-INPUT OR GATE 7432		EITHER INPUT HIGH, OUTPUT HIGH	J-K FLIP-FLOP 74107		SET INPUT LOW, LATCH SETS SET AND RESET INPUT LOW, BOTH OUTPUTS HIGH RESET INPUT LOW, LATCH RESETS			I OUTPUT FOLLOWS DATA INPUT WHEN CLOCK GOES LOW TO HIGH. O OUTPUT COMPLEMENTS I OUTPUT. DIRECT SET INPUT LOW, FLIP-FLOP SETS, DIRECT RES- ET INPUT LOW, FLIP-FLOP RESETS.
2-INPUT NAND GATE 7400		BOTH INPUTS HIGH OUTPUT LOW			J INPUT HIGH AND K INPUT LOW, I OUTPUT GOES HIGH WHEN CLOCK GOES LOW. J INPUT LOW AND K INPUT HIGH, I OUTPUT GOES LOW WHEN CLOCK GOES LOW. O OUT- PUT COMPLEMENTS I OUT- PUT. J AND K INPUTS BOTH HIGH, FLIP-FLOP TOGGLES WHEN CLOCK GOES LOW. DIRECT RESET INPUT LOW, FLIP-FLOP RESETS WITHOUT CLOCK	QUAD LATCH 7475		CLOCK INPUT HIGH, Q OUTPUT FOLLOWS D INPUT
2-INPUT AND GATE 7408		BOTH INPUTS HIGH, OUTPUT HIGH			INPUTS OR OUTPUTS THRU O'S ARE LOW ACTIVE, INPUTS ARE A B AND C _I (CARRY IN). OUTPUTS ARE S _I (S/I IS INVERSE OF S) AND C _O (CARRY OUT)	QUAD 2-TO-1 MULTIPLEXER 74157		WITH ENABLE INPUT LOW, OUTPUTS CONNECT TO 0 DATA INPUTS WHEN S IS LOW, AND TO 1 DATA INPUTS WHEN S IS HIGH
EXCLUSIVE OR GATE 7486		INPUTS NOT IDENTICAL, HIGH OUTPUT, INPUTS IDENTICAL, LOW OUTPUT	FULL ADDERS MOT MC8304			COUNTER 74191		THE COUNTER INCREMENTS OR DECREMENTS WITH A POSITIVE TRANSITION OF THE CLOCK INPUT IF THE ENABLE INPUT IS LOW. A HIGH AT THE ENABLE INPUT INHIBITS COUNTING. WHEN THE DOWN/UP INPUT IS LOW, THE COUNTER INCREMENTS. THE OUTPUTS ARE SET TO THE DATA INPUTS WHEN THE LOAD INPUT IS LOW. THE M OUTPUT IS HIGH WHEN ALL DATA OUTPUT ARE HIGH AND IN COUNT UP MODE OR WHEN ALL ARE LOW AND IN COUNT DOWN MODE. THE CARRY OUT IS LOW ACTIVE WHEN ALL DATA OUTPUTS ARE HIGH AND THE CLOCK IS LOW AND IN UP COUNT MODE. THE CARRY OUT IS ALSO LOW ACTIVE WHEN ALL DATA OUTPUTS ARE LOW AND THE CLOCK IS LOW AND IN DOWN COUNT MODE.
LINE RECEIVERS 7404		HIGH IN, LOW OUT						
LINE DRIVERS 7404		LOW IN, HIGH OUT						
RELAY DRIVERS		LOW IN, LOW OUT						
LAMP DRIVERS 7407		HIGH IN, HIGH OUT						
MOT MC1733		DIFFERENTIAL						
LM324		OPERATIONAL OR SCHMITT TRIGGER COMPARATOR						
LM339		LOW ENABLE						
AMPLIFIERS		HIGH ENABLE						

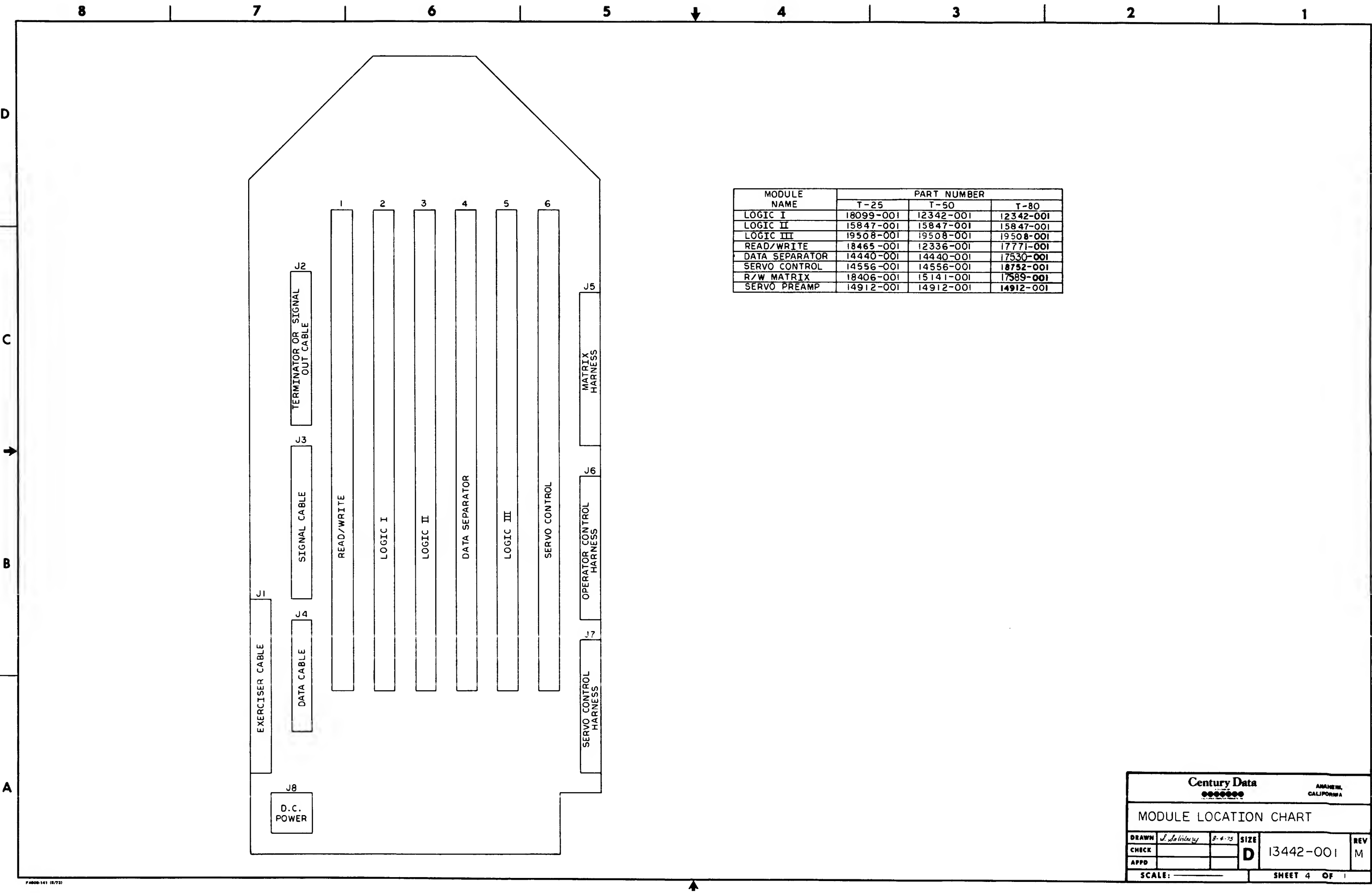
LOGIC SYMBOLS(SHT 1)

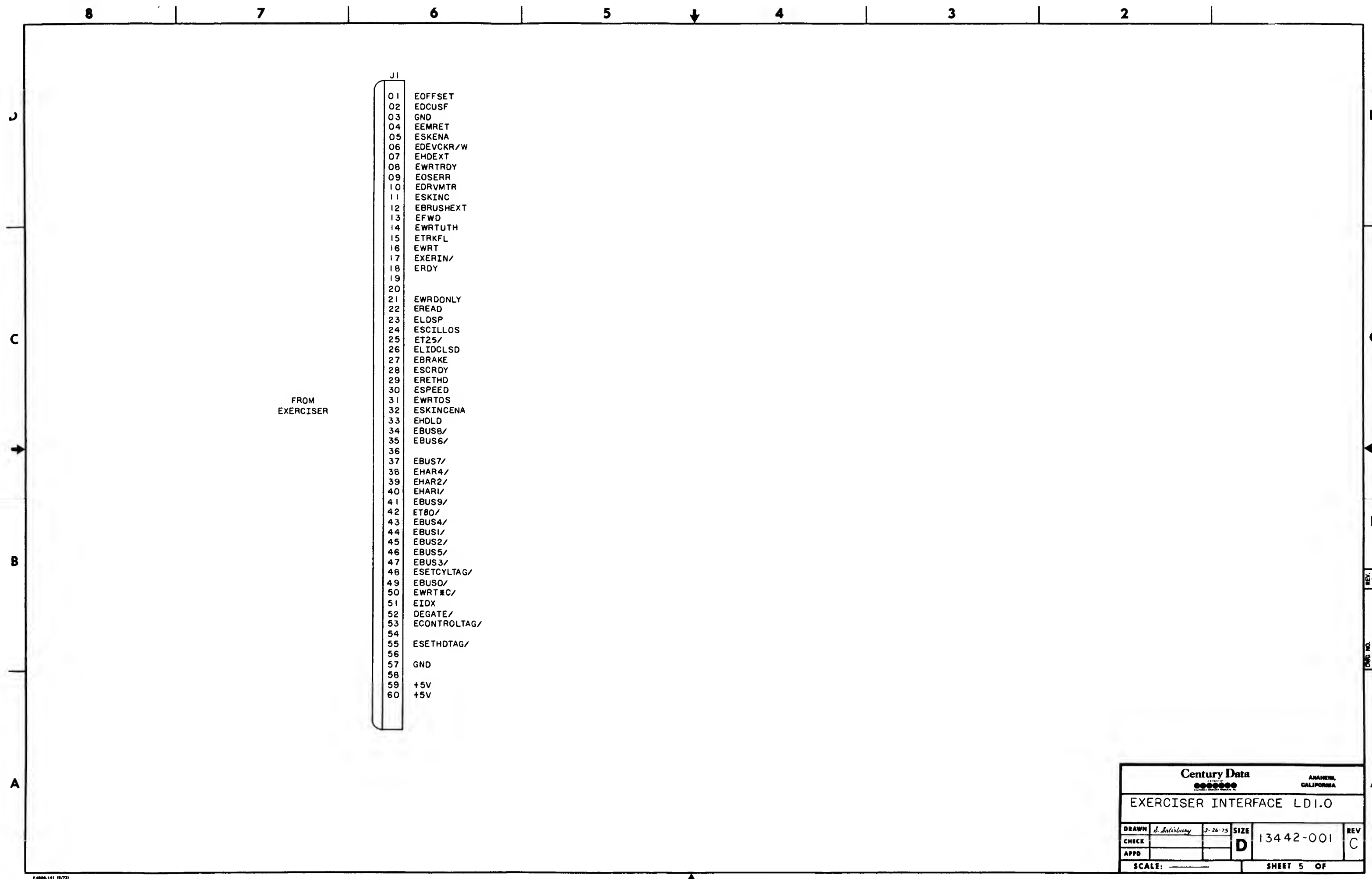


LOGIC DIAGRAM INDEX AND REVISION CONTROL		
SH	DIAGRAM INDEX	REV
5	LD1.0 EXERCISER INTERFACE	C
6	LD1.1 INPUT INTERFACE AND DEGATE	T
7	LD1.2 INPUT INTERFACE	E
8	LD1.3 BUS / TAG DECODE	G
9	LD2.0 SWITCHES AND INTERLOCKS	T
10	LD2.1 SEQUENCE OSCILLATOR COUNTER	G
11	LD2.2 PACK ON AND SEQUENCE LOGIC	V
12	LD2.3 SEQUENCE LOGIC AND RELAY DRIVERS	P
13	LD2.4 AC CONTROL AND DISTRIBUTION	P
14	LD2.5 SPEED DETECTION	V
15	LD3.0 SERVO CONTROL	T
16	LD3.1 SERVO CONTROL LOGIC	G
17	LD3.2 SERVO CONTROL AND READY	F
18	LD3.3 HOLD AND HOLDDY, DIFF. CLOCK AND UTH	T
19	LD3.4.0 SUBTRACTOR CONTROL AND ILLCYL LOGIC T50, T80	F
20	LD3.4.1 SUBTRACTOR CONTROL AND ILLCYL LOGIC T25	F
21	LD3.5.0 CAR AND SUBTRACTOR T50, T80	T
22	LD3.5.1 CAR AND SUBTRACTOR T25	T
23	LD3.6.0 DIFFERENCE COUNTER T50, T80	E
24	LD3.6.1 DIFFERENCE COUNTER T25	C
25	LD3.7 D/A CONVERTER AND AMPLIFIER DRIVER	C
26	LD3.8 POWER AMP AND LINEAR MOTOR	F
27	LD4.0 SERVO HEAD PREAMP AND POWER ON/	T
28	LD4.1 PLO T25, T50	T
29	LD4.1.1 PLO T80	T
30	LD4.2 POSITION, UTH #HOLD, AGC, 6APCLK	T
31	LD5.0 READY INDICATOR AND READ ONLY	T
32	LD5.1 INDEX AND SECTOR	C
33	LD6.0 R/W MATRIX T25, T50	T
34	LD6.0.1 R/W MATRIX T80	K
34.1	LD6.0.2 R/W MATRIX POWER T25, T50	S
34.2	LD6.0.3 R/W MATRIX POWER T80	S
35	LD6.1 READ AMP, C.O.D AND HEADS UNSAFE T25, T50	E
36	LD6.1.1 READ AMP, C.O.D AND HEADS UNSAFE T80	K
37	LD6.2 DATA SEPARATOR T25, T50	P
38	LD6.2.1 DATA SEPARATOR T80	U
39	LD6.3 DATA SEPARATOR DATA AND CLOCKS T25, T50	U
39.1	LD6.3.1 DATA AND CLOCKS T80	U
40	LD6.4 DATA SEPARATOR FAST LOCK AND ADMK CONTROL T25, T50	F
41	LD6.4.1 ADMK CONTROL AND PHASE SYNC T80	U
42	LD6.5 DATA SEPARATOR - WRITE ENCODE T25, T50	F
43	LD6.5.1 WRITE ENCODE T80	U
43.1	LD6.5.2 WRITE ENCODE T80	U
43.2	LD6.5.3 WRITE ENCODE T80	U
44	LD7.0 ERROR DETECTION	T
45	LD7.1 DCUSF EMERGENCY RETRACT AND MODULE IN INTERLOCK	T
46	LD7.2 SEEK INCOMPLETE	T
47	LD8.0 ATTENTION AND EOC	C
48	LD8.1 OUTPUT INTERFACE	P
49	LD9.0.1 POWER SUPPLY TRIDENT	V
50	LD9.1 POWER SUPPLY 5V REGULATOR	V

REVISIONS			
REV	ZONE	DESCRIPTION	APPROVAL & DATE
E		REVISED AND REDRAWN, EXTENSIVE CHANGES	6.9.82 J. P. [Signature] 12/17/75
F		REVISED AND REDRAWN, EXTENSIVE CHANGES, SEE REV E FOR WAS CONDITION	2.4.82 J. P. [Signature] 11-30-74
FI		PRODUCTION RELEASE PER ECO 11552	2.1.82 J. P. [Signature] 11-30-74
G		REVISED PER ECO 12512	12/17/75 J. P. [Signature] 12/17/75
H		REVISED PER ECO 12514	12/17/75 J. P. [Signature] 12/17/75
J		REV. PER ECO 12905	12/17/75 J. P. [Signature] 12/17/75
K		REV PER ECO 12959	12/17/75 J. P. [Signature] 12/17/75
L		CHGD SIG NAME AT 3A/B-B40 ON SH 31 WAS; DCUSF/ (LD1.3) PER ECO 13233	10-3-77 J. P. [Signature] 10-3-77
M		SHT 4, LOGIC III, PNT-25, T50, T80 WAS 12348-001 PER ECO 13442	11-30-77 J. P. [Signature] 11-30-77
N		REVISED PER ECO 13545	12-1-77 J. P. [Signature] 12-1-77
P		REVISED PER ECO 13737	12-1-77 J. P. [Signature] 12-1-77
R		REVISED PER ECO 14541	12-1-77 J. P. [Signature] 12-1-77
S		REVISED PER ECO 14449	12-1-77 J. P. [Signature] 12-1-77
T		REVISED PER ECO 14432	12-1-77 J. P. [Signature] 12-1-77
U		REVISED PER ECO 14707	12-1-77 J. P. [Signature] 12-1-77
V		REVISED PER ECO 13442-V	12-1-77 J. P. [Signature] 12-1-77

MODEL NO. FIRST USE T50		NEXT ASSY FIRST USE	
UNLESS OTHERWISE SPECIFIED		DRAWN 6. DREWRY 11-25-75	
DIMENSIONS ARE IN INCHES		CHECK	
TOLERANCES ON		APPD	
DECIMALS ANGLES		APPD	
XX ± .001 ± .001		FINISH	
XXX ± .001 ± .001		MATERIAL	
		SCALE: NONE	
		SIZE D	
		DWG NO. 13442-001	
		REV V	
SURFACE ROUGHNESS PER MIL-STD-10		DO NOT SCALE THIS DRAWING	
		WEIGHT	
		SHEET 1 OF 50	

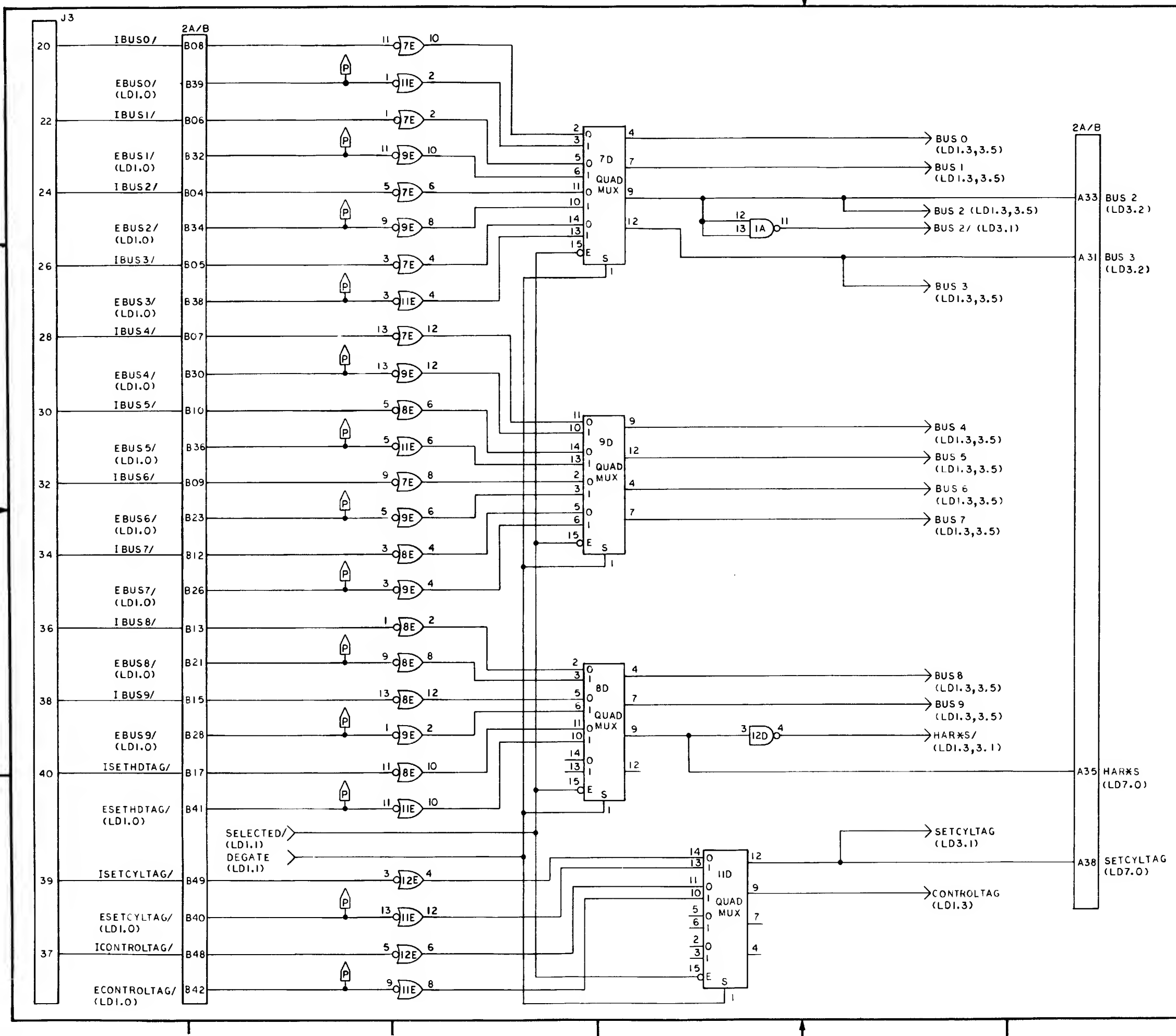




- J1
- 01 EOFFSET
 - 02 EDCUSF
 - 03 GND
 - 04 EEMRET
 - 05 ESKENA
 - 06 EDEVCKR/W
 - 07 EHDEXT
 - 08 EWRTRDY
 - 09 EOSERR
 - 10 EDRVMTR
 - 11 ESKINC
 - 12 EBRUSHEXT
 - 13 EFWD
 - 14 EWRTUTH
 - 15 ETRKFL
 - 16 EWRT
 - 17 EXERIN/
 - 18 ERDY
 - 19
 - 20
 - 21 EWRDONLY
 - 22 EREAD
 - 23 ELDSP
 - 24 ESCILLOS
 - 25 ET25/
 - 26 ELIDCLSD
 - 27 EBRAKE
 - 28 ESCRDY
 - 29 ERETHD
 - 30 ESPEED
 - 31 EWRTOS
 - 32 ESKINCENA
 - 33 EHOLD
 - 34 EBUS8/
 - 35 EBUS6/
 - 36
 - 37 EBUS7/
 - 38 EHAR4/
 - 39 EHAR2/
 - 40 EHARI/
 - 41 EBUS9/
 - 42 ET80/
 - 43 EBUS4/
 - 44 EBUS1/
 - 45 EBUS2/
 - 46 EBUS5/
 - 47 EBUS3/
 - 48 ESETCYLTAG/
 - 49 EBUS0/
 - 50 EWRT#C/
 - 51 EIDX
 - 52 DEGATE/
 - 53 ECONTROLTAG/
 - 54
 - 55 ESETHDTAG/
 - 56
 - 57 GND
 - 58
 - 59 +5V
 - 60 +5V

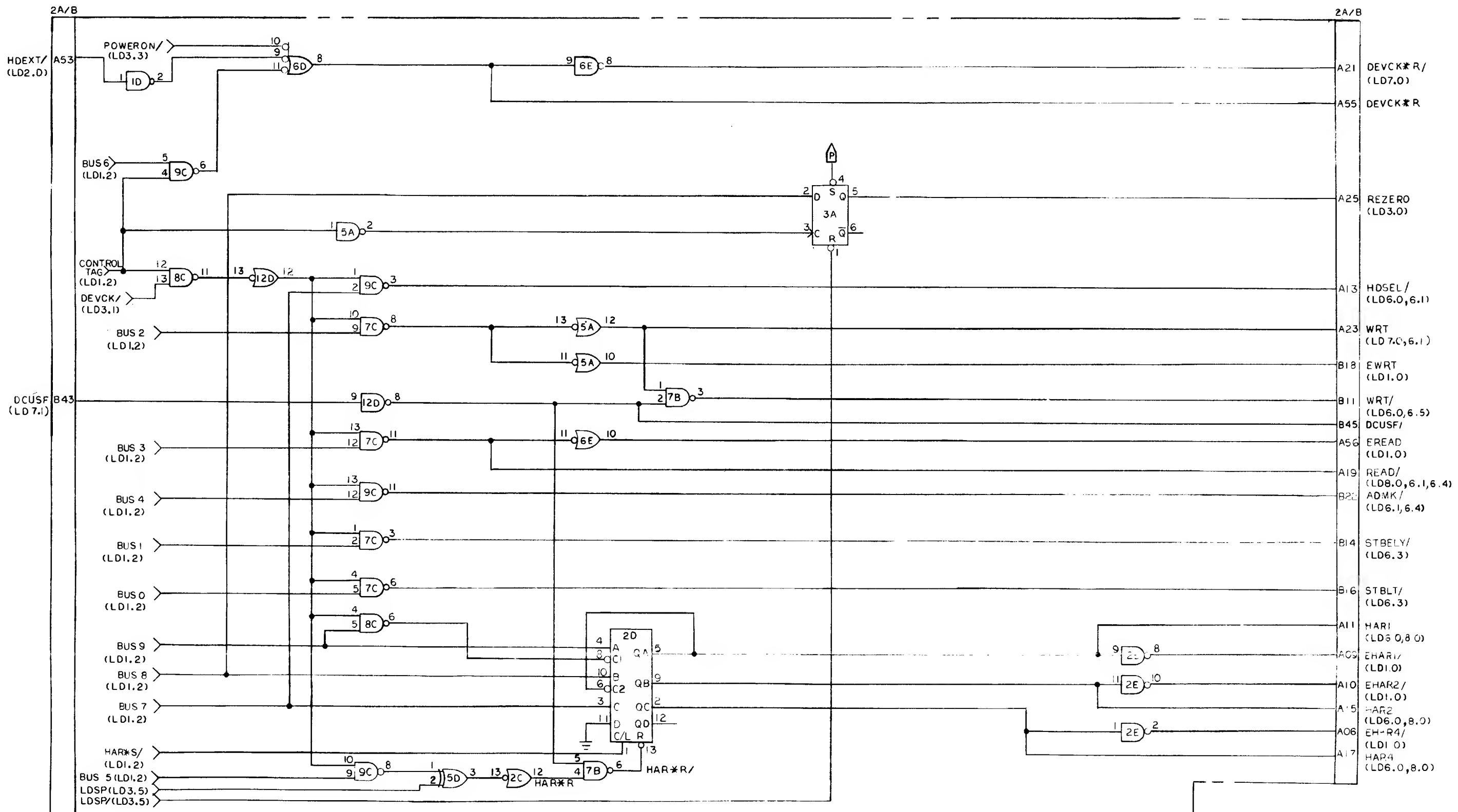
FROM
EXERCISER

Century Data		ANAHEIM, CALIFORNIA	
EXERCISER INTERFACE LD1.0			
DRAWN	J. Salisbury	3-26-75	SIZE
CHECK			D
APPD			
SCALE:		SHEET 5 OF	

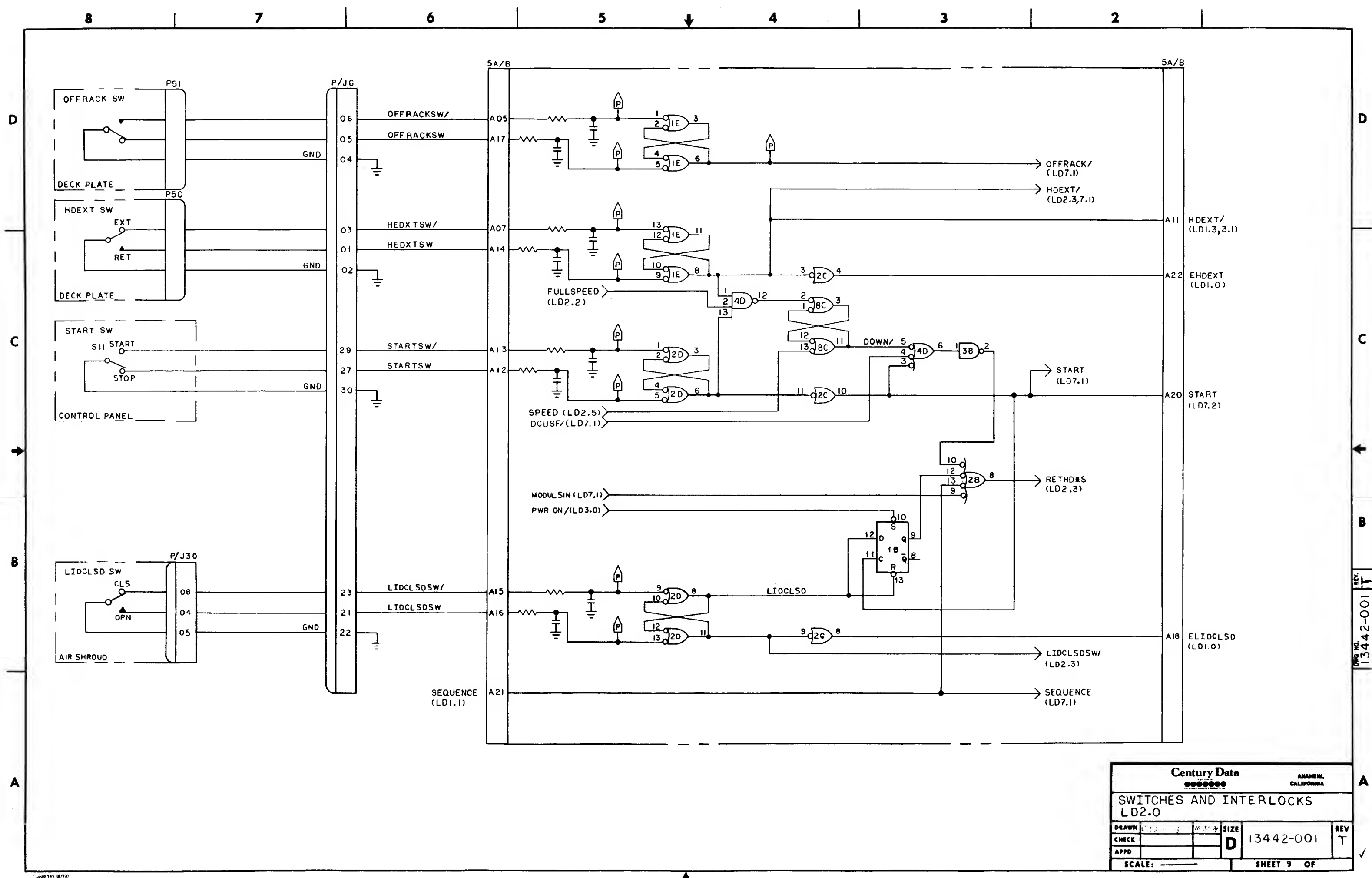


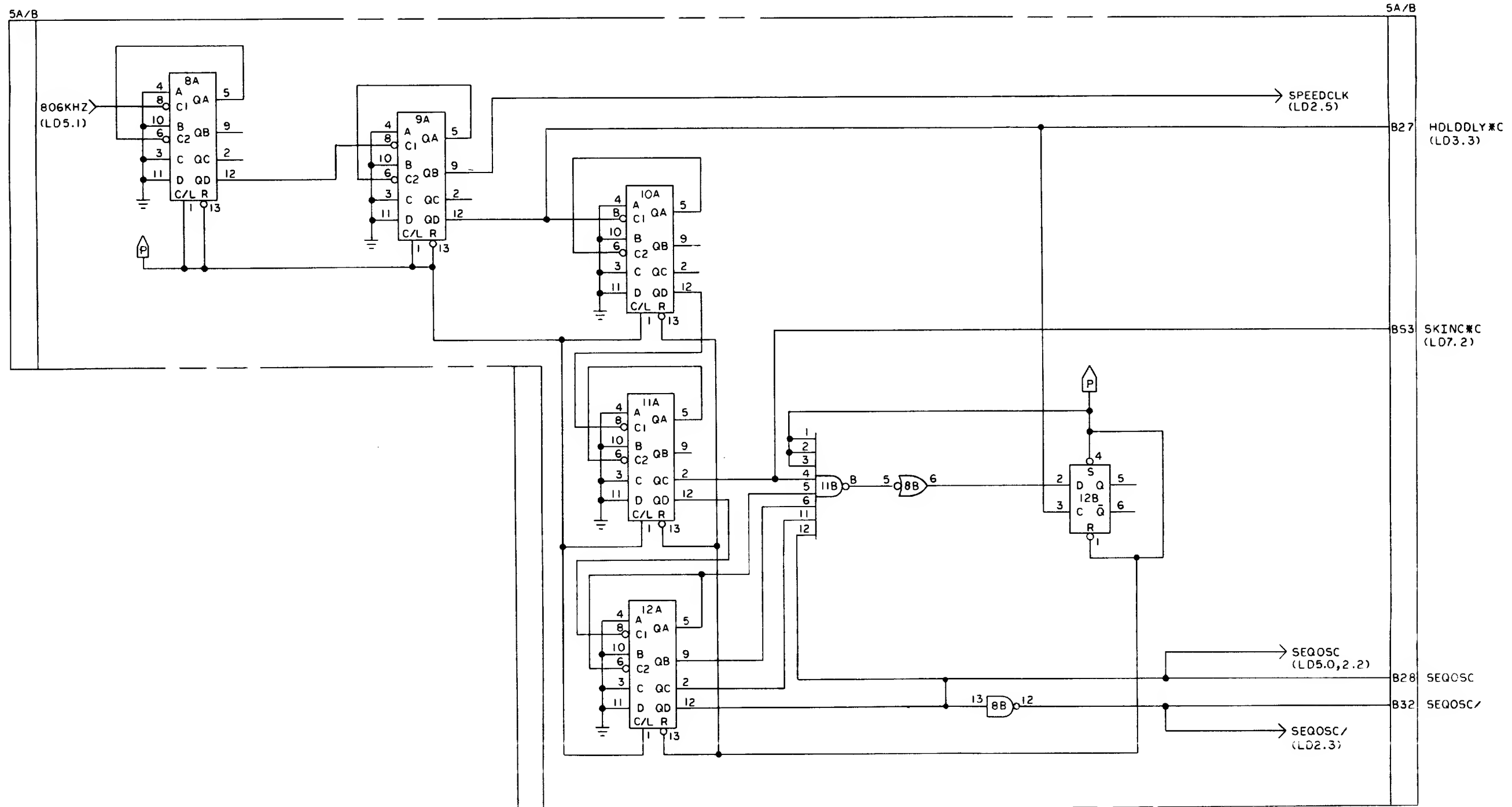
CENTURY DATA SYSTEMS, INC. ANAHEIM, CALIFORNIA			
INPUT INTERFACE LD1.2			
DRAWN	CHECK	SIZE	REV
			13442-001 E
APPD	SCALE:		SHEET 7 OF

13442-001 E



CENTURY DATA SYSTEMS, INC. ANAHEIM, CALIFORNIA			
BUS/TAG DECODE LD 1.3			
DRAWN	G. Dewey	2-16-75	SIZE
CHECK			
APPD			
SCALE:			13442-001
SHEET 8 OF			REV G





CENTURY DATA SYSTEMS, INC. ANAHEIM, CALIFORNIA					
SEQUENCE OSCILLATOR COUNTER (LD2.1)					
DRAWN	<i>J. Salisbury</i>	12-3-74	SIZE	D	13442-001
CHECK					REV G
APPD					
SCALE: _____				SHEET 10 OF _____	

REV G
13442-001

8

7

6

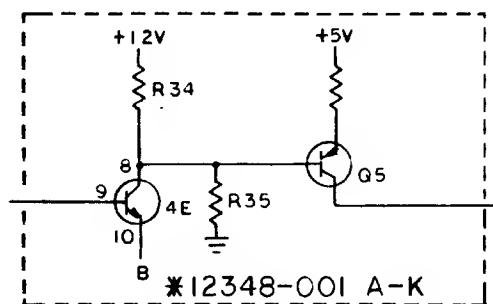
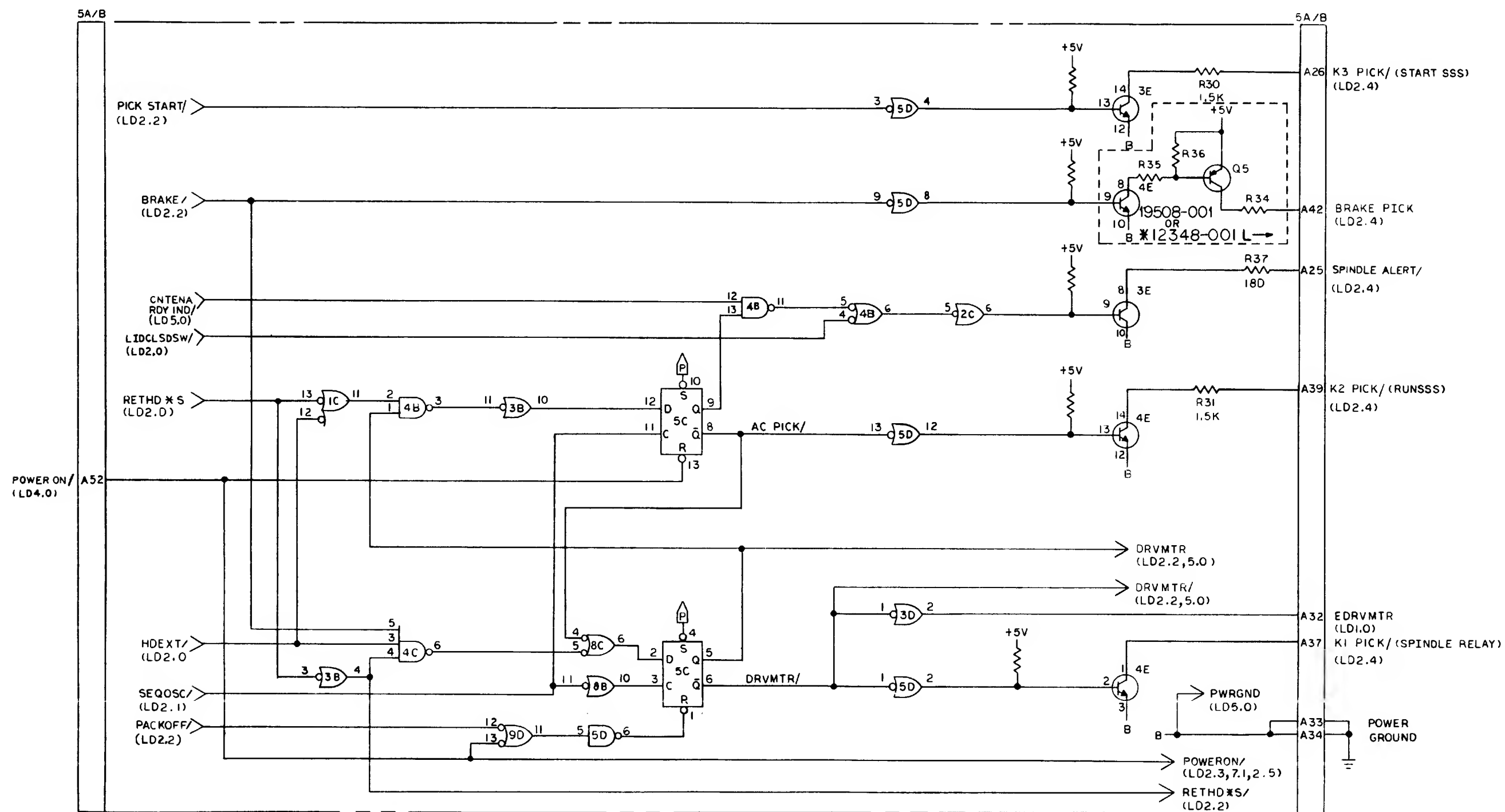
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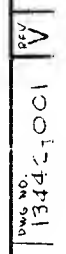
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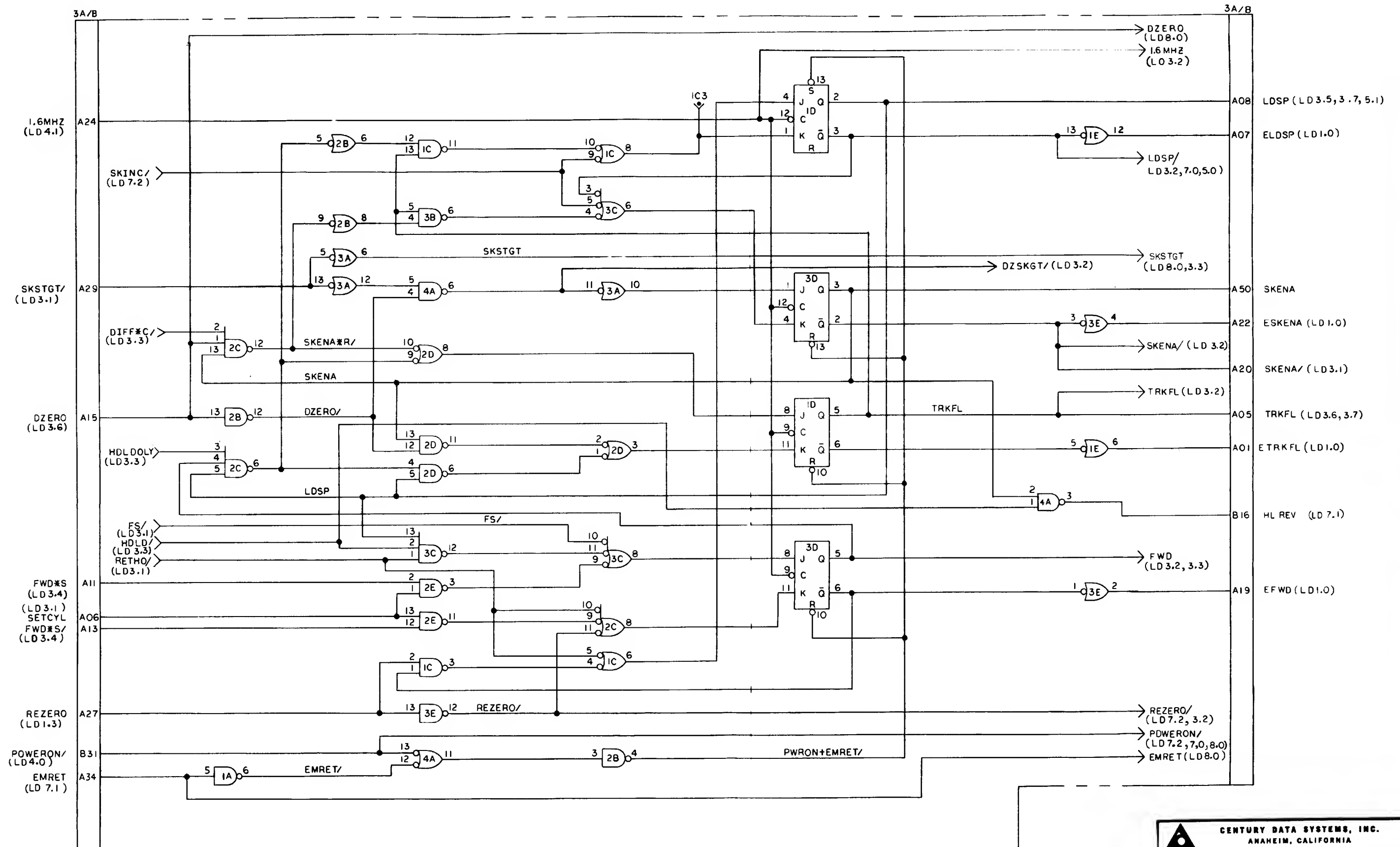
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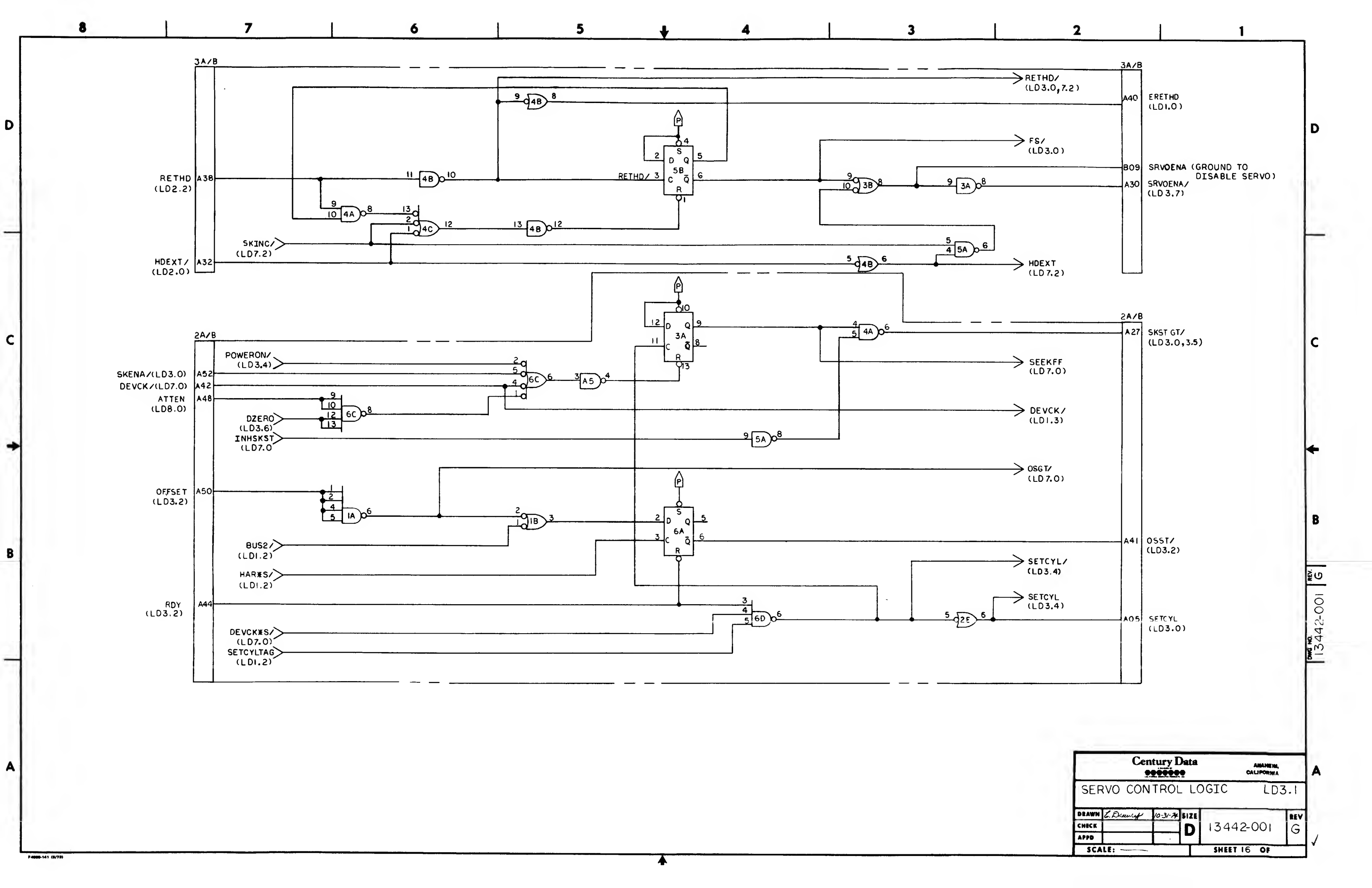


CENTURY DATA SYSTEMS, INC. ANAHEIM, CALIFORNIA			
SEQUENCE LOGIC AND RELAY DRIVERS (LD2.3)			
DRAWN	C. Dwyer	12-16-79	SIZE
CHECK			13442-001
APPD			REV P
SCALE:		SHEET 12 OF	





CENTURY DATA SYSTEMS, INC. ANAHEIM, CALIFORNIA			
SERVO CONTROL LD3.0			
DRAWN	J. Salisbury	SIZE	D
CHECK		13442-001	REV T
APPD		SCALE: _____ SHEET 15 OF _____	

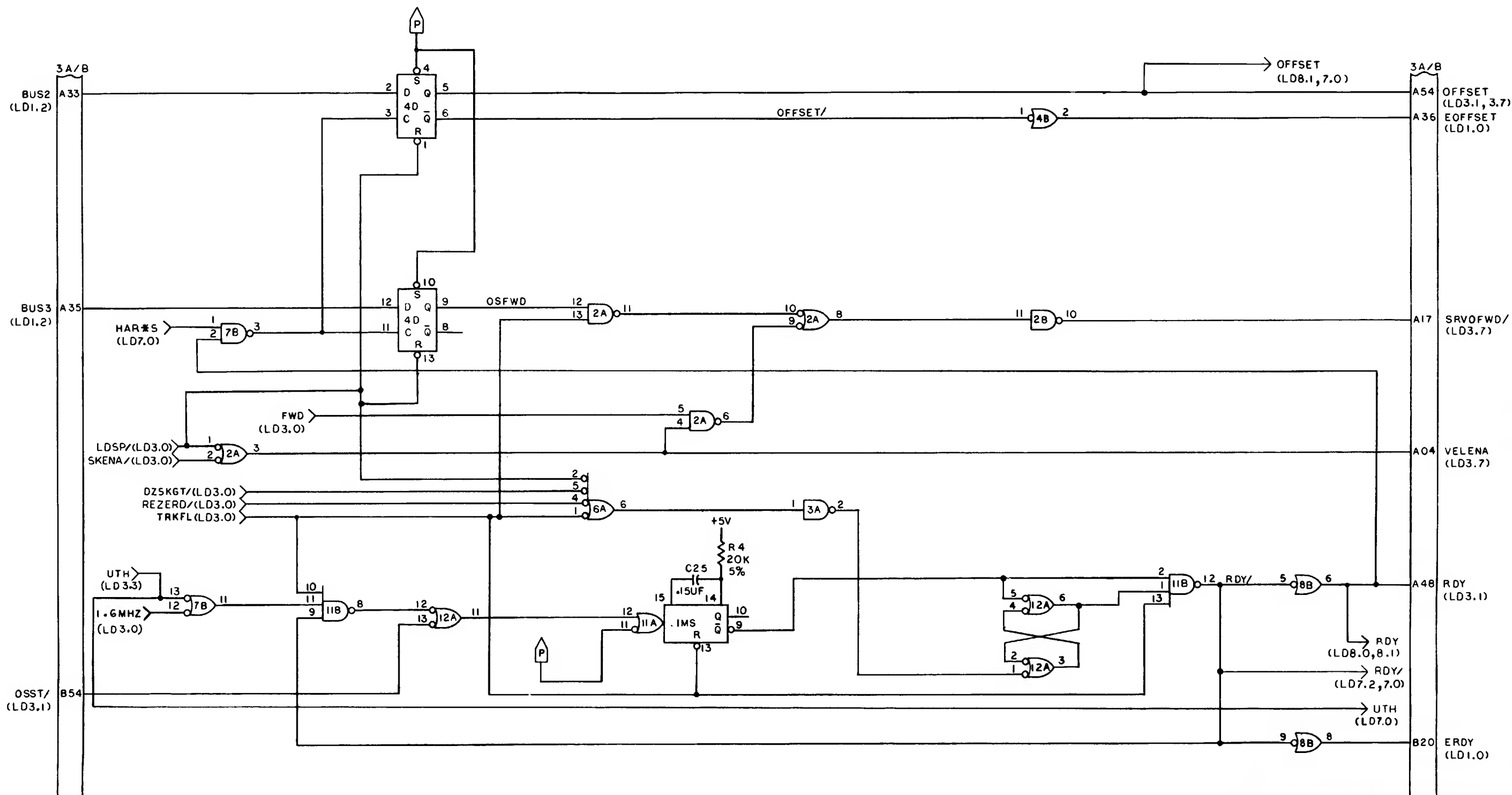


D

C

B

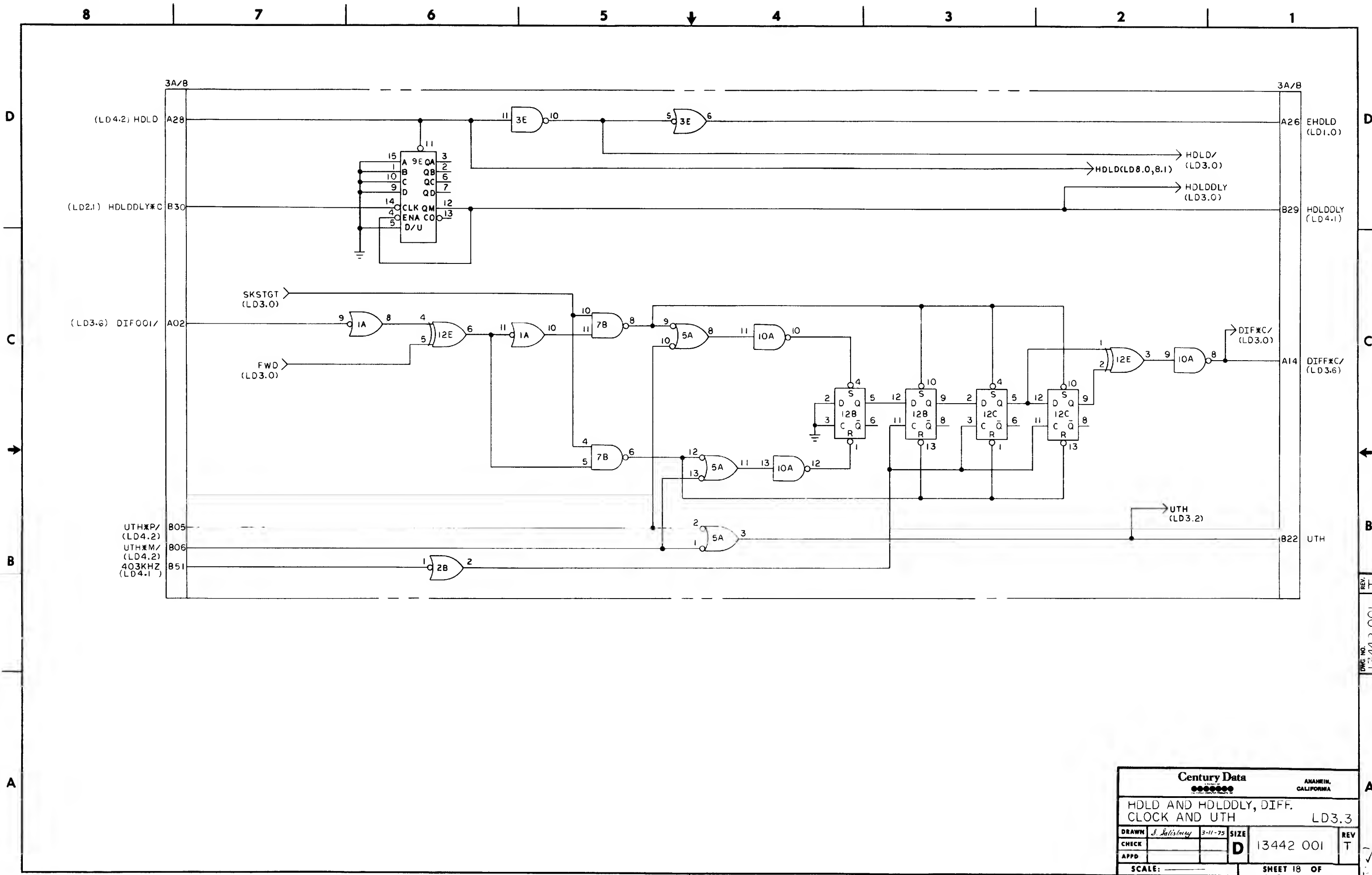
A



Century Data		ANAHEIM, CALIFORNIA	
SERVO CONTROL AND READY			
LD3.2			
DRAWN	C. Remy	10-22-74	SIZE
CHECK			D
APPD			13442-001
SCALE:		SHEET 17 OF	

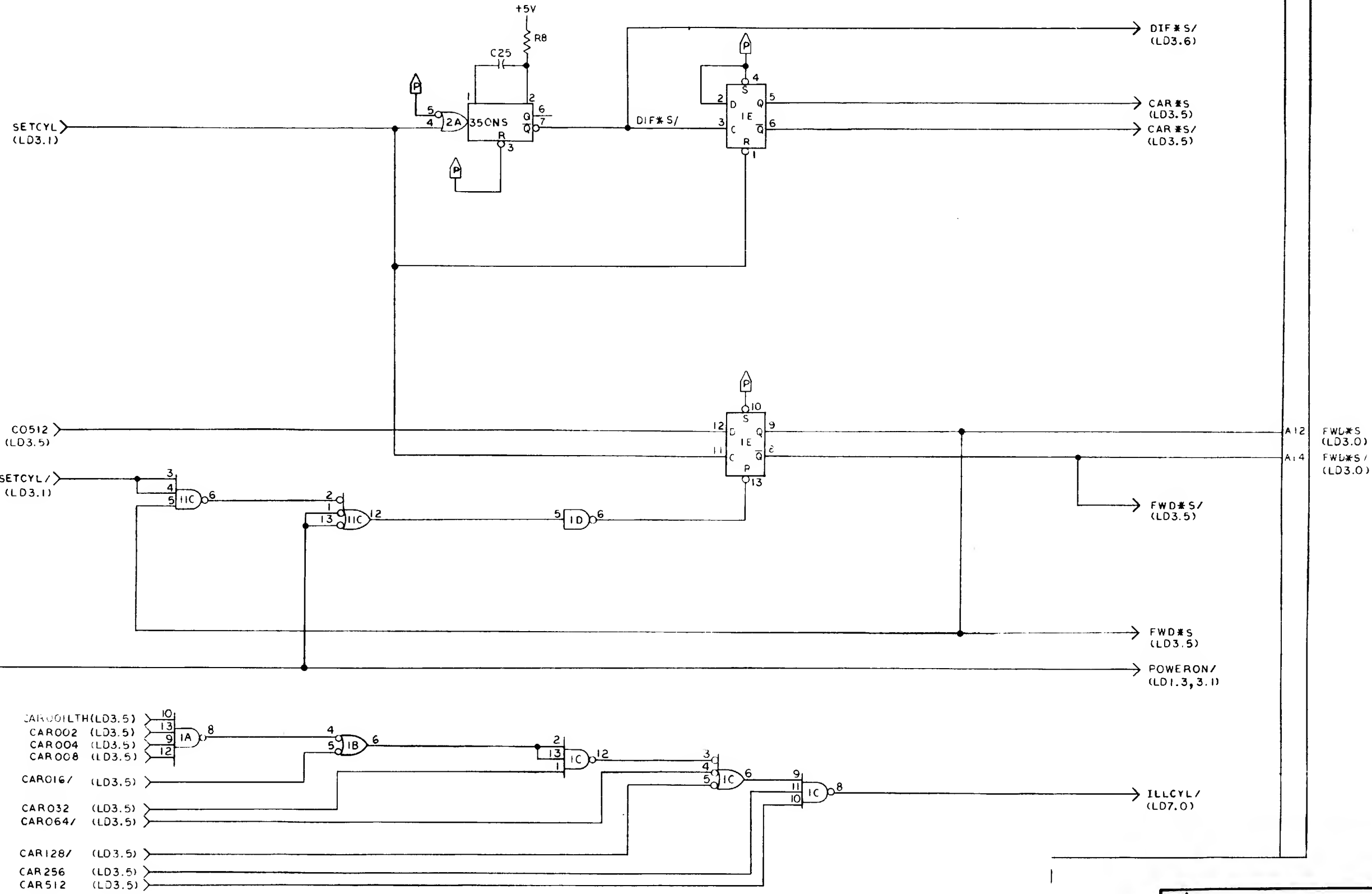
13442-001

REV F



2 A/B

2 A/B



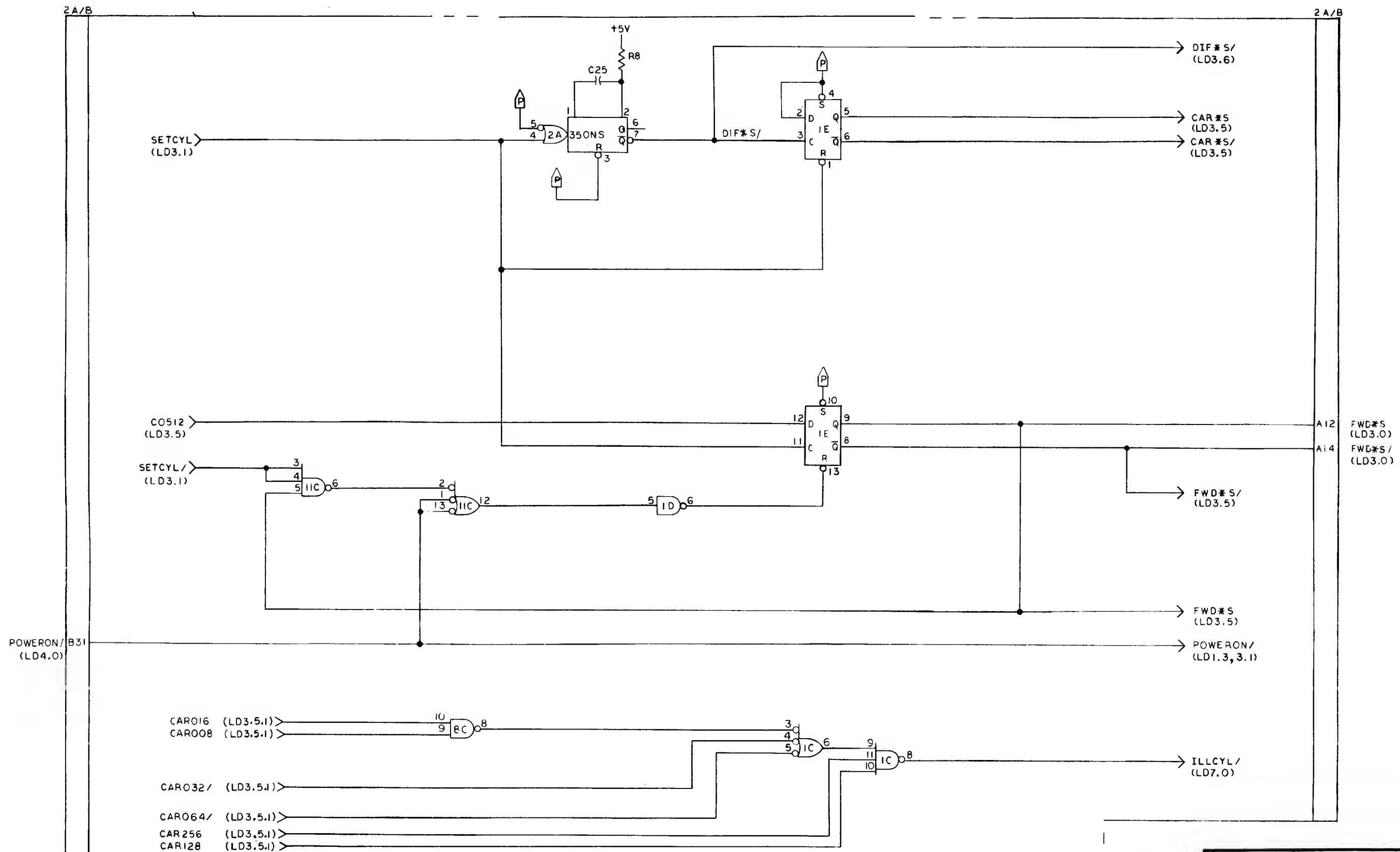
CENTURY DATA SYSTEMS, INC.
ANAHEIM, CALIFORNIA

SUBTRACTOR CONTROL AND
ILLCYL LOGIC-T50,T80 LD3.4.0

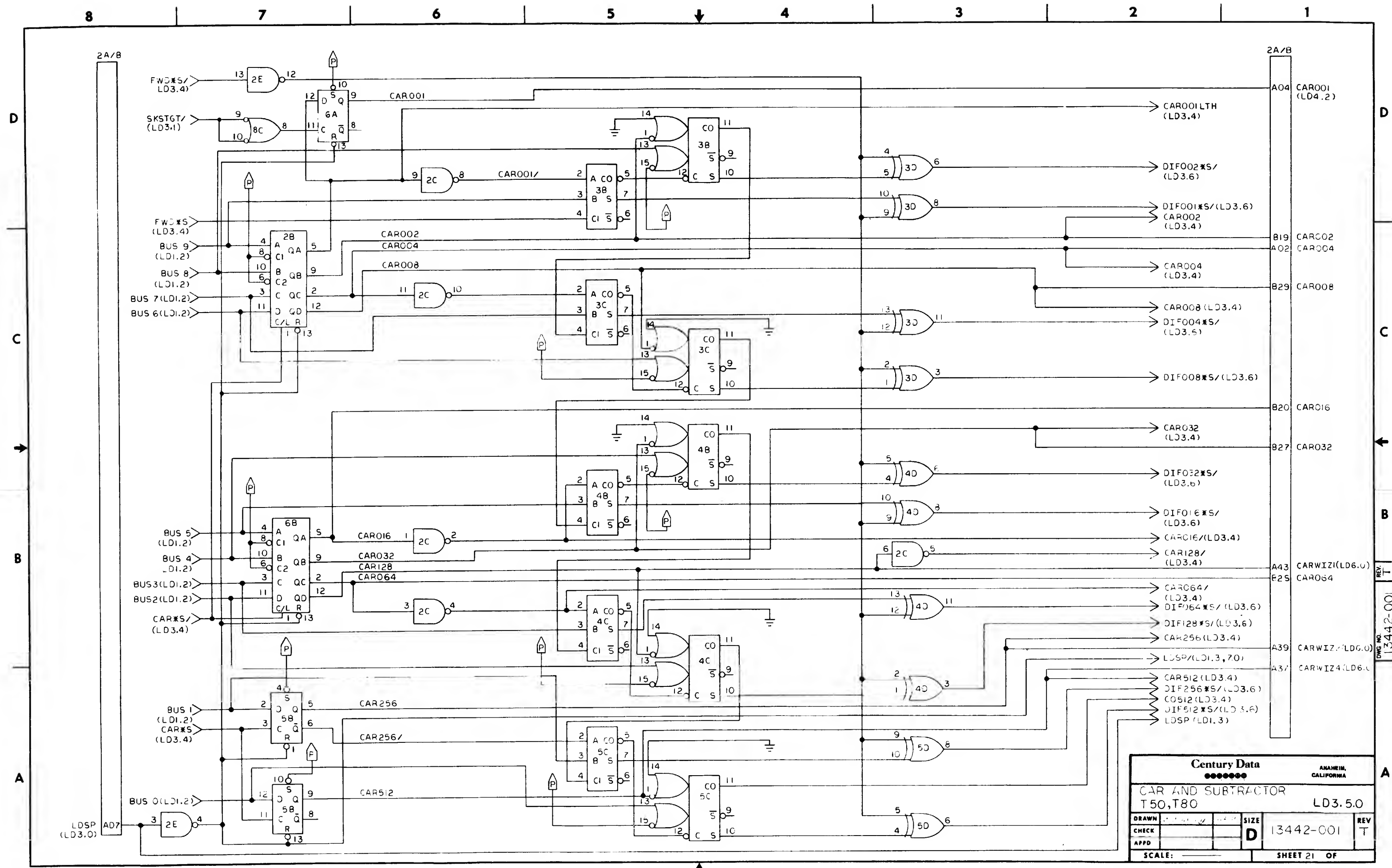
DRAWN	CHECK	APPD	SIZE	REV
			D	F

SCALE: _____

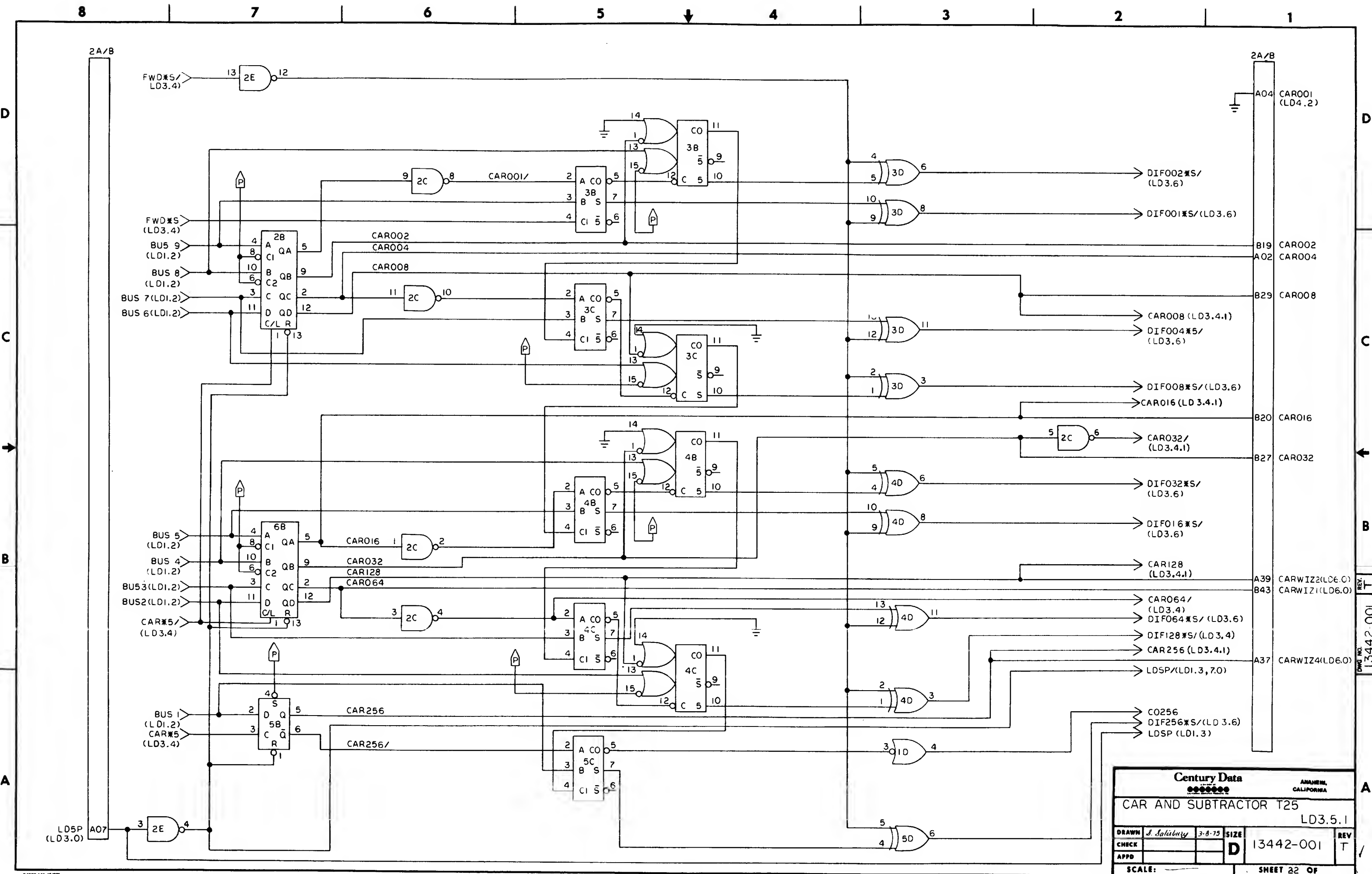
SHEET 19 OF _____



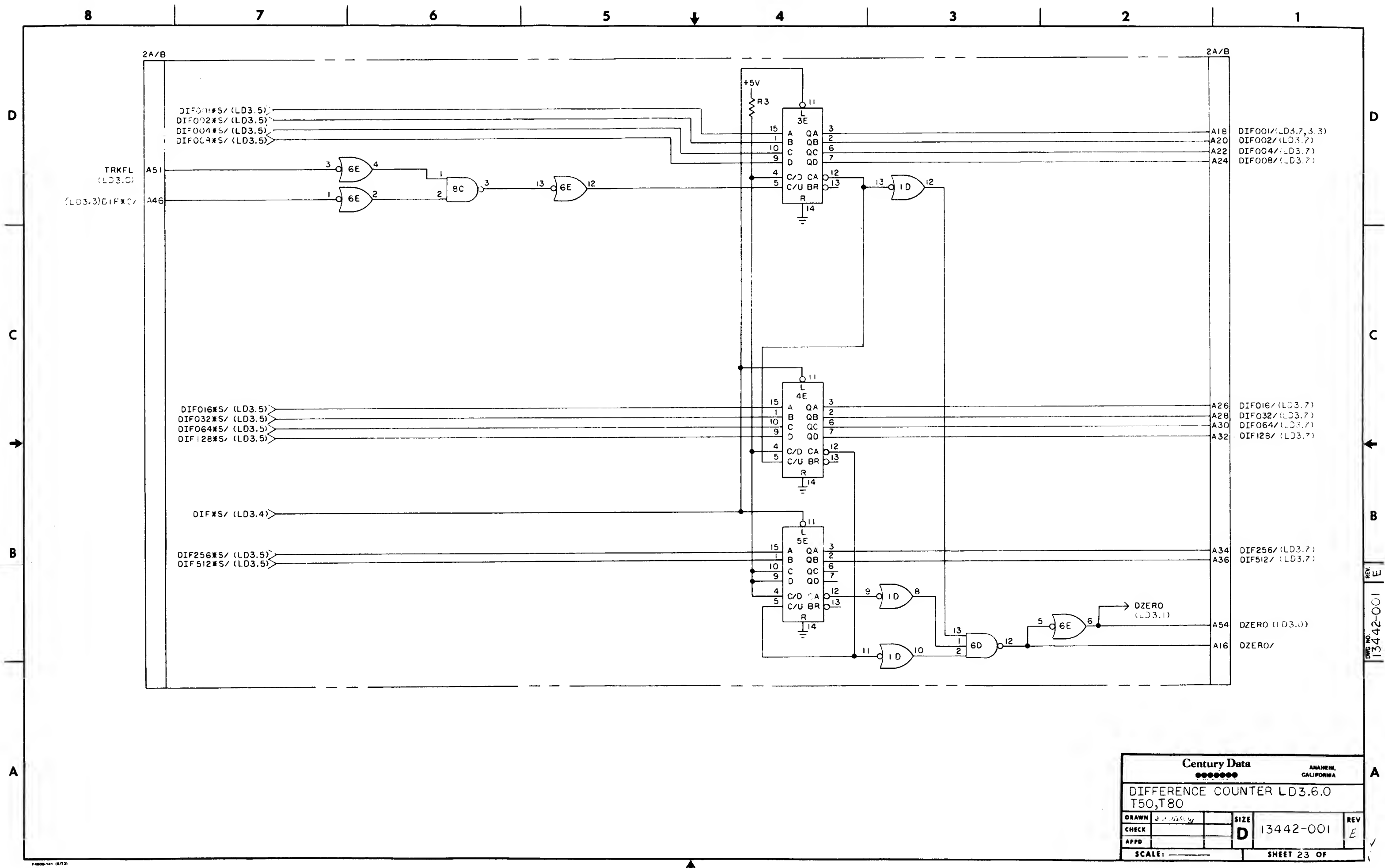
CENTURY DATA SYSTEMS, INC. ANAHEIM, CALIFORNIA			
SUBTRACTOR CONTROL AND ILLCYL LOGIC T25			
DRAWN <i>C. Dewey</i>		SIZE	REV F
CHECK		13442-001	REV F
APPD		SCALE:	SHEET 20 OF

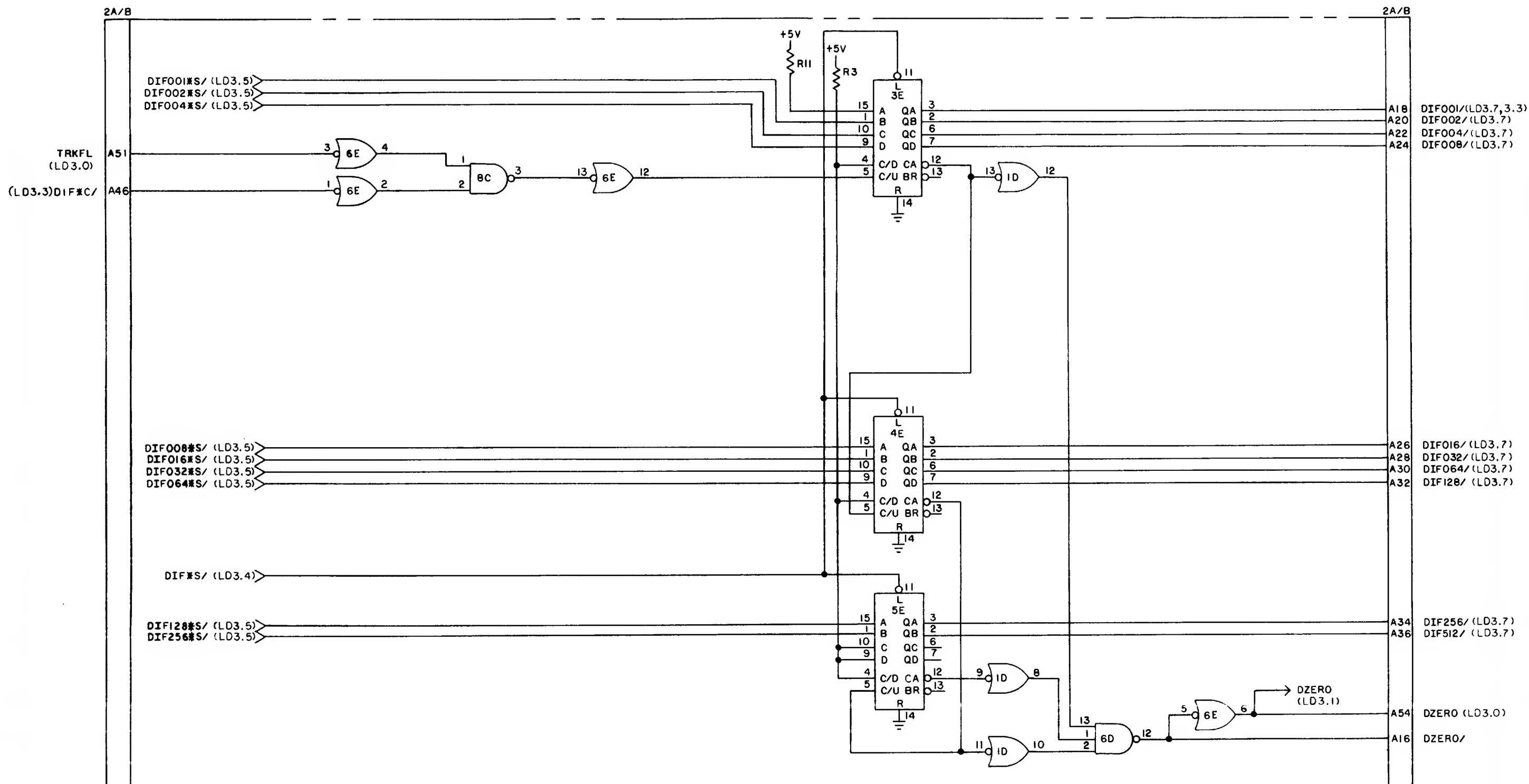


Century Data		ANAHEIM, CALIFORNIA	
CAR AND SUBTRACTOR			
T50, T80		LD3.5.0	
DRAWN	CHECK	SIZE	REV
APPD		D	T
SCALE:		SHEET 21 OF	



Century Data ANALYTICAL RECORDS ●●●●●●●●			ANAHEIM, CALIFORNIA		
CAR AND SUBTRACTOR T25					
LD3.5.1					
DRAWN	J. Salisbury	3-8-75	SIZE	13442-001	REV T
CHECK			D		
APPD					
SCALE: _____			SHEET 22 OF _____		





Century Data		ANAHEIM, CALIFORNIA	
DIFFERENCE COUNTER			
T25 (LD3.6.1)			
DRAWN	J. Antikarov	SIZE	D
CHECK		REV	C
APPD		13442-001	
SCALE: _____		SHEET 24 OF	

D

C

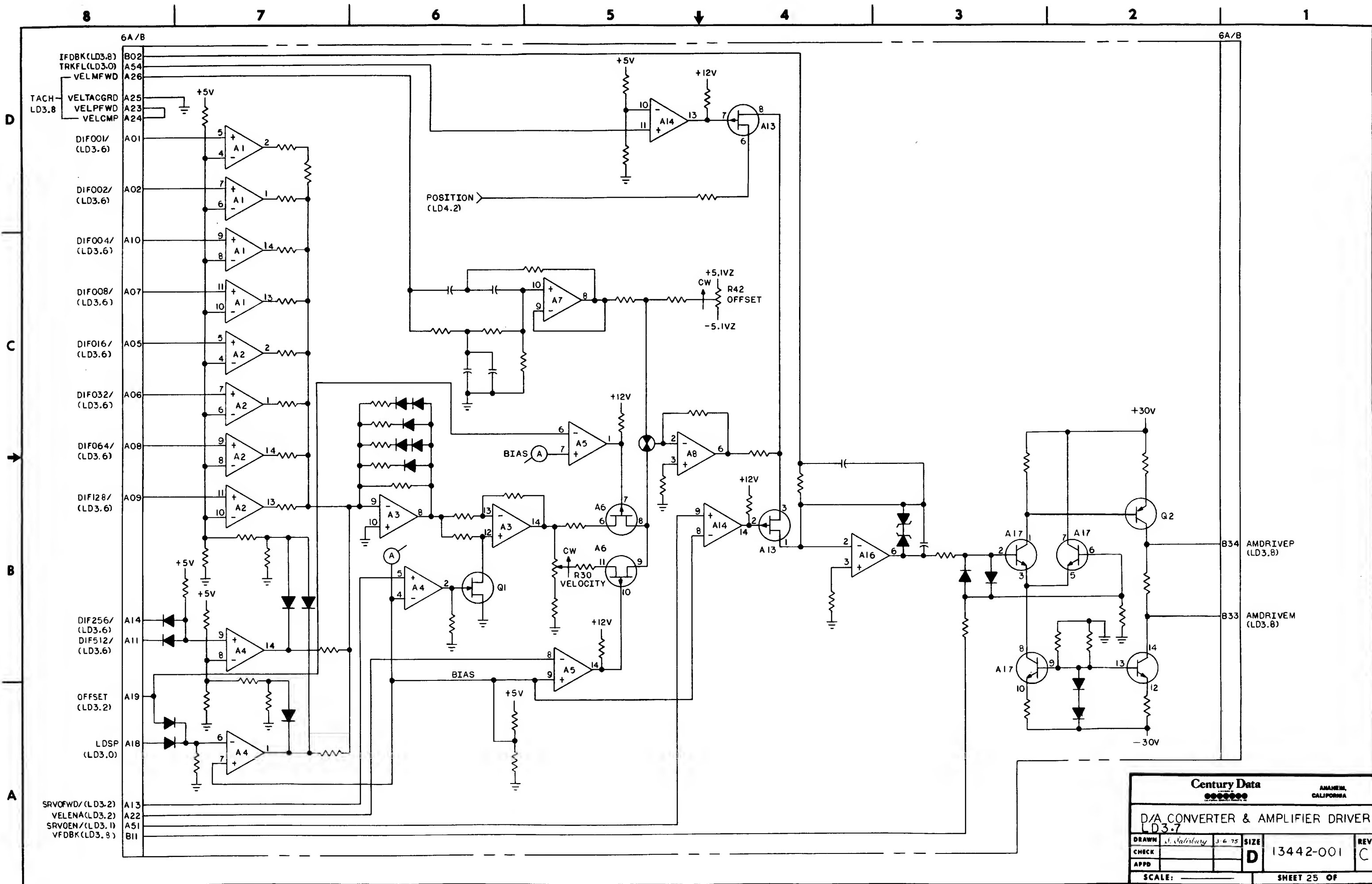
B

REV

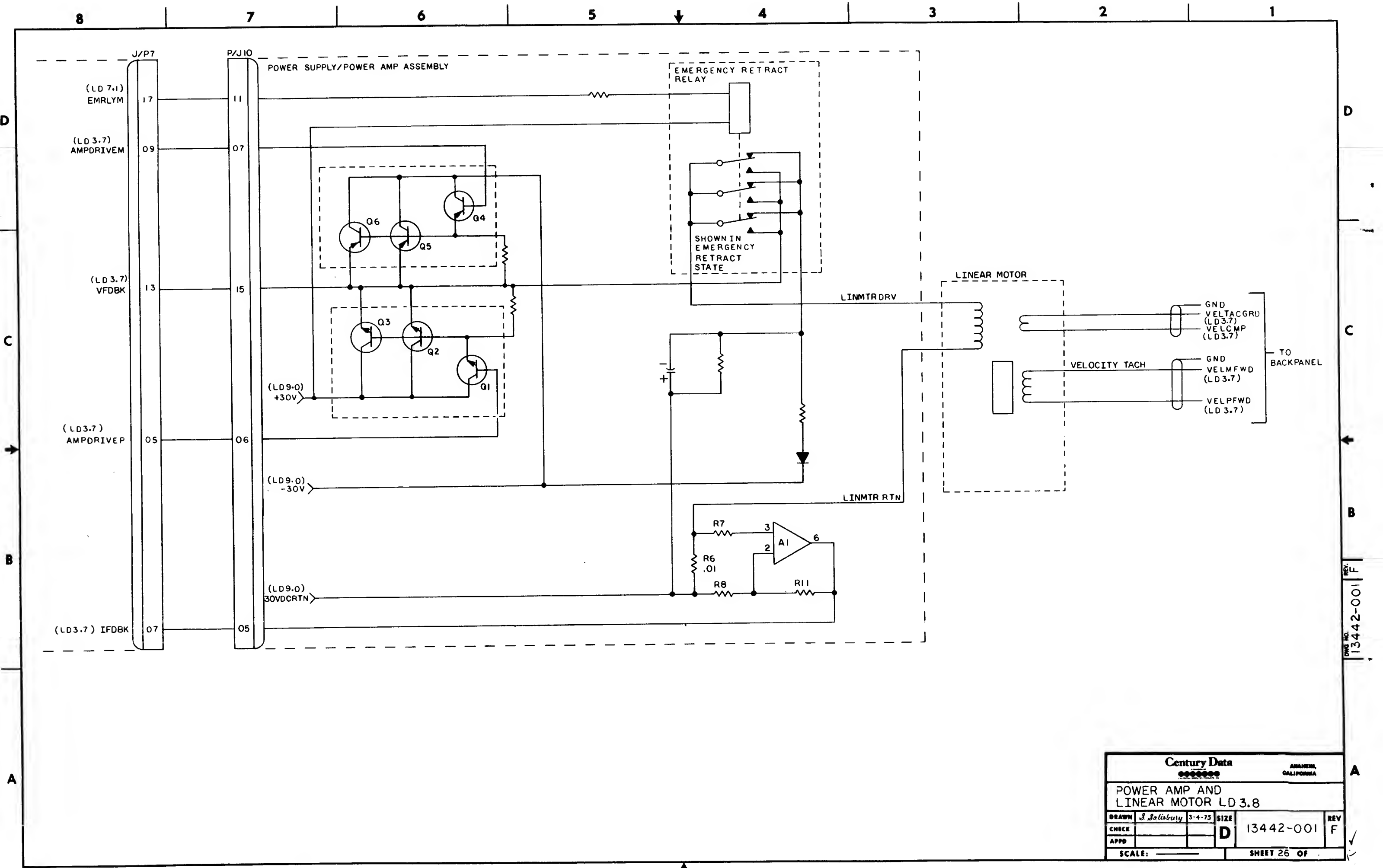
13442-001

C

A

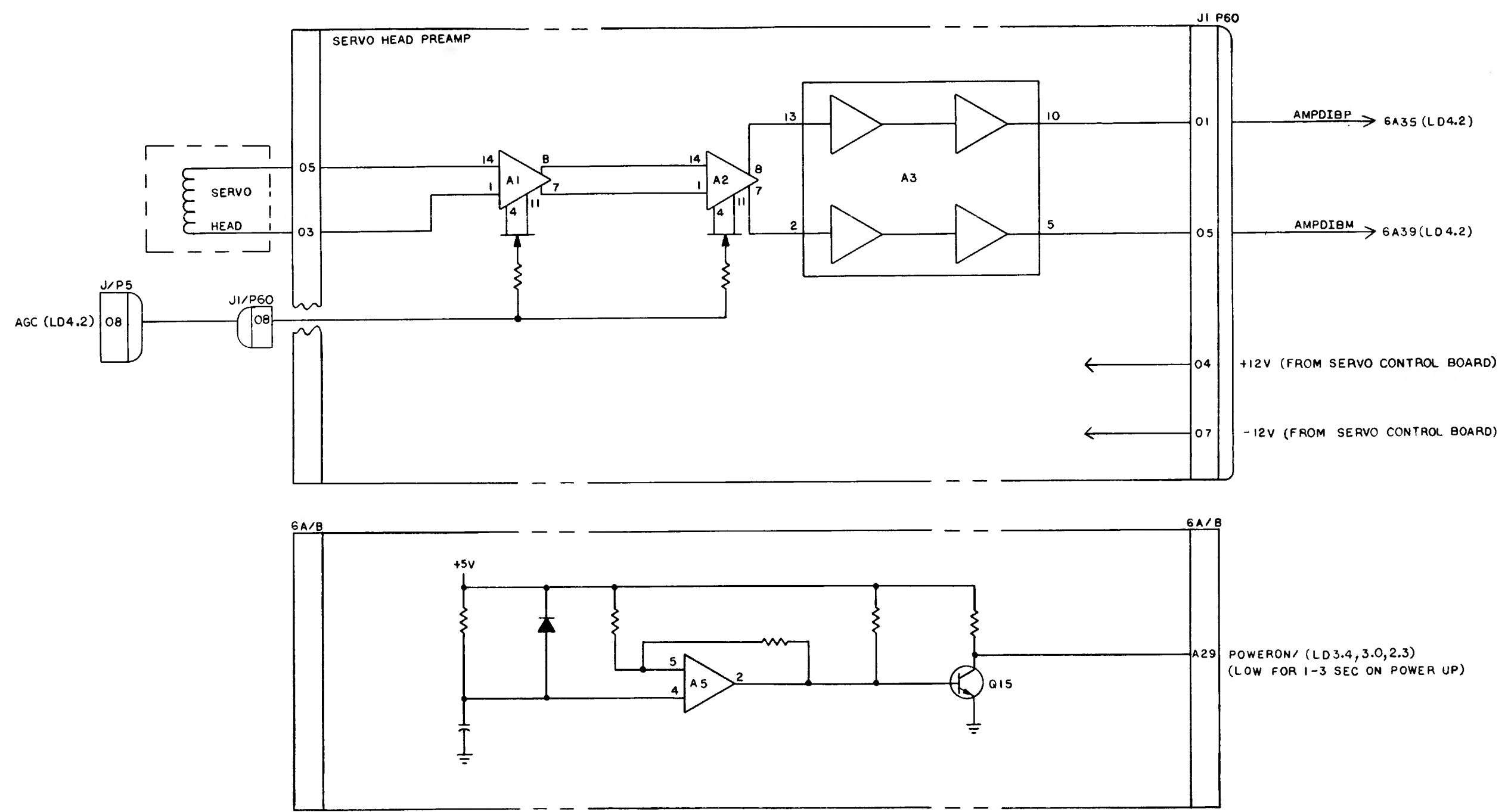


Century Data		ANAHEIM, CALIFORNIA	
D/A CONVERTER & AMPLIFIER DRIVER			
LD3.7			
DRAWN	J. Salisbury	SIZE	D
CHECK			13442-001
APPD			REV C
SCALE:		SHEET 25 OF	



8 7 6 5 4 3 2 1

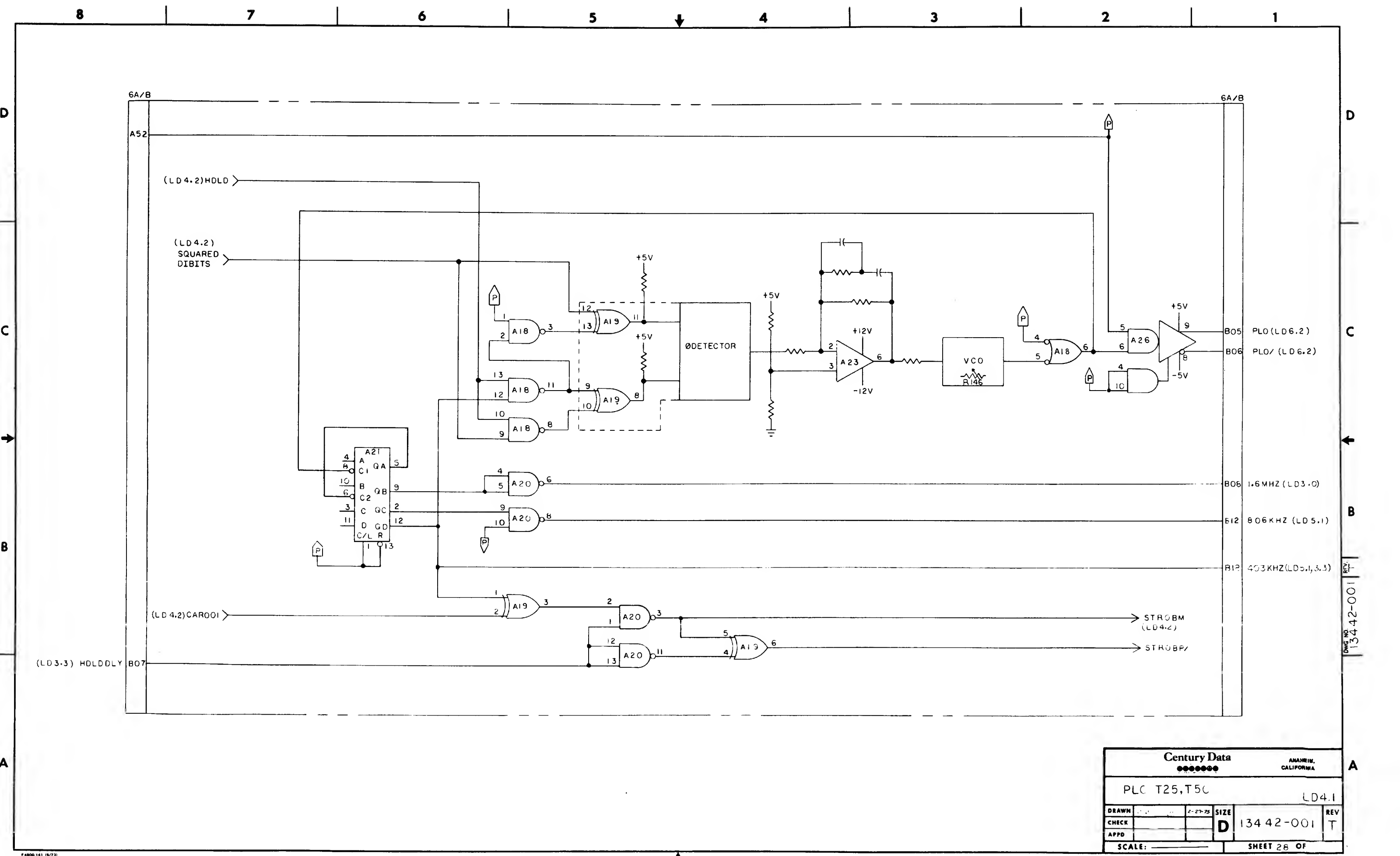
D
C
B
A



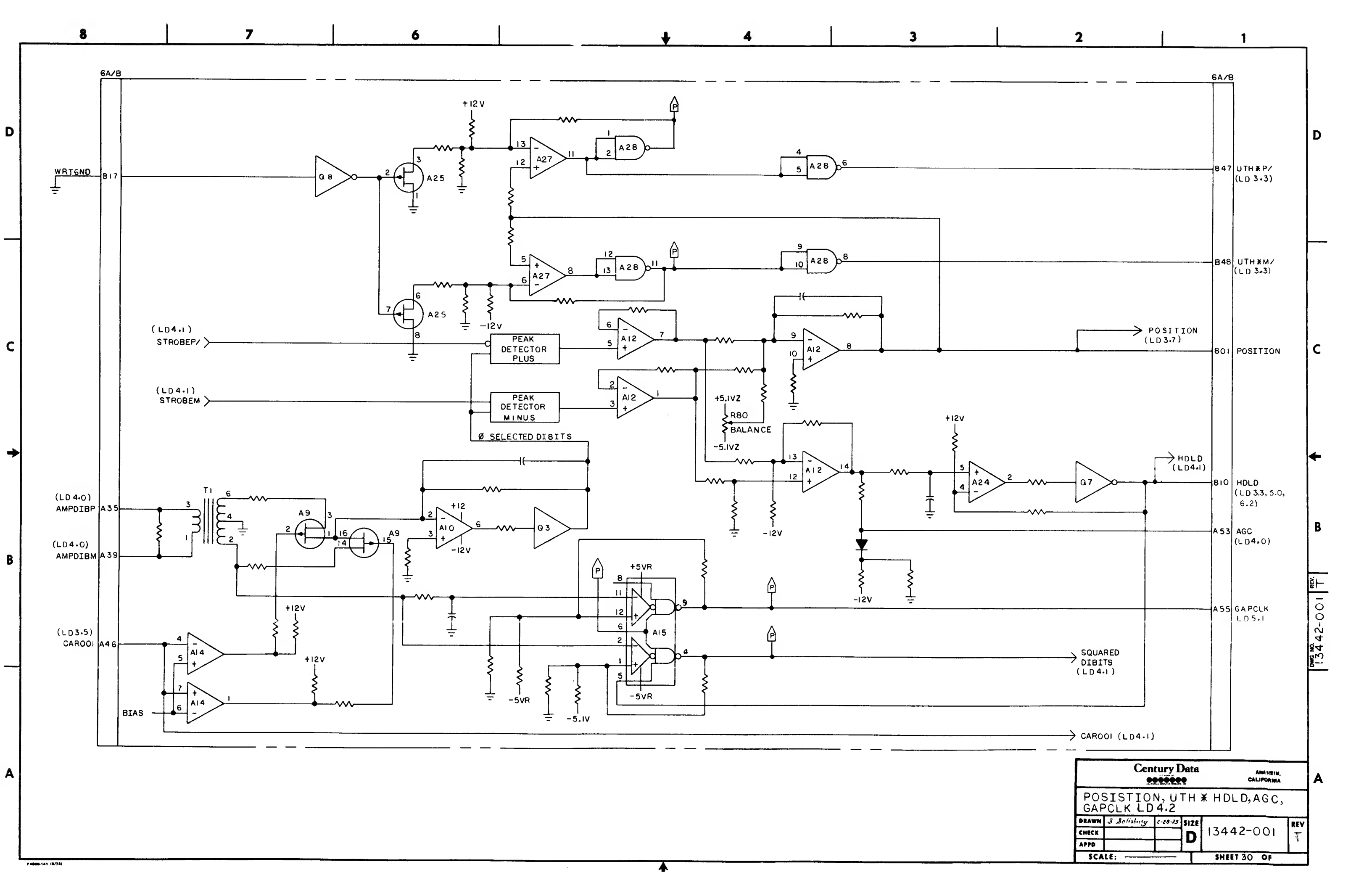
D
C
B
A

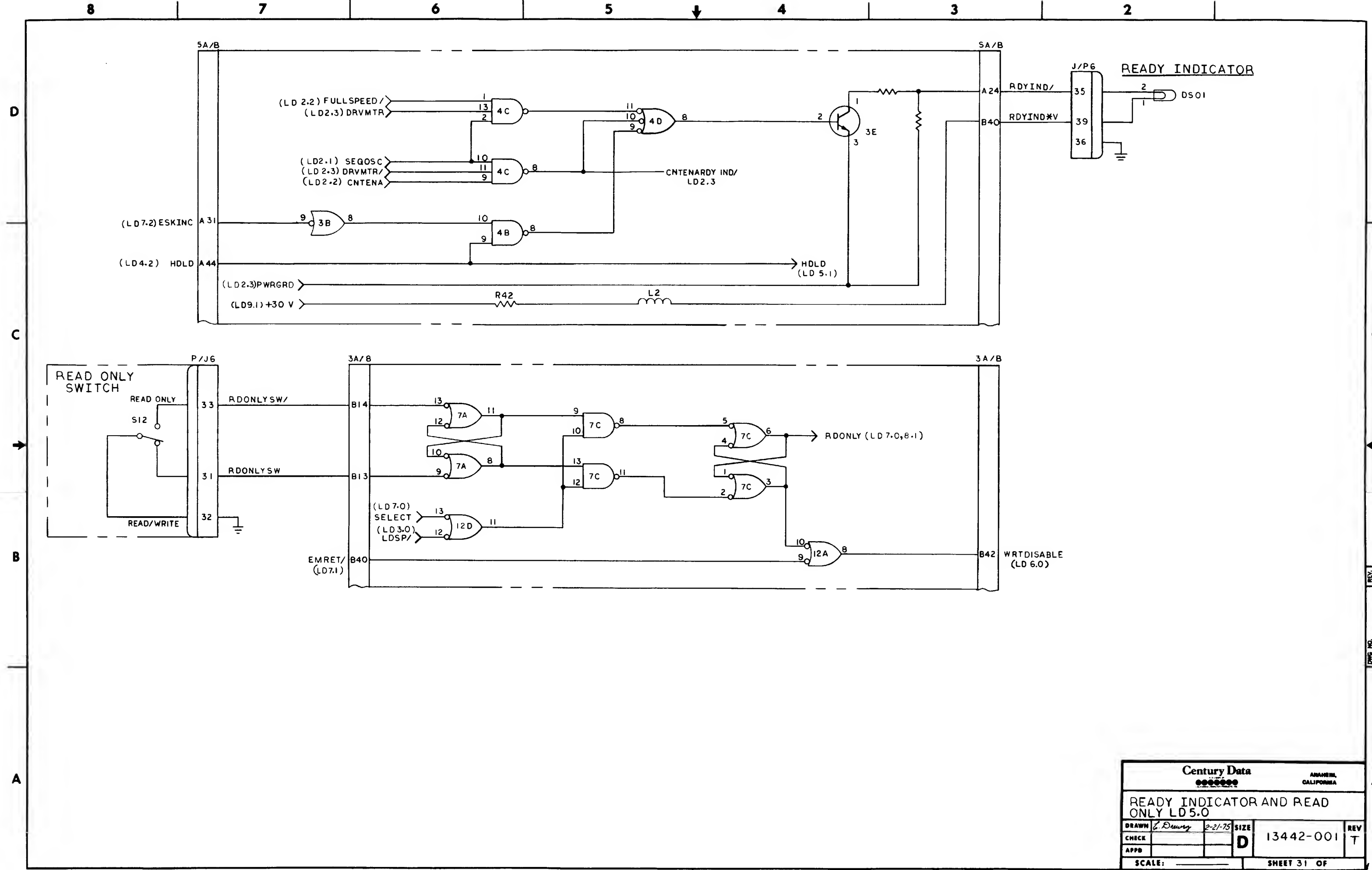
REV. T
13442-001
Dwg. No.


Century Data			ANAHEIM, CALIFORNIA	
SERVO HEAD PREAMP AND POWER ON/ LD 4.0				
DRAWN	S. Salisbury	3-10-75	SIZE	D 13442-001
CHECK				
APPD				
SCALE:				SHEET 27 OF



Century Data				
PLC T25, T5C				
LD4.1				
DRAWN	134 42-001	SIZE	D	REV
CHECK	134 42-001	SIZE	D	T
APPD	134 42-001	SIZE	D	T
SCALE:			SHEET 28 OF	





Century Data			ANAHEIM, CALIFORNIA		
					
READY INDICATOR AND READ ONLY LD 5.0					
DRAWN	G. Dewey	2-21-75	SIZE	13442-001	REV T
CHECK			D		
APPD					
SCALE: _____			SHEET 31 OF _____		

(LD 4.1) 806KHZ

5A/B

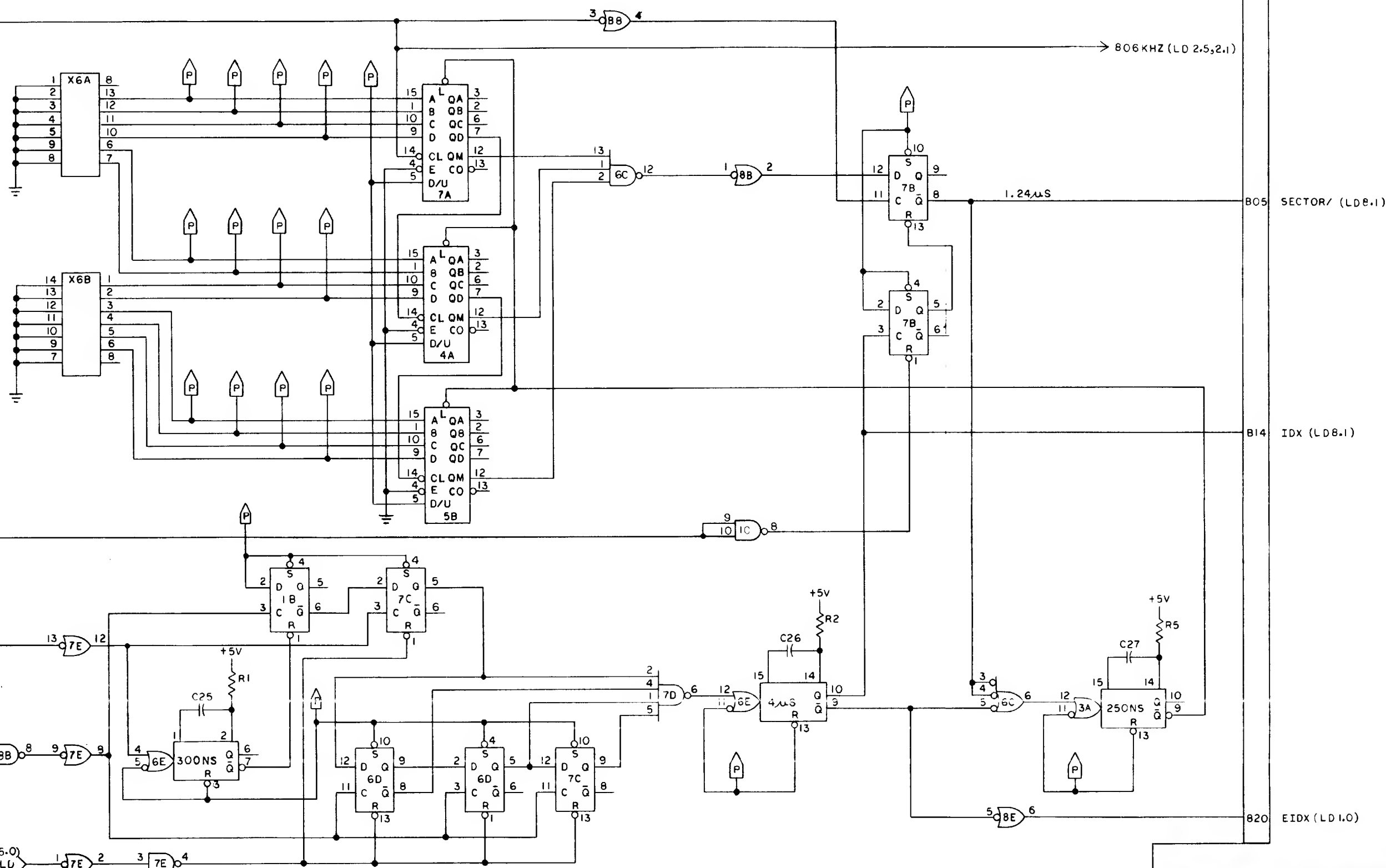
5A/B

(LD 3.0) LDSP

(LD 4.1) 403KHZ

(LD 4.2) GAPCLK

(LD 5.0) HDLU



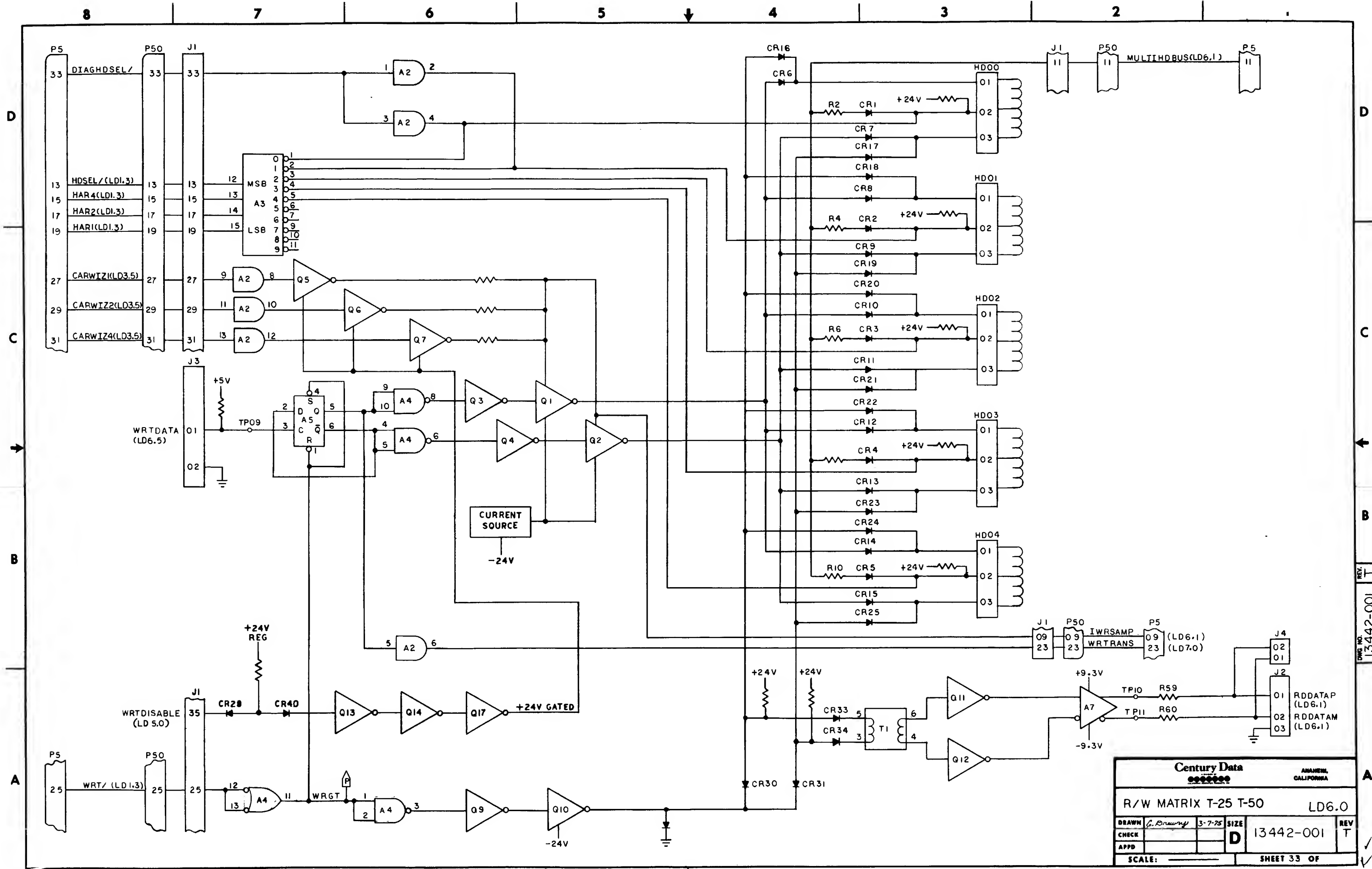
CENTURY DATA SYSTEMS, INC.
ANAHEIM, CALIFORNIA

INDEX AND SECTOR

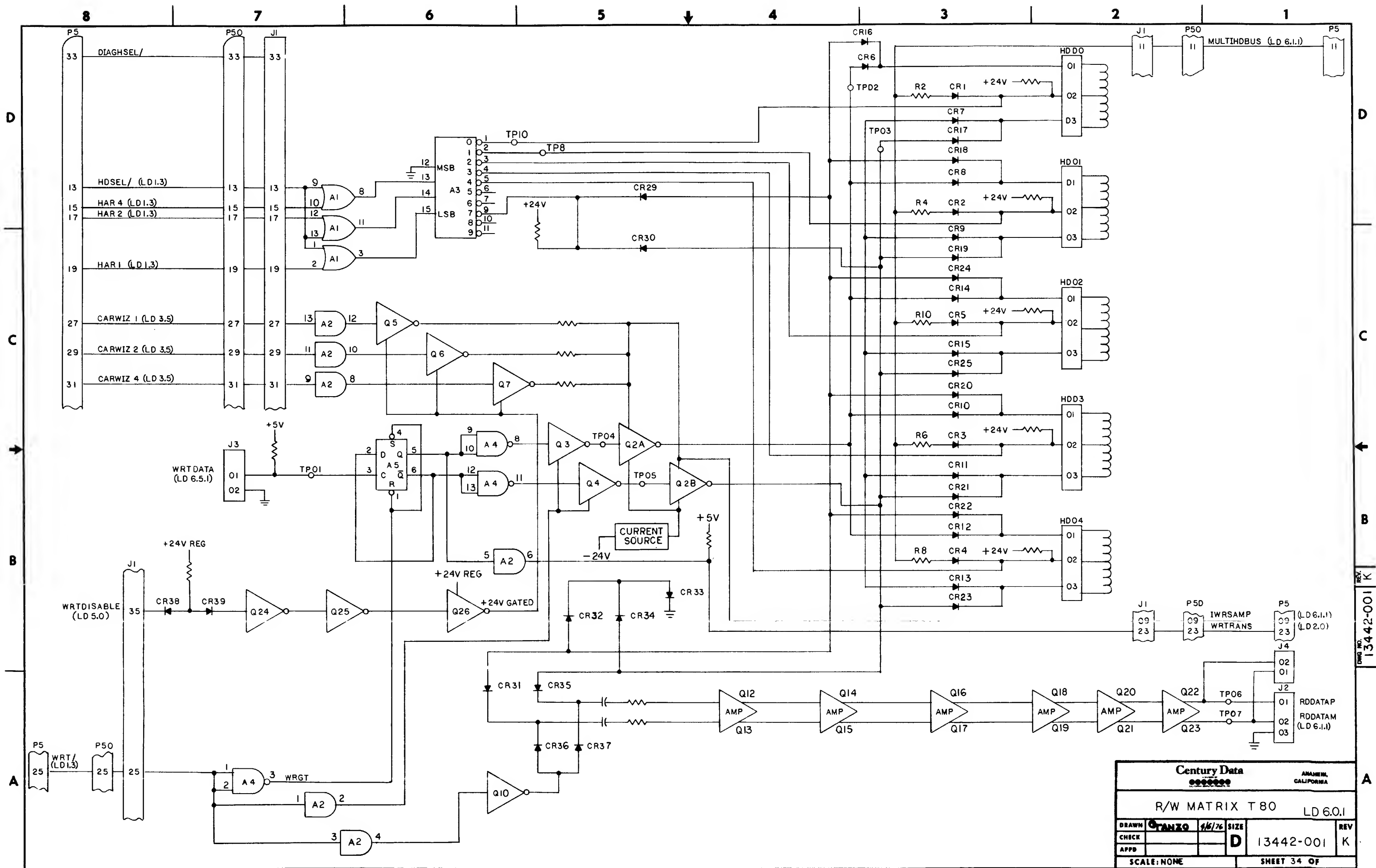
LD 5.1

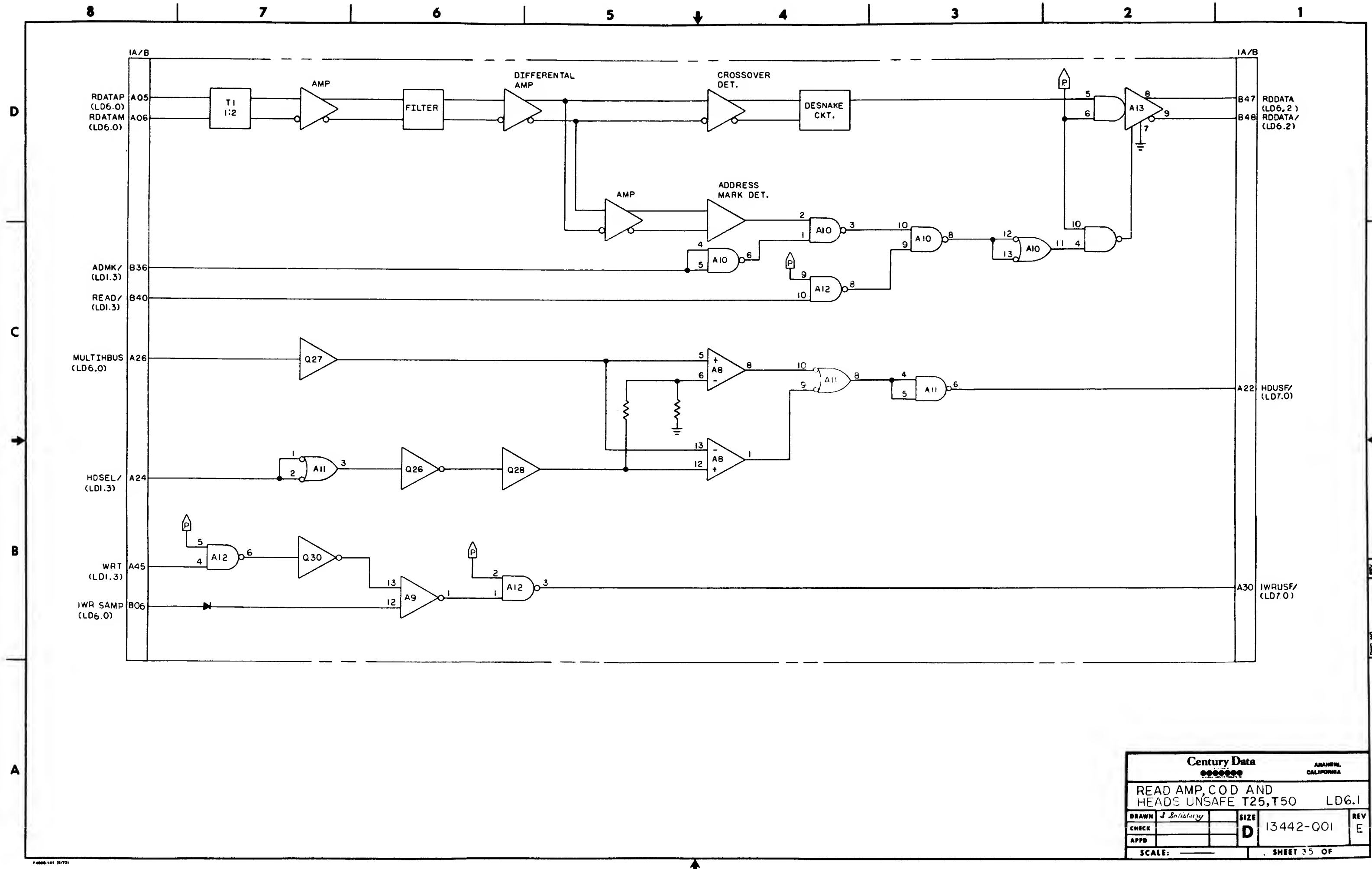
DRAWN	J. Antisburg	SIZE	13442-001	REV	C
CHECK					
APPD					

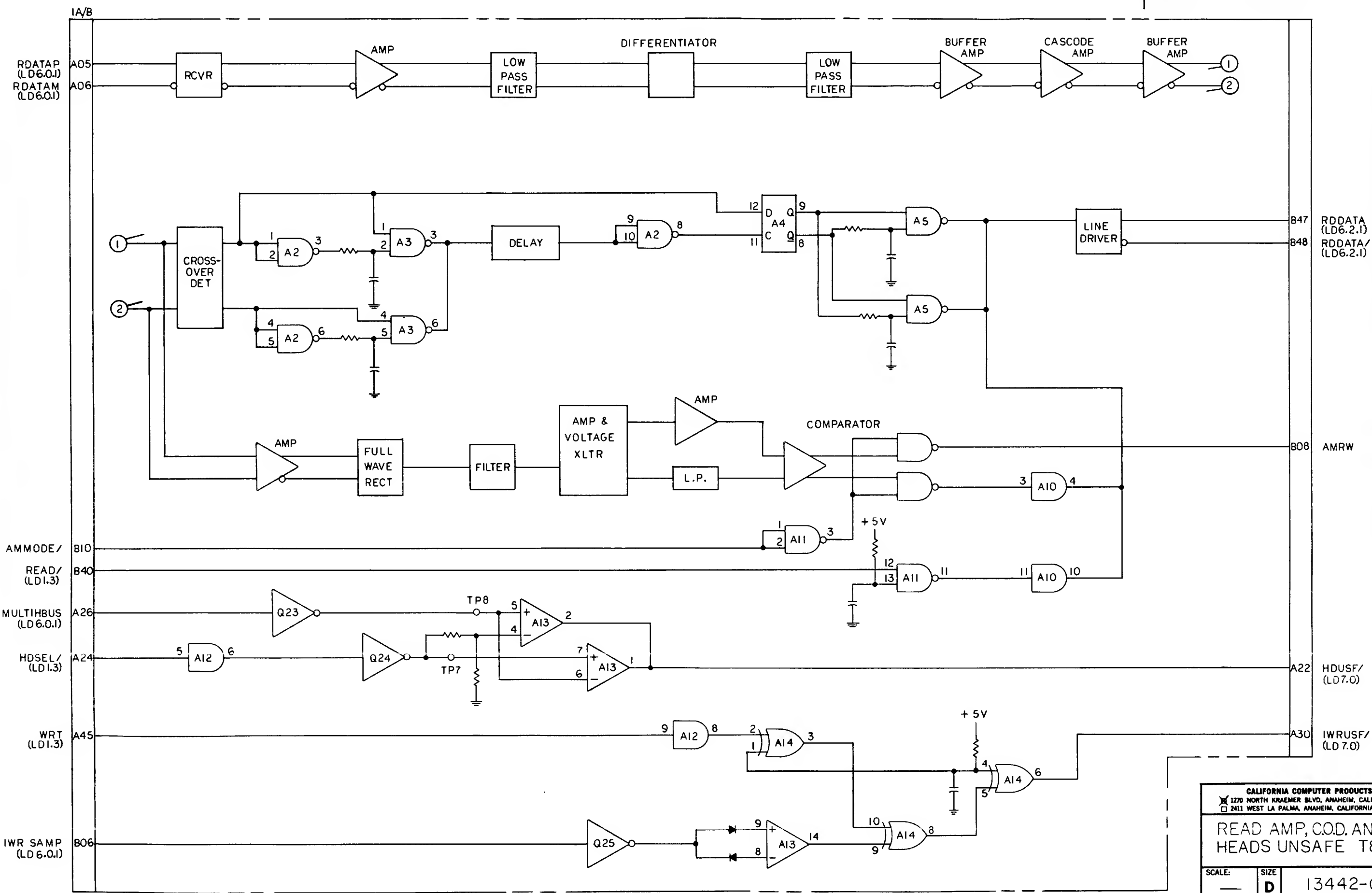
SCALE: SHEET 32 OF 1



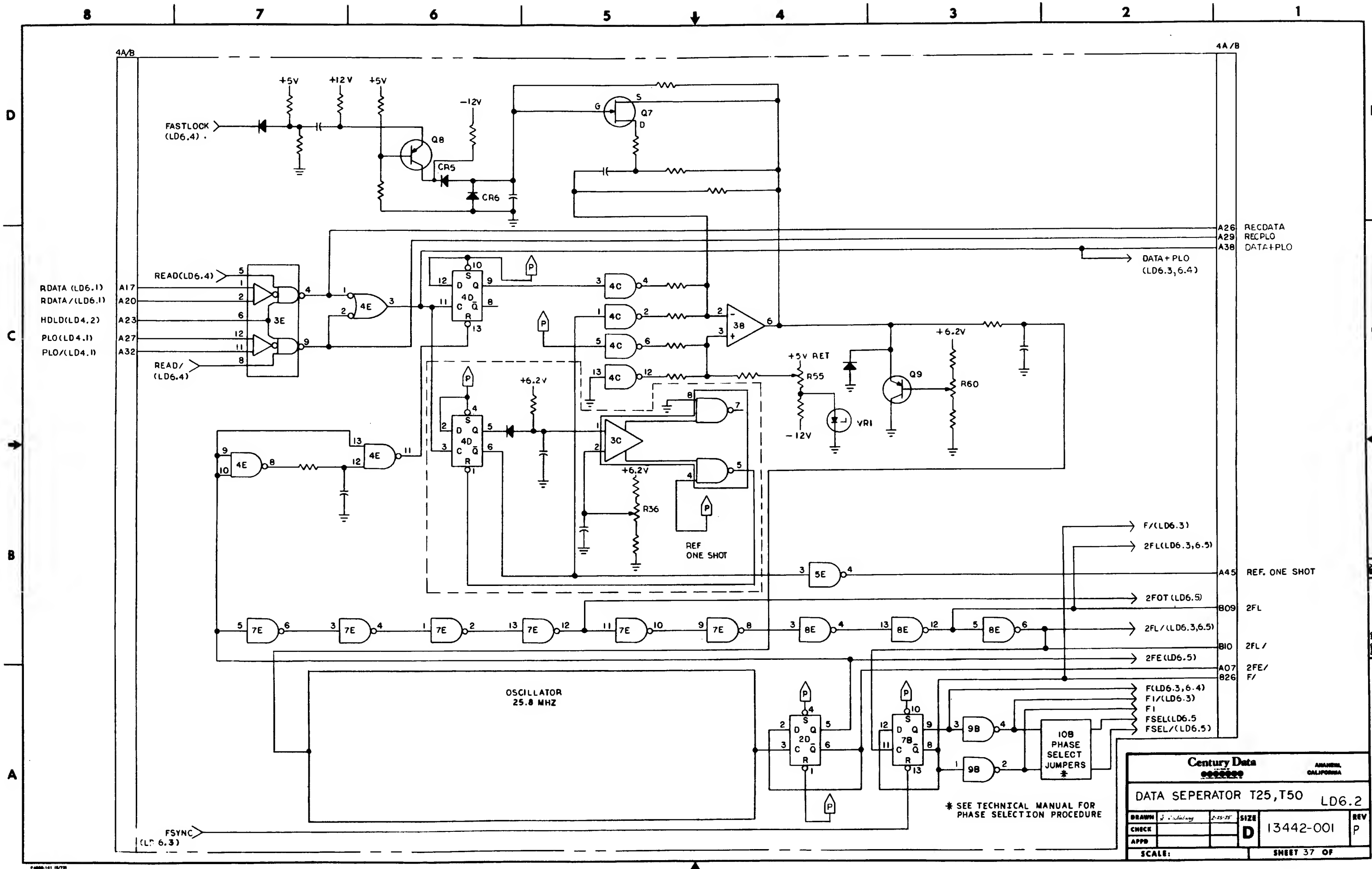
Century Data		ANAHEIM, CALIFORNIA	
R/W MATRIX T-25 T-50 LD6.0			
DRAWN	G. Brown	3-7-75	SIZE
CHECK			D
APPD			13442-001
SCALE:		SHEET 33 OF	





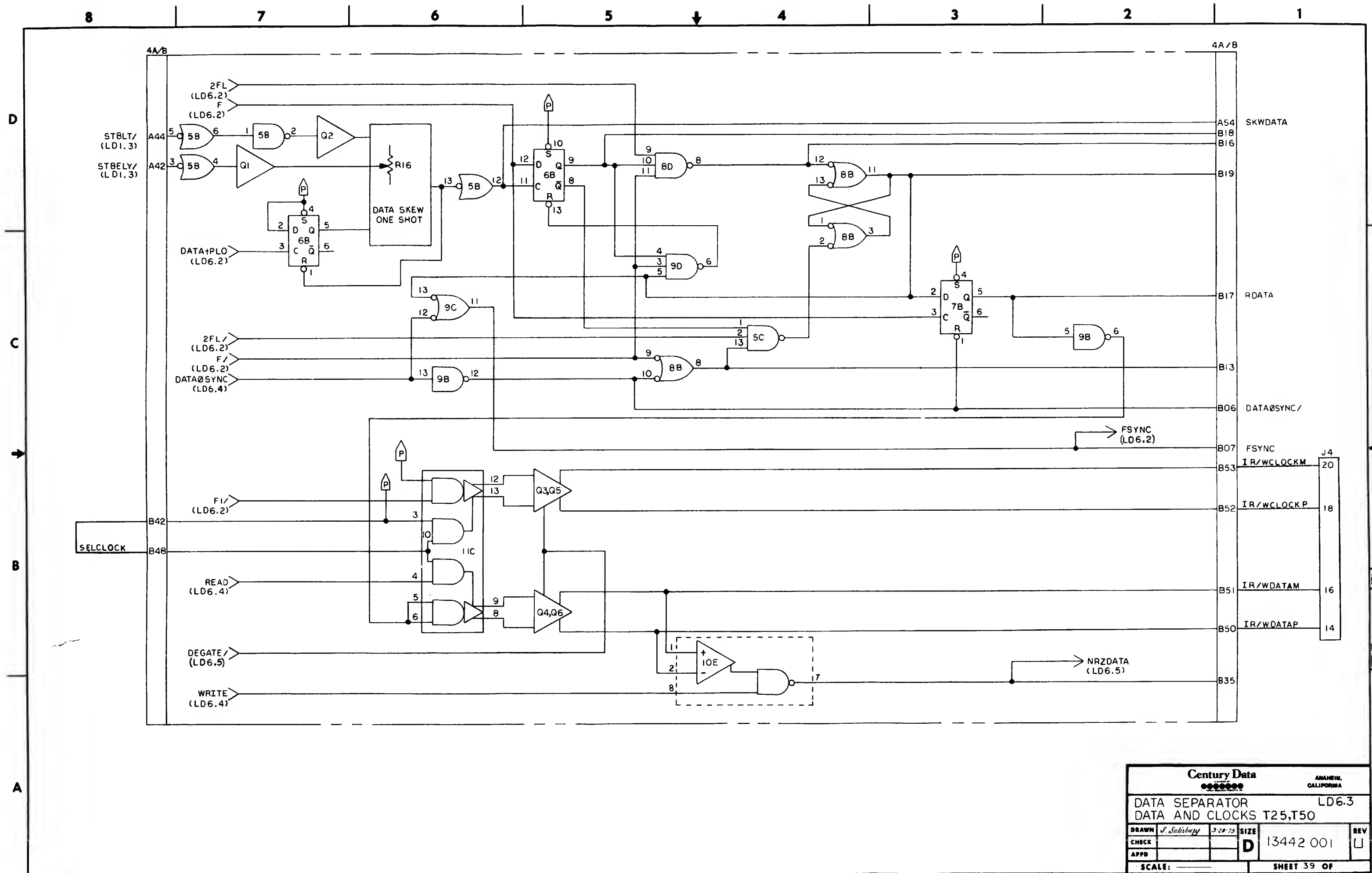


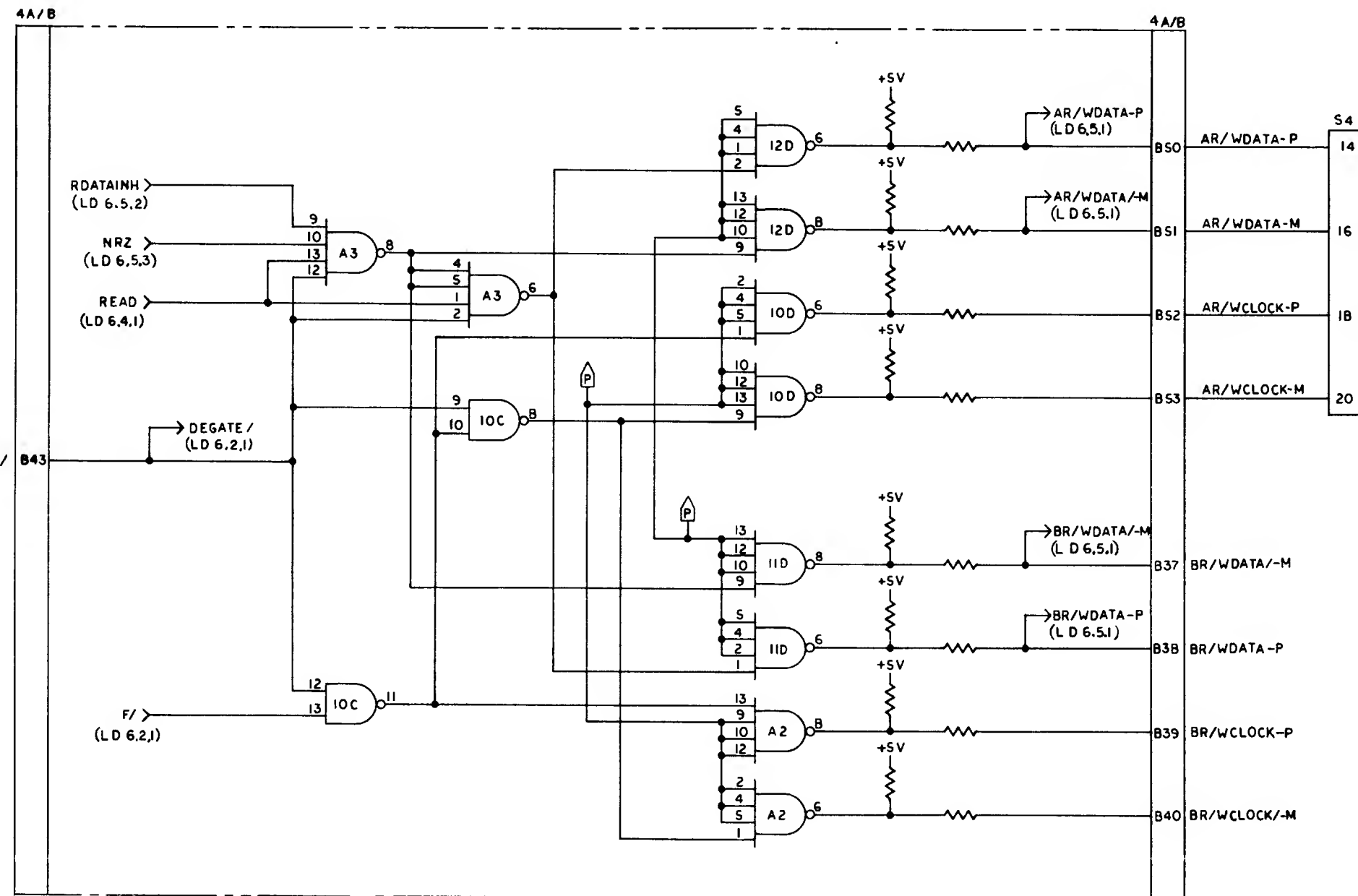
CALIFORNIA COMPUTER PRODUCTS INC. 1270 NORTH KRAEMER BLVD, ANAHEIM, CALIFORNIA 92805 2411 WEST LA PALMA, ANAHEIM, CALIFORNIA 92801			
READ AMP, C.O.D. AND HEADS UNSAFE T80 LD6.1.1			
SCALE:	SIZE	REV	
—	D	13442-001	K
SHEET 36 OF			



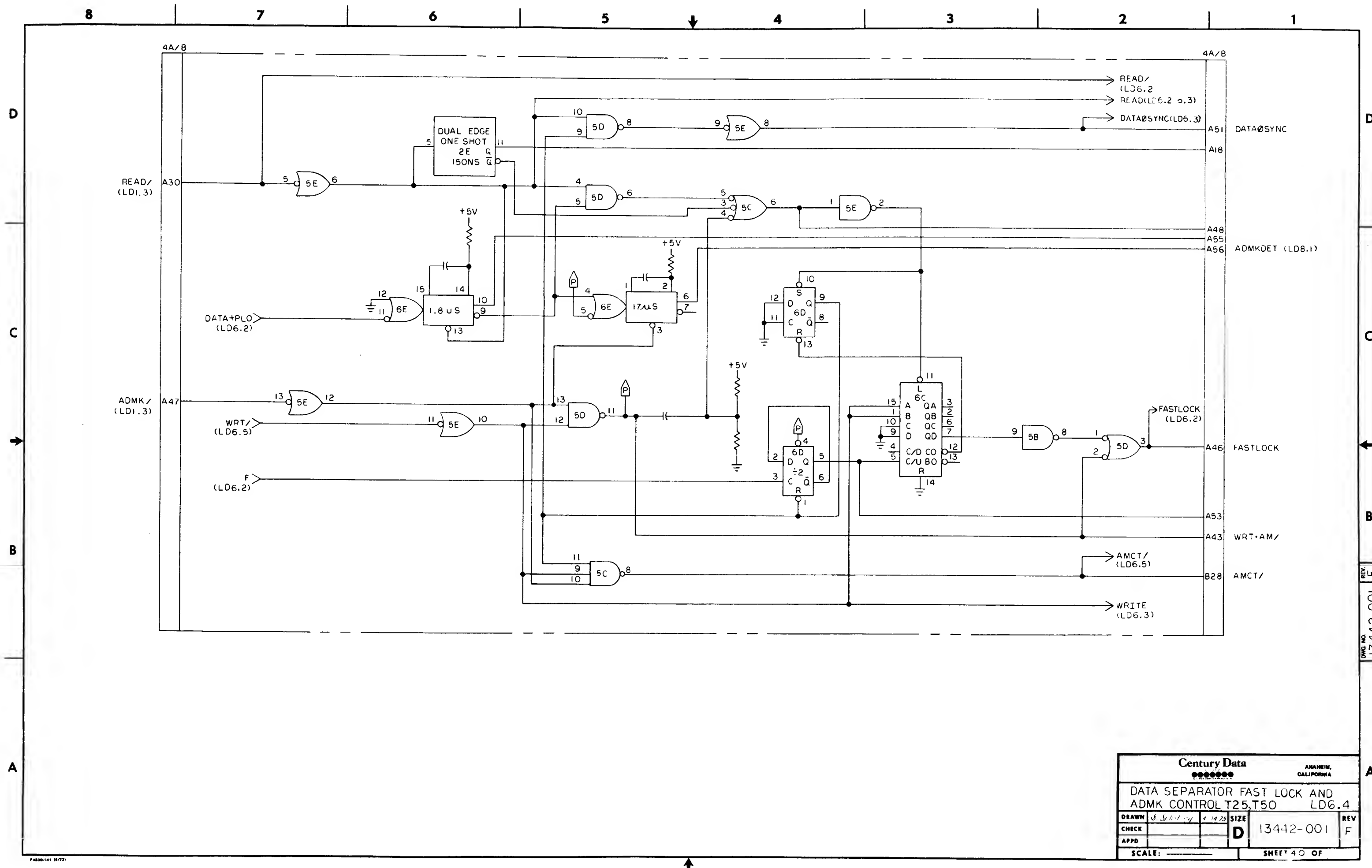
Century Data		ANAHEIM, CALIFORNIA	
DATA SEPARATOR T25, T50 LD6.2			
DRAWN	3	DATE	2-15-75
CHECK		SIZE	D
APPD		REV	P
SCALE:		SHEET 37 OF	

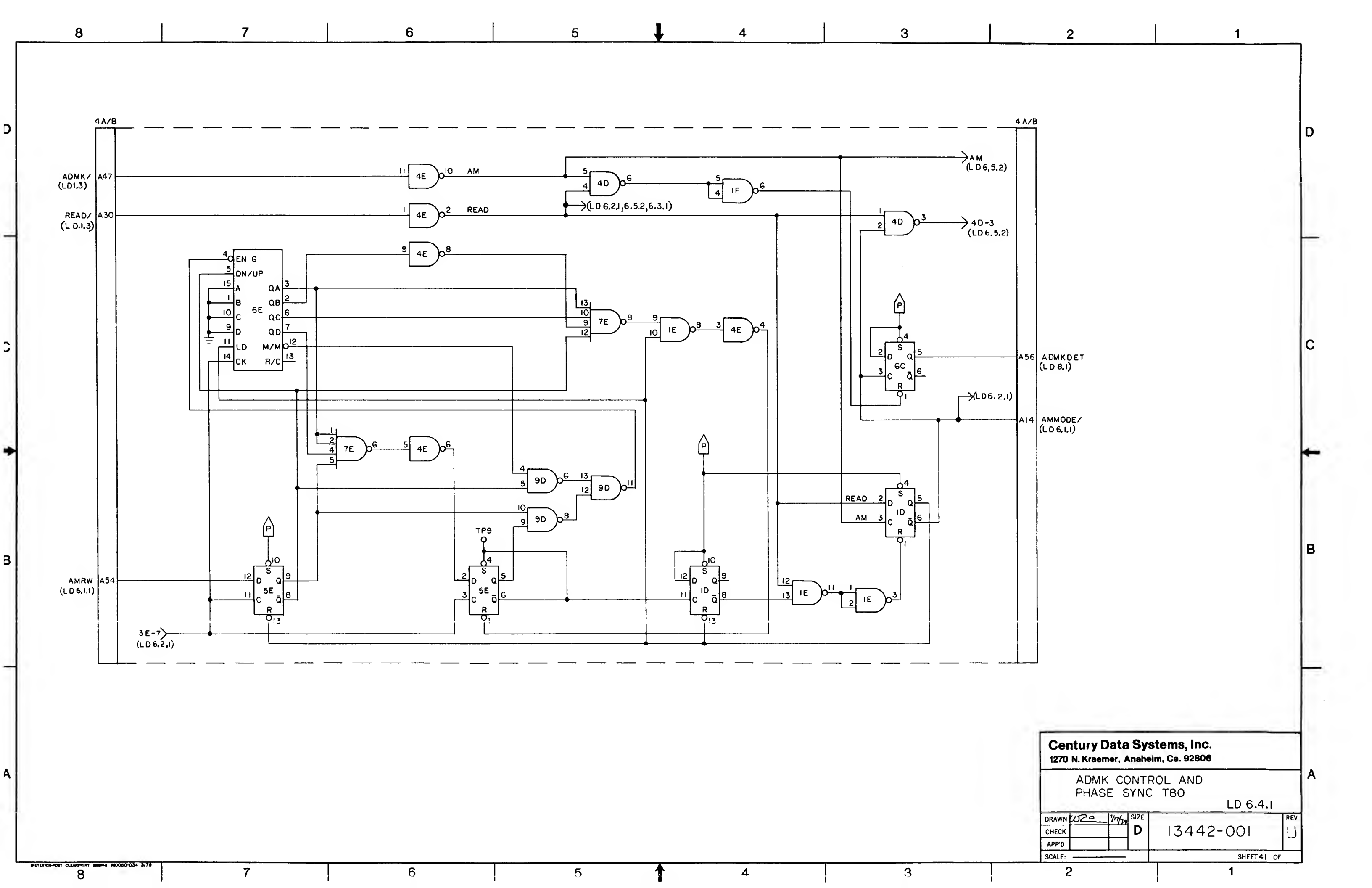
13442-001 P



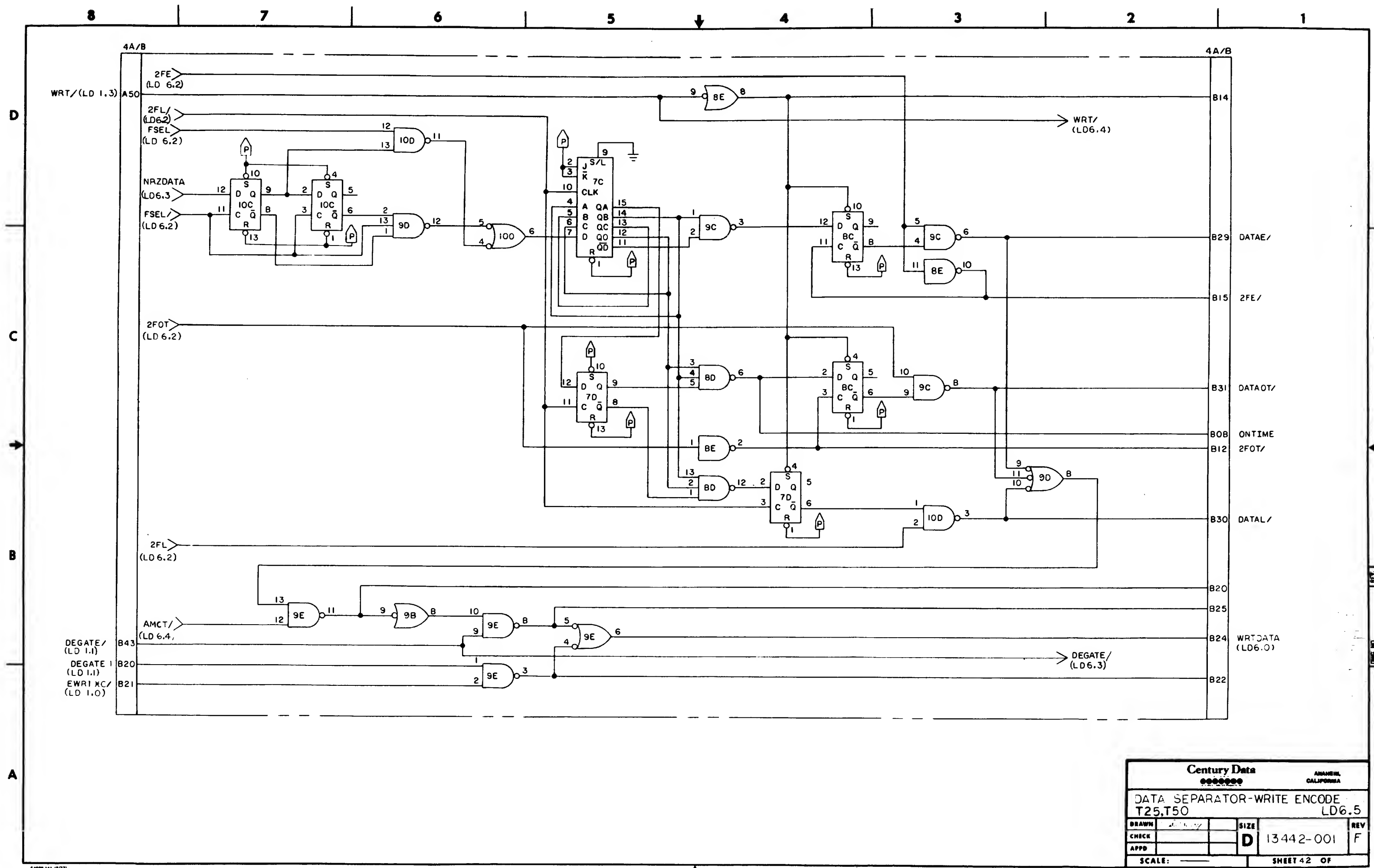


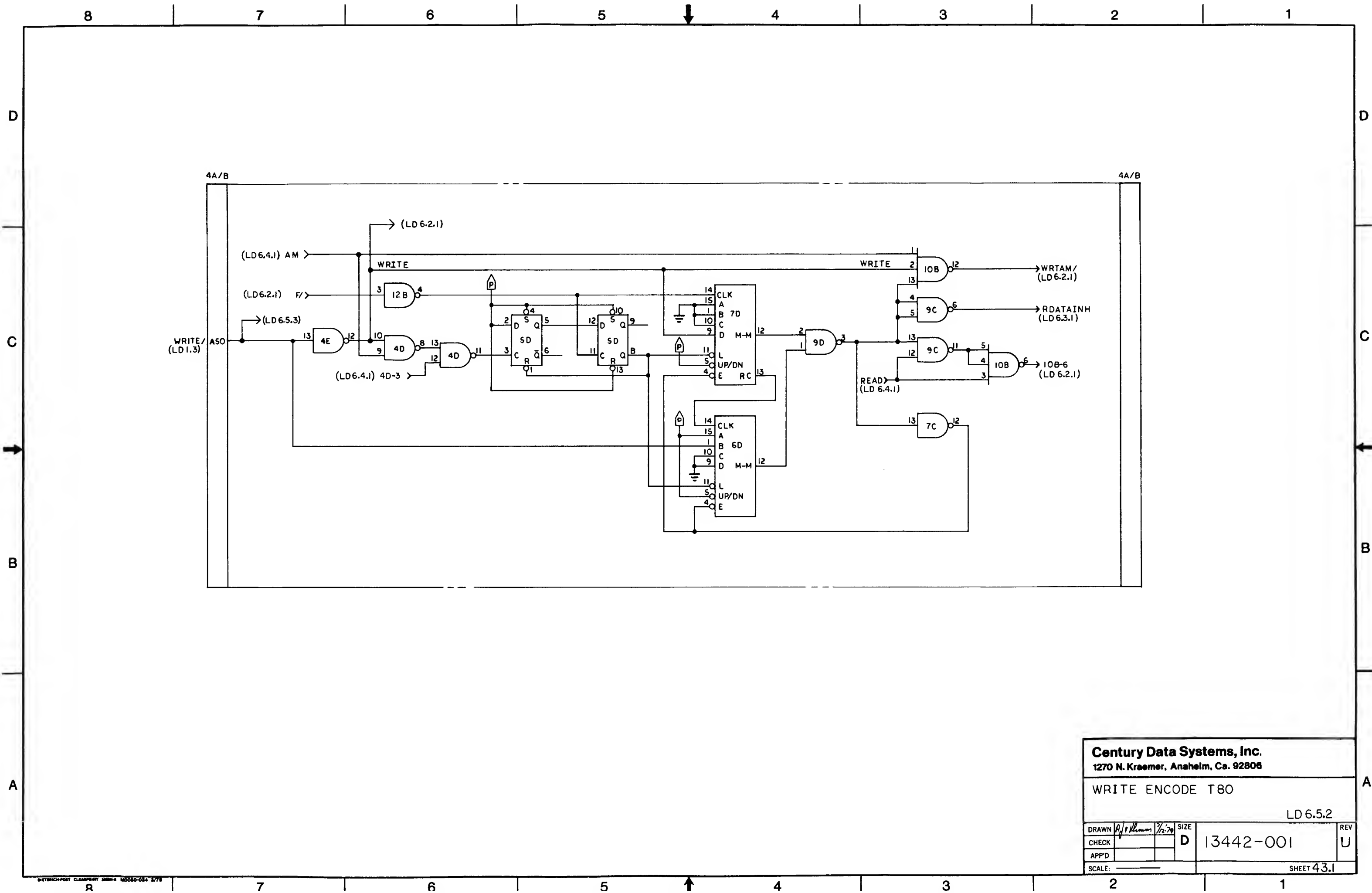
Century Data Systems, Inc. 1270 N. Kraemer, Anaheim, Ca. 92806			
DATA AND CLOCKS, T80 LD 6.3.1			
DRAWN	<i>[Signature]</i>	SIZE	D
CHECK			
APP'D			
SCALE:		13442 - 001	
		SHEET 39 OF 106	





Century Data Systems, Inc. 1270 N. Kraemer, Anaheim, Ca. 92806			
ADMK CONTROL AND PHASE SYNC T80			
LD 6.4.1			
DRAWN	W20	1/17/77	SIZE
CHECK			D
APP'D			
SCALE:			13442-001
SHEET 41 OF			REV





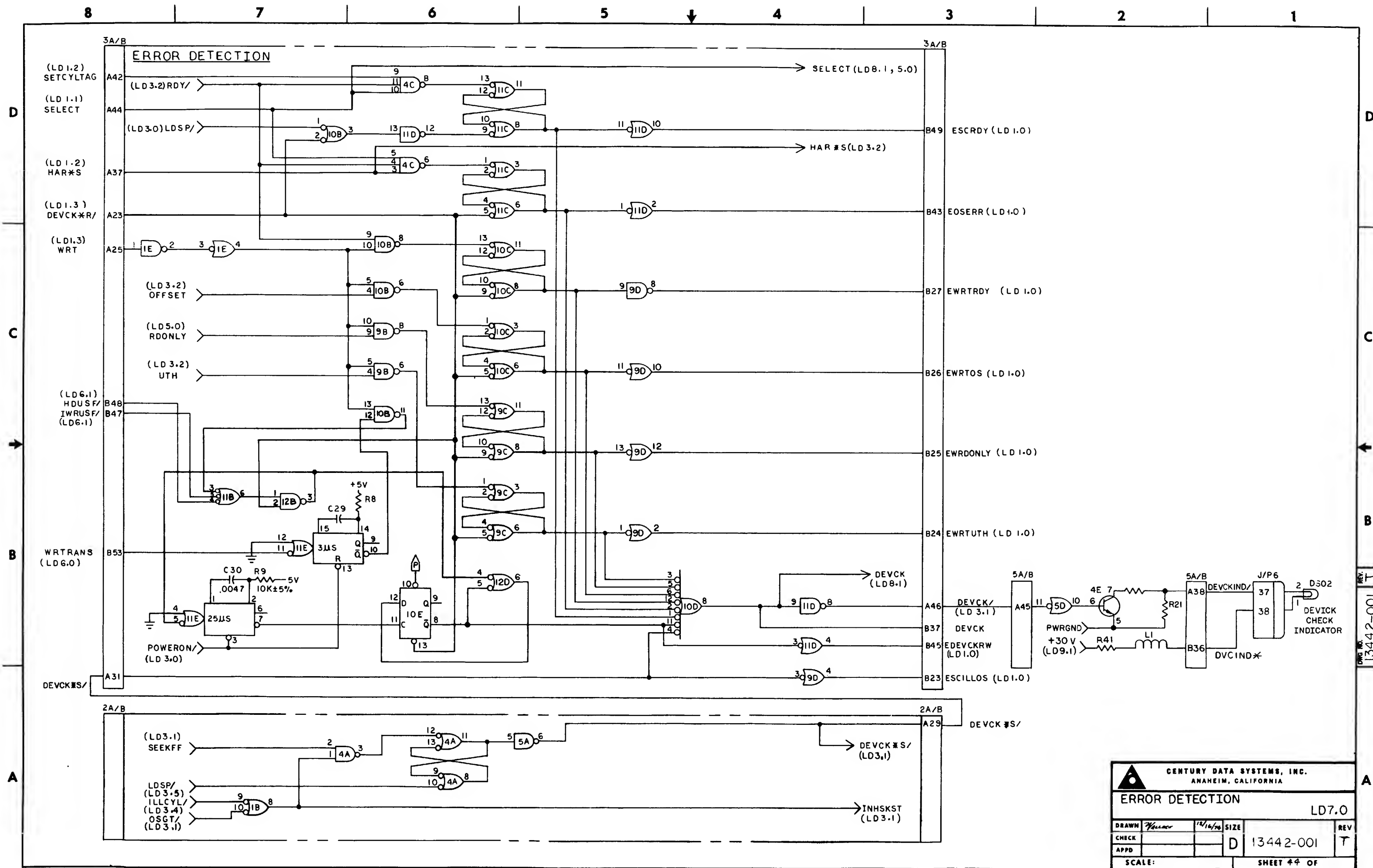
Century Data Systems, Inc.
1270 N. Kraemer, Anaheim, Ca. 92806

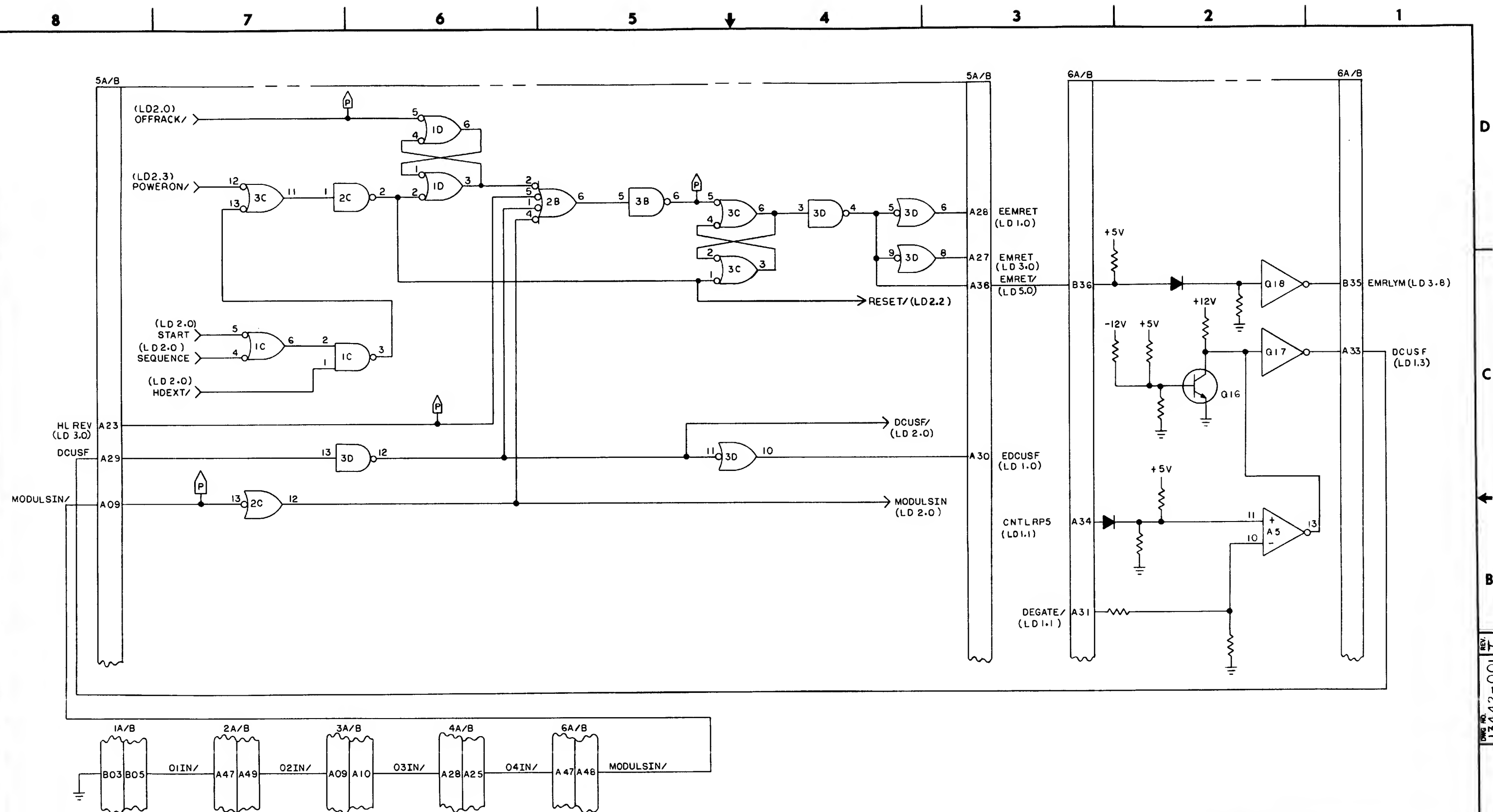
WRITE ENCODE T80

LD 6.5.2

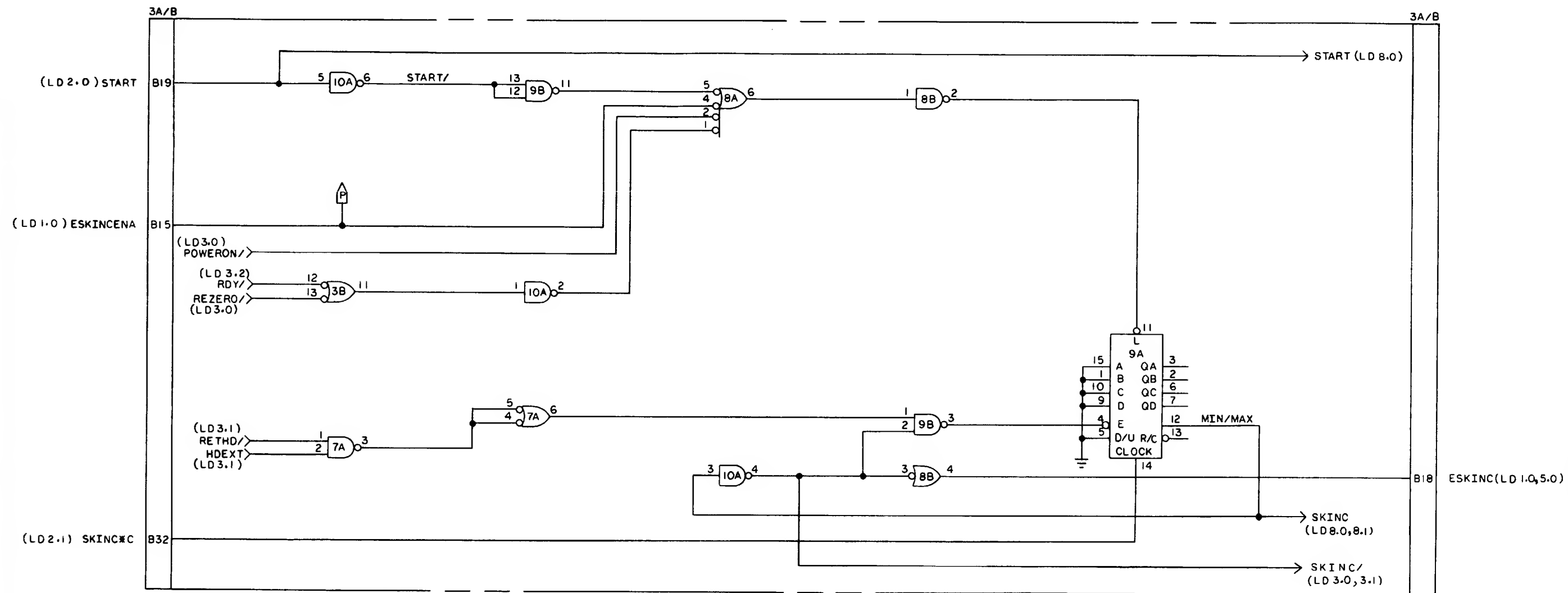
DRAWN	<i>[Signature]</i>	SIZE	D	13442-001	REV U
CHECK					
APP'D					
SCALE					

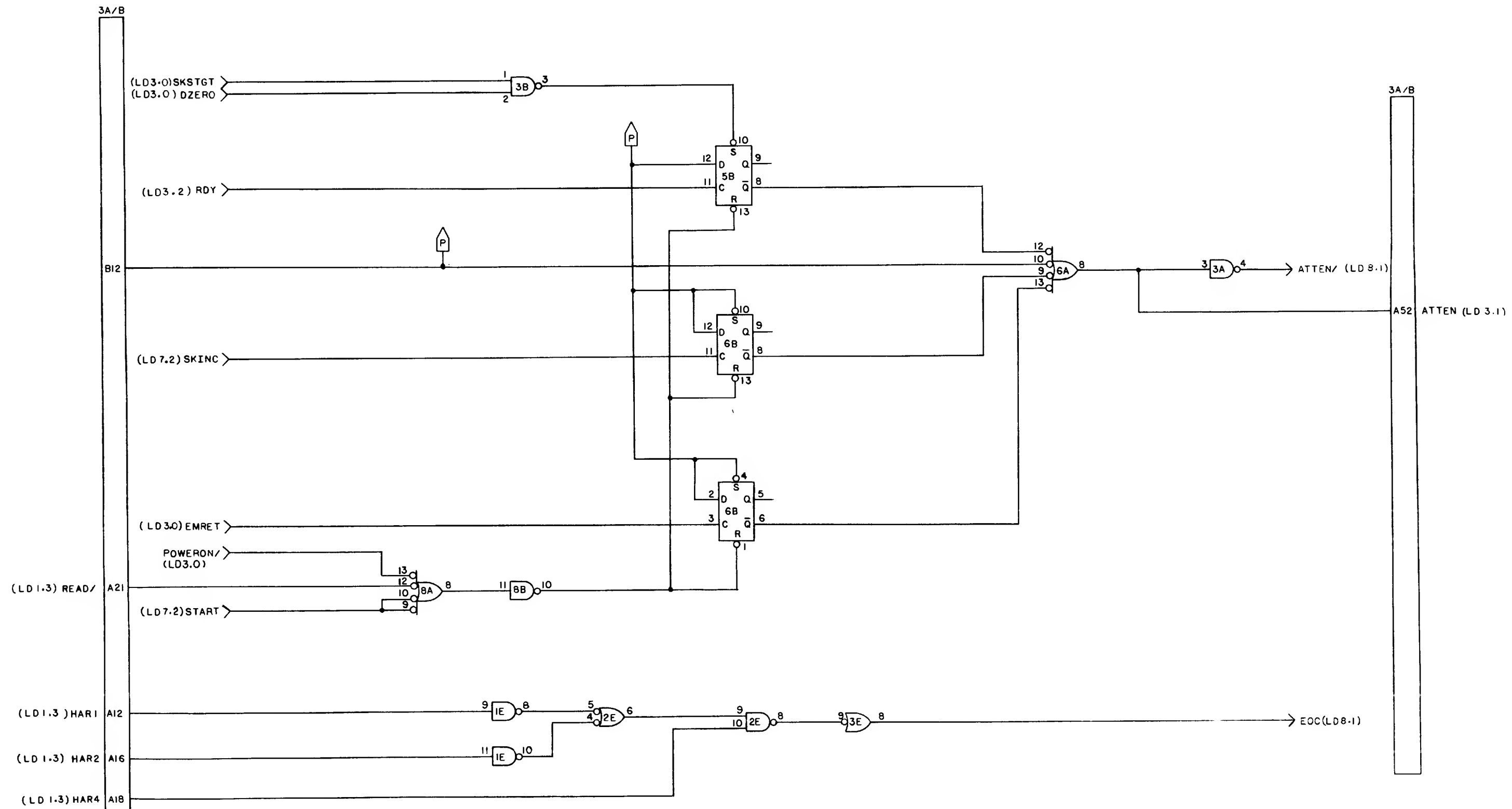
SHEET 43.1






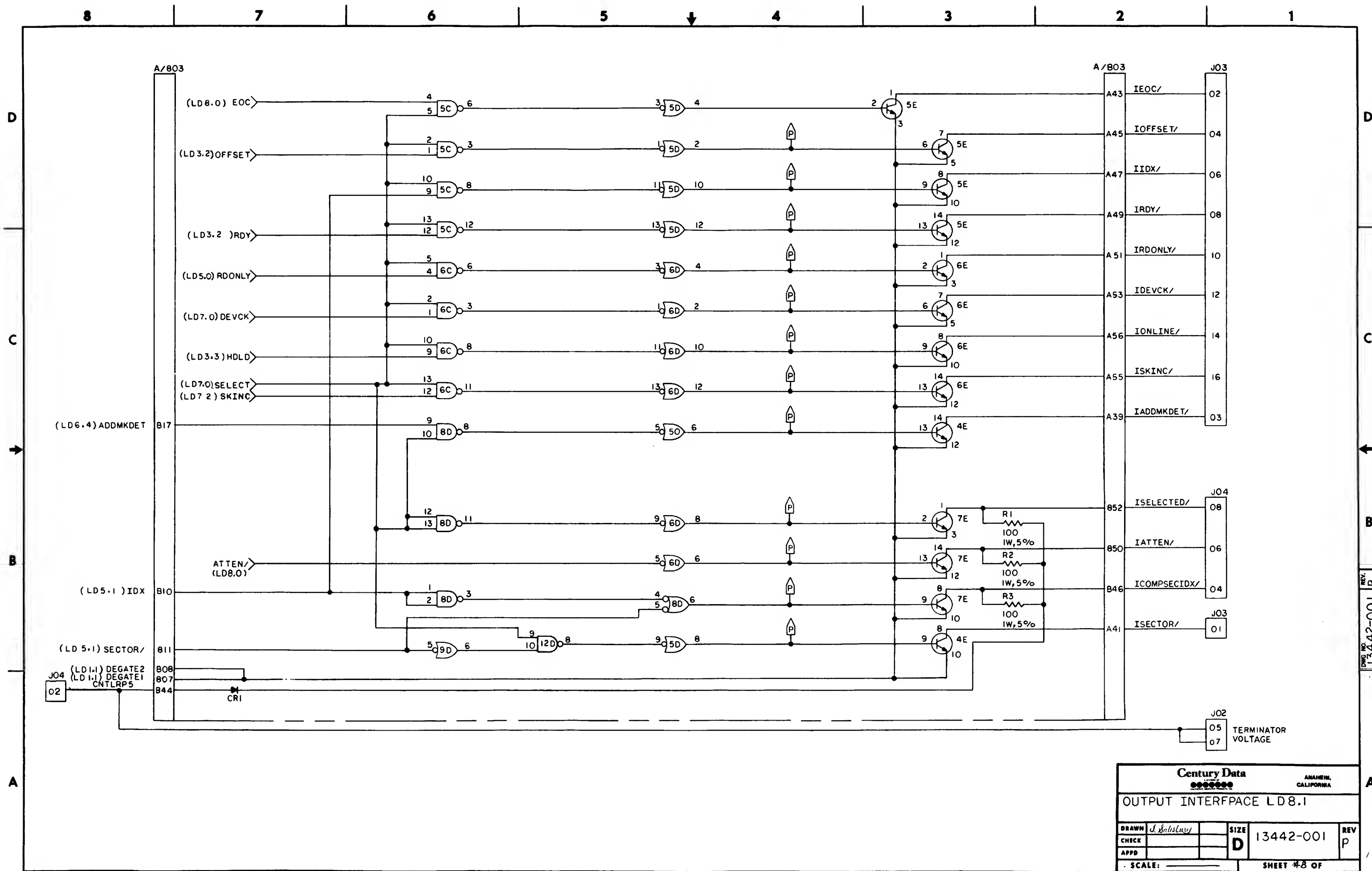
Century Data			ANALOG, CALIFORNIA		
●●●●●●●●					
DCUSF, EMERGENCY RETRACT AND MODULE IN INTERLOCK LD7.1					
DRAWN	J. Salisbury	3-3-73	SIZE	13442-001	REV T
CHECK			D		
APPD					
SCALE: _____			SHEET 45 OF		



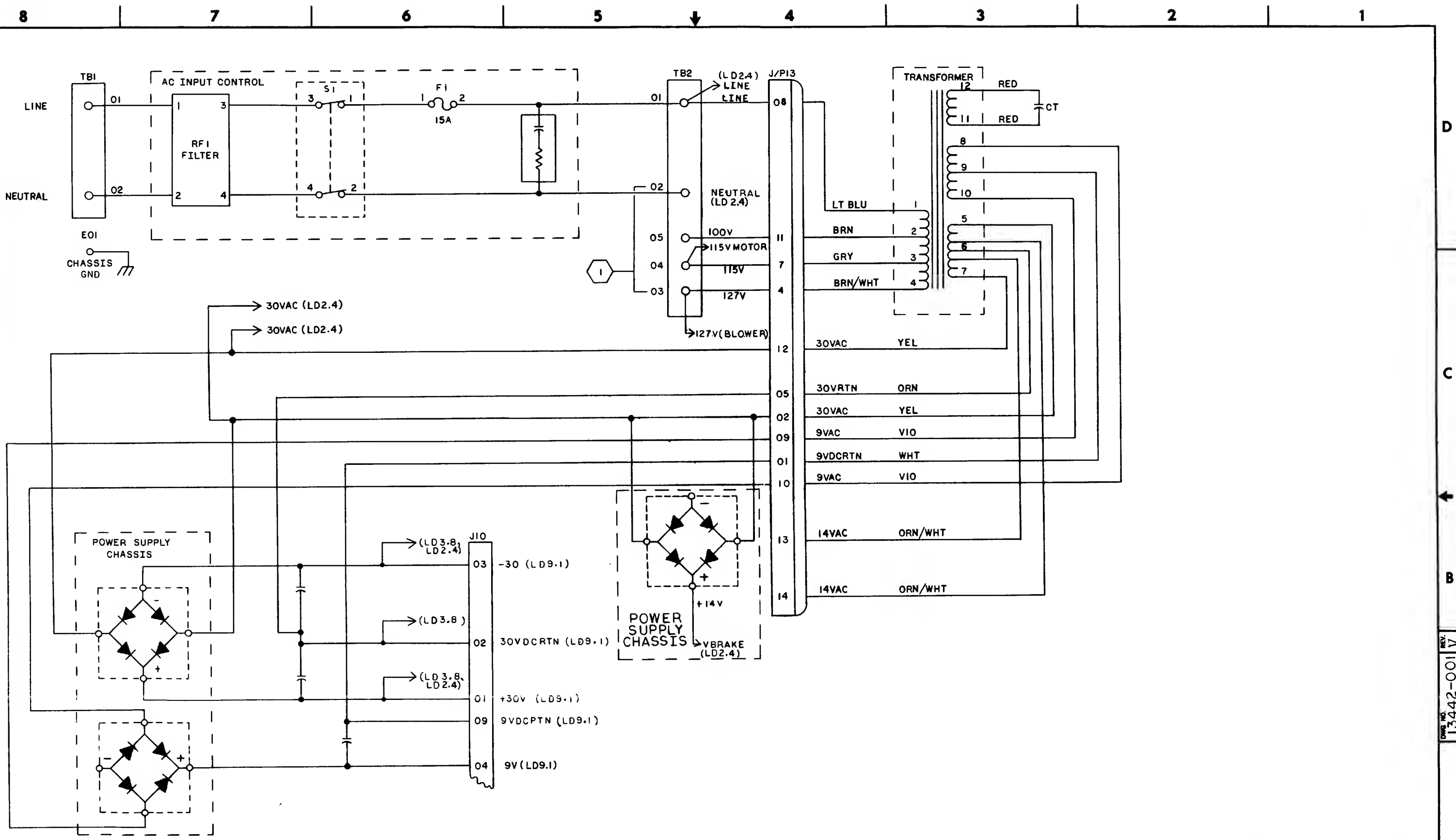


 CENTURY DATA SYSTEMS, INC. ANAHEIM, CALIFORNIA			
ATTENTION AND EOC LD8.0			
DRAWN		SIZE	REV
CHECK		D	13442-001 C
APPRO			
SCALE:		SHEET 47 OF	

13442-001 C



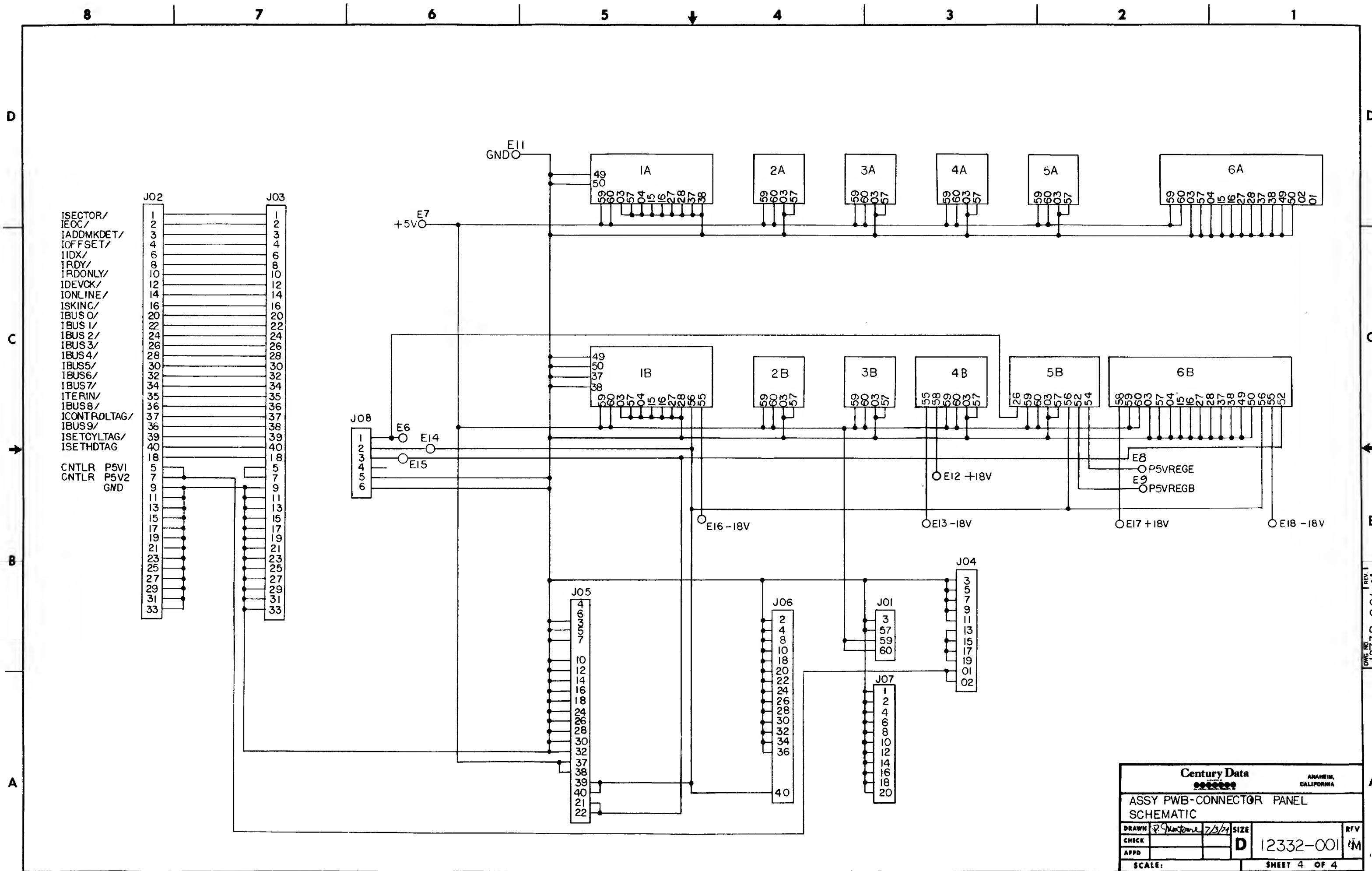
Century Data		ANAHEIM, CALIFORNIA	
OUTPUT INTERFACE LD8.1			
DRAWN	J. Salas	SIZE	13442-001
CHECK		REV	P
APPD		SHEET 48 OF	
SCALE:			



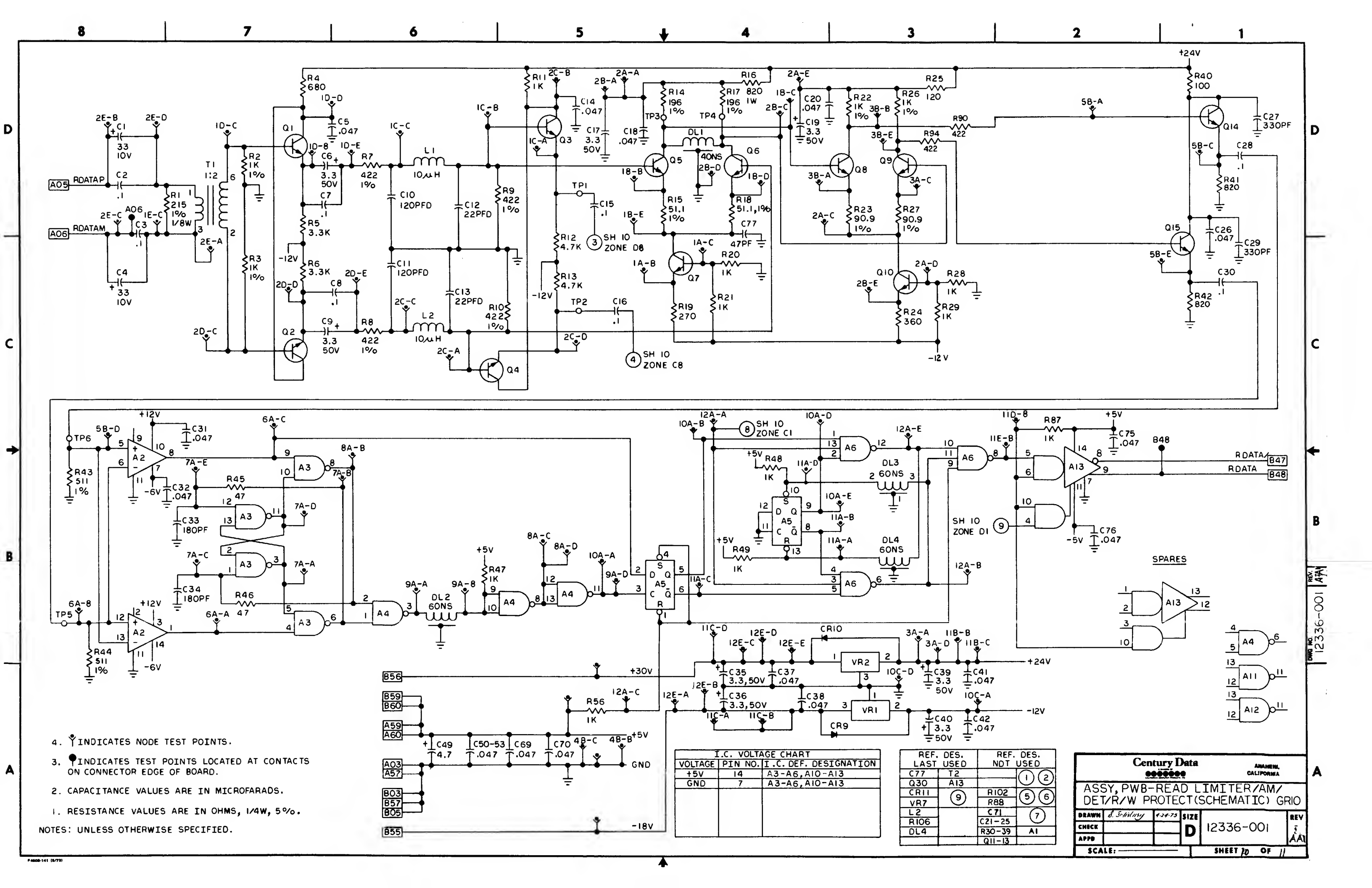
NOTES:

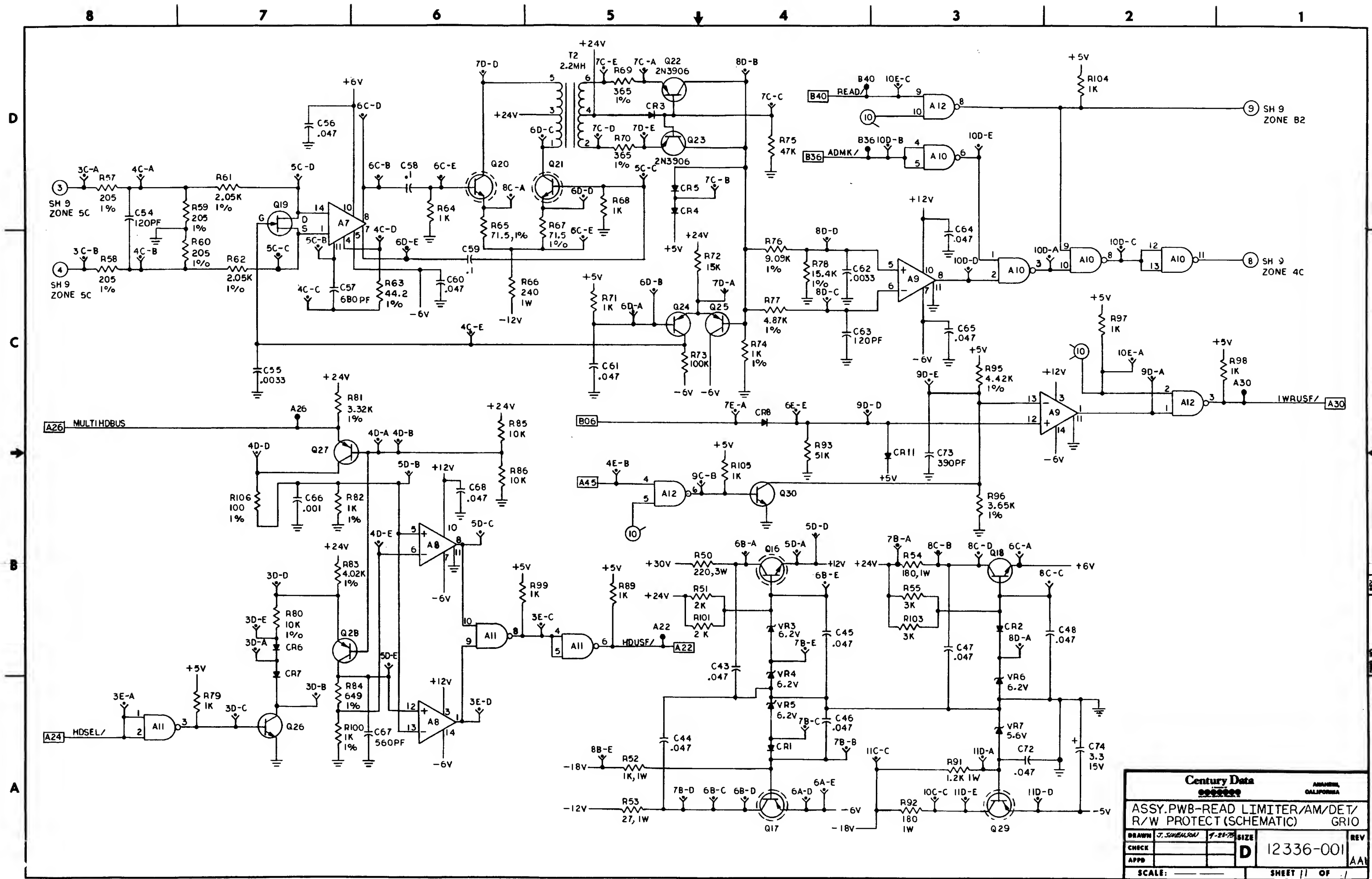
1 TB2-2 IS JUMPERED TO TB2-3, (127VAC), TO TB2-4, (115V), OR TO TB2-5, (100VAC).

Century Data ANALOG ●●●●●●●● 10 MILLI AMPERE @ 50 VOLTS DC			ANAHEIM, CALIFORNIA	
POWER SUPPLY(LD9.0.1)				
TRIDENT				
DRAWN	2 DEW 4	12-6-75	SIZE	13442-001
CHECK			D	
APPD				
SCALE: ———			SHEET 49 OF	



Century Data		ANAHEIM, CALIFORNIA	
ASSY PWB-CONNECTOR PANEL			
SCHEMATIC			
DRAWN	8/10/73	SIZE	D
CHECK			
APPD			
SCALE:		SHEET 4 OF 4	





Century Data				ANAHEIM, CALIFORNIA	
9999999					
ASSY.PWB-READ LIMITER/AM/DET/ R/W PROTECT (SCHEMATIC) GR10					
DRAWN	J. SHELTON	1-21-75	SIZE	12336-001	REV
CHECK			D		
APPD					AA
SCALE: _____			SHEET 11 OF 1		



REF DES LAST USED		REF OES NOT USED	
6A			
7B			
12C		10C	
120			
12E			
R10	49	68	60
C28	S1		



CYLINDER ADDRESS REGISTER

SUBTRACTOR

DIFFERENCE INPUT CONTROL

D

D

C

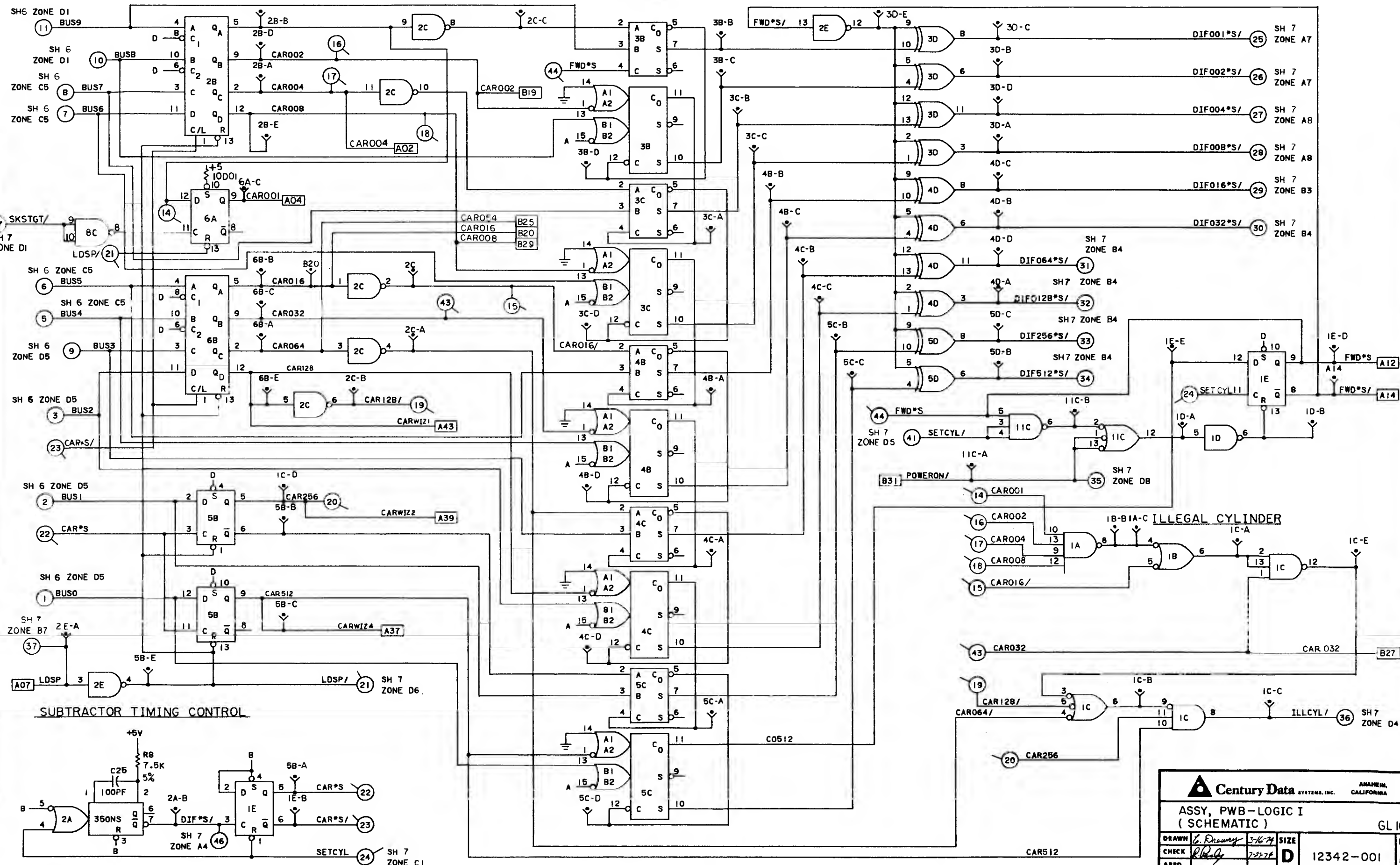
C

B

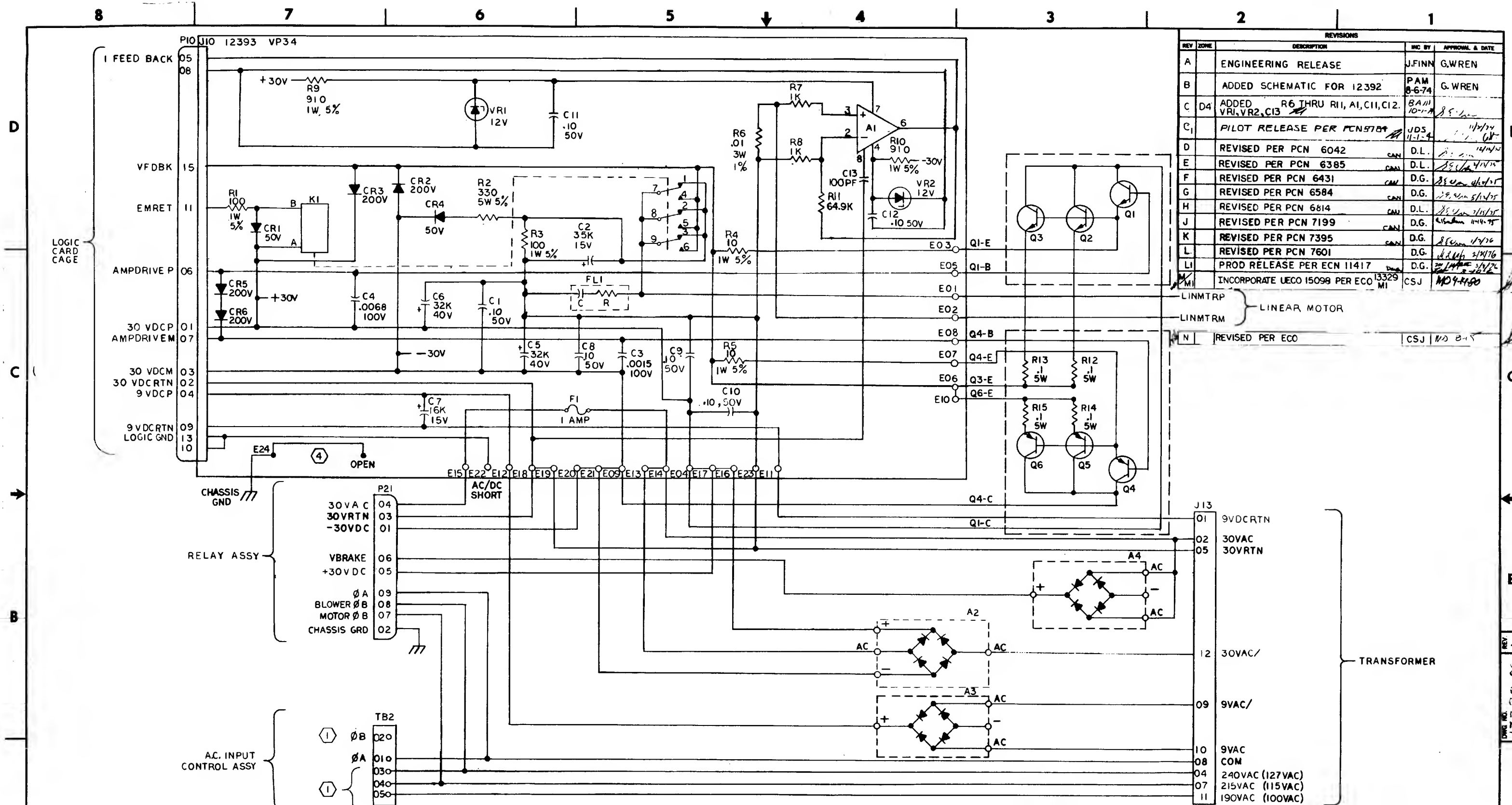
B

A

A



Century Data SYSTEMS, INC. ANAHEIM, CALIFORNIA	
ASSY, PWB-LOGIC I (SCHEMATIC) GL 10	
DRAWN <i>E. Droug</i> 3-16-74	SIZE D
CHECK <i>R. Kelly</i> 7-27-74	REV M
APPD	12342-001
SCALE: —	SHEET 8 OF 8



REVISIONS			
REV	ZONE	DESCRIPTION	INC BY / APPROVAL & DATE
A		ENGINEERING RELEASE	J.FINN / G.WREN
B		ADDED SCHEMATIC FOR 12392	PAM 8-6-74 / G.WREN
C	D4	ADDED R6 THRU R11, A1, C11, C12.	BA/11 10-1-74 / JDS
C1		PILOT RELEASE PER PCN 5784	11-1-74 / JDS
D		REVISED PER PCN 6042	D.L. 12/1/74
E		REVISED PER PCN 6385	D.L. 12/1/74
F		REVISED PER PCN 6431	D.G. 12/1/74
G		REVISED PER PCN 6584	D.G. 12/1/74
H		REVISED PER PCN 6814	D.L. 12/1/74
J		REVISED PER PCN 7199	D.G. 12/1/74
K		REVISED PER PCN 7395	D.G. 12/1/74
L		REVISED PER PCN 7601	D.G. 12/1/74
LI		PROD RELEASE PER ECN 11417	D.G. 12/1/74
MI		INCORPORATE UECO 15098 PER ECO 13329	CSJ 12/1/74

- ④ MOVE JUMPER FROM "OPEN" TERMINAL TO "AC/DC SHORT" FOR OFF LINE OPERATION.
3. CAPACITANCE VALUES ARE IN MICROFARADS
2. RESISTANCE VALUES ARE IN OHMS, 1/4W, 5%.
- ① TB2-2(ØB) IS JUMPED TO TB2-3 (240VAC/127VAC), TB2-4 (215VAC/115VAC) OR TB2-5 (190VAC/100VAC) DEPENDING ON LINE VOLTAGE.
- NOTES: UNLESS OTHERWISE SPECIFIED.

REF DES LAST USED	REF DES NOT USED
C13	E23
CR4	J13
R11	VR2
K1	A4
FL1	P21
F1	TB2

MODEL NO. FIRST USE T-50		NEXT ASSY FIRST USE 12449 -	
UNLESS OTHERWISE SPECIFIED		DRAWN J.FINN 6/74	
DIMENSIONS ARE IN INCHES		CHECK ADDIS 8-22-74	
TOLERANCES ON DECIMALS ANGLES		APPD M. J. FINN 5-7-74	
XX ±		FINISH	
XXX ±		MATERIAL	
SURFACE ROUGHNESS PER MIL-STD-10		SCALE: D	
		SIZE DWG NO. 13329-001	
		WEIGHT	
		SHEET 1 OF 1	

Century Data SYSTEMS, INC. ANAHEIM, CALIFORNIA

PWR SUPPLY AND AMPLIFIER (SCHEMATIC)

VP34

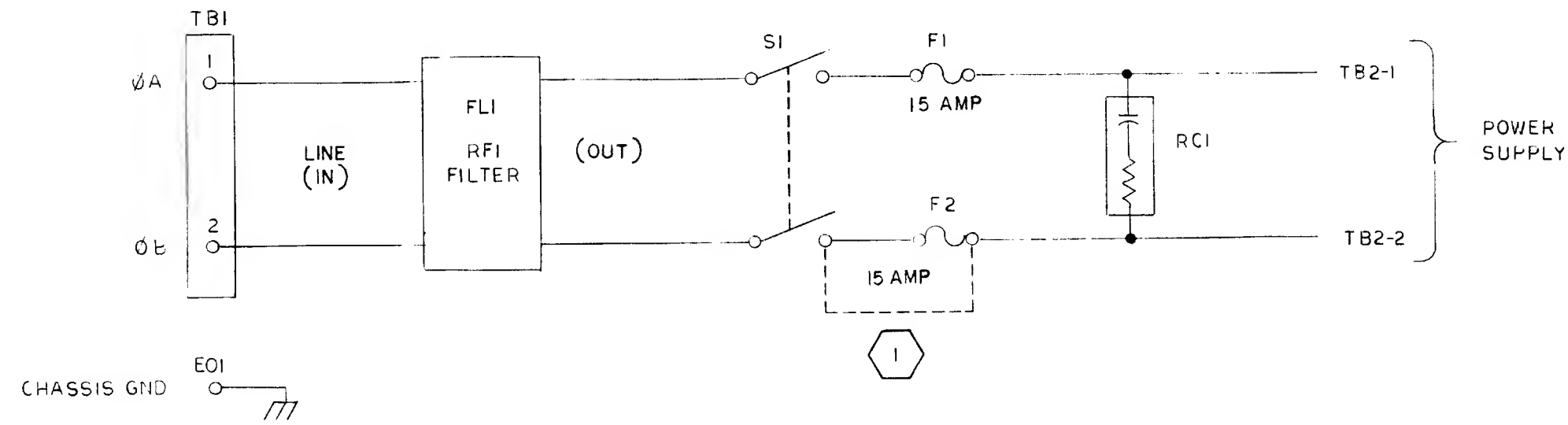
4

3

2

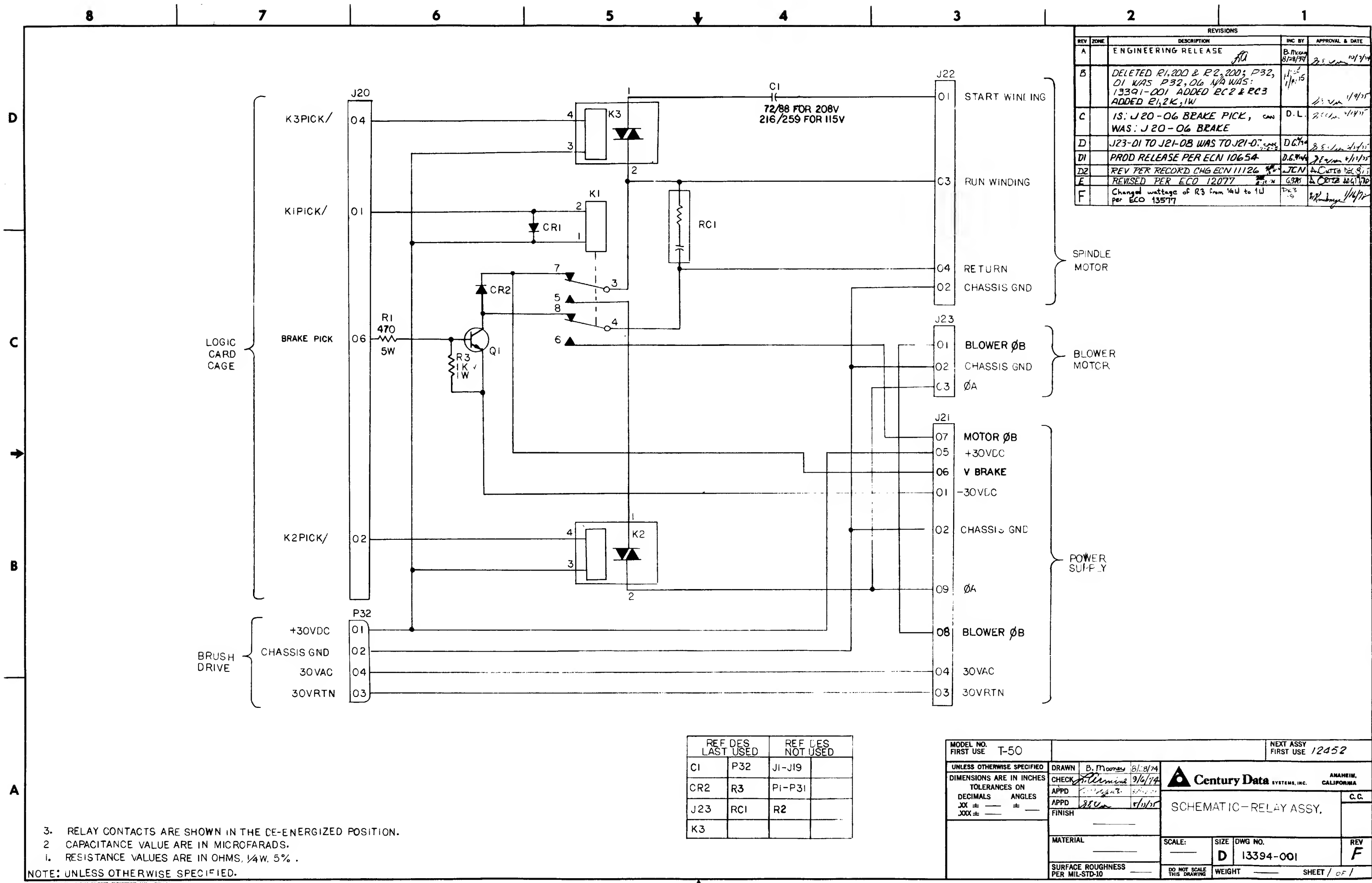
1

REVISIONS				
REV	ZONE	DESCRIPTION	INC BY	APPROVAL & DATE
A		ENGINEERING RELEASE	JFINN 6/74	<i>[Signature]</i> 6/74
B		ADDED 15 AMP CALLOUT TO F1,F2; ADDED FLI,LINE (IN) & (OUT) CALLOUT TO RFI FILTER; ADDED NOTE 1; NEXT ASSY WAS,12-49 001.	D.L.	<i>[Signature]</i> 1/4/75
BI		PROD RELEASE PER ECN 10654	D.G. 1/4/75	<i>[Signature]</i> 1/14/75



1 F1,F2 FOR LINE TO LINE 13372-001 F1 ONLY FOR LINE TO NEUTRAL 13372-002.

MODEL NO. FIRST USE T-50		NEXT ASSY FIRST USE 13372		
UNLESS OTHERWISE SPECIFIED		DRAWN J FINN 6/74	Century Data SYSTEMS, INC. ANAHEIM, CALIFORNIA	
DIMENSIONS ARE IN INCHES		CHECK		C.C.
TOLERANCES ON		APPD <i>[Signature]</i> 8/1/74		
DECIMALS ANGLES		APPD <i>[Signature]</i> 5/10/75		
XXX ±		FINISH	A.C. INPUT CONTROL (SCHEMATIC)	
MATERIAL		SCALE:	SIZE C	DWG NO. 13343-001
		REV B1		
		SHEET 1 OF 1		
SURFACE ROUGHNESS PER MIL-STD-10		DO NOT SCALE THIS DRAWING	WEIGHT	

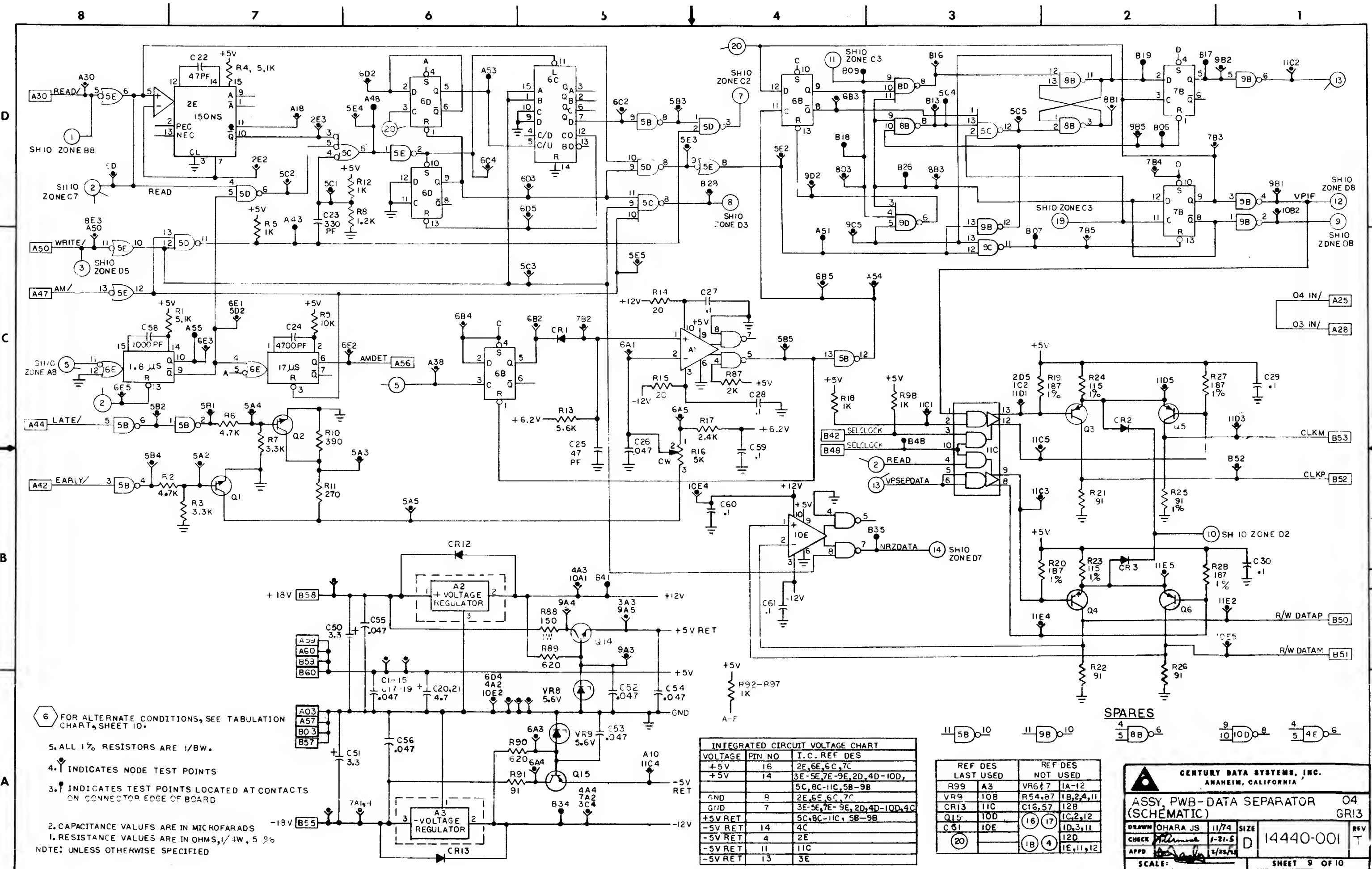


REVISIONS			
REV	ZONE	DESCRIPTION	APPROVAL & DATE
A		ENGINEERING RELEASE	B. Moody 8/28/74
B		DELETED R1, 200 & R2, 200; P32, D1 WAS P32, D6 N/A WAS: 13391-001 ADDED RC2 & RC3 ADDED R1, 2K, 1W	11/15/75
C		IS: J20-06 BRAKE PICK, CAN WAS: J20-06 BRAKE	D.L. 2/14/77
D		J23-01 TO J21-03 WAS TO J21-01	D.G.H. 8/5/77
D1		PROD RELEASE PER ECN 10654	D.G.H. 2/11/78
D2		REV PER RECORD CHG ECN 11126	JCN 4/11/78
E		REVISED PER ECO 12077	6/28/78
F		Changed wattage of R3 from 1/4W to 1W per ECO 13577	1/6/79

REF DES LAST USED		REF DES NOT USED	
C1	P32	J1-J19	
CR2	R3	P1-P31	
J23	RC1	R2	
K3			

MODEL NO. FIRST USE	T-50	NEXT ASSY FIRST USE	12452
UNLESS OTHERWISE SPECIFIED	DRAWN	B. Moody	8/28/74
DIMENSIONS ARE IN INCHES	CHECK	9/6/74	
TOLERANCES ON DECIMALS	APPD	8/10/75	
ANGLES	APPD	8/10/75	
XXX ±	FINISH		
MATERIAL	SCALE:	SIZE	DWG NO.
SURFACE ROUGHNESS PER MIL-STD-10	DO NOT SCALE THIS DRAWING	WEIGHT	13394-001
SCHEMATIC-RELAY ASSY.		REV	F

3. RELAY CONTACTS ARE SHOWN IN THE DE-ENERGIZED POSITION.
2. CAPACITANCE VALUE ARE IN MICROFARADS.
1. RESISTANCE VALUES ARE IN OHMS, 1/4W, 5% .
NOTE: UNLESS OTHERWISE SPECIFIED.



6 FOR ALTERNATE CONDITIONS, SEE TABULATION CHART, SHEET 10.

- 5. ALL 1% RESISTORS ARE 1/BW.
- 4. INDICATES NODE TEST POINTS
- 3. INDICATES TEST POINTS LOCATED AT CONTACTS ON CONNECTOR EDGE OF BOARD
- 2. CAPACITANCE VALUES ARE IN MICROFARADS
- 1. RESISTANCE VALUES ARE IN OHMS, 1/4W, 5%
- NOTE: UNLESS OTHERWISE SPECIFIED

INTEGRATED CIRCUIT VOLTAGE CHART			
VOLTAGE	PIN NO	I.C. REF	DES
+5V	16	2E, 6E, 6C, 7C	
+5V	14	3E, 5E, 7E, 9E, 2D, 4D, 10D, 4C	
GND	8	2E, 6E, 6C, 7C	
GND	7	3E, 5E, 7E, 9E, 2D, 4D, 10D, 4C	
+5V RET		5C, 8C, 11C, 5B, 9B	
-5V RET	14	4C	
-5V RET	4	2E	
-5V RET	11	11C	
-5V RET	13	3E	

REF DES	LAST USED	REF DES	NOT USED
R99	A3	VR6	7
VR9	10B	R54, 57	1B, 2, 4, 11
CR13	11C	C16, 57	12B
Q15	10D	IC2, 12	
C61	10E	1D, 3, 11	
		1E, 11, 12	

CENTURY DATA SYSTEMS, INC.
ANAHEIM, CALIFORNIA

ASSY, PWB-DATA SEPARATOR (SCHEMATIC) 04
GRI3

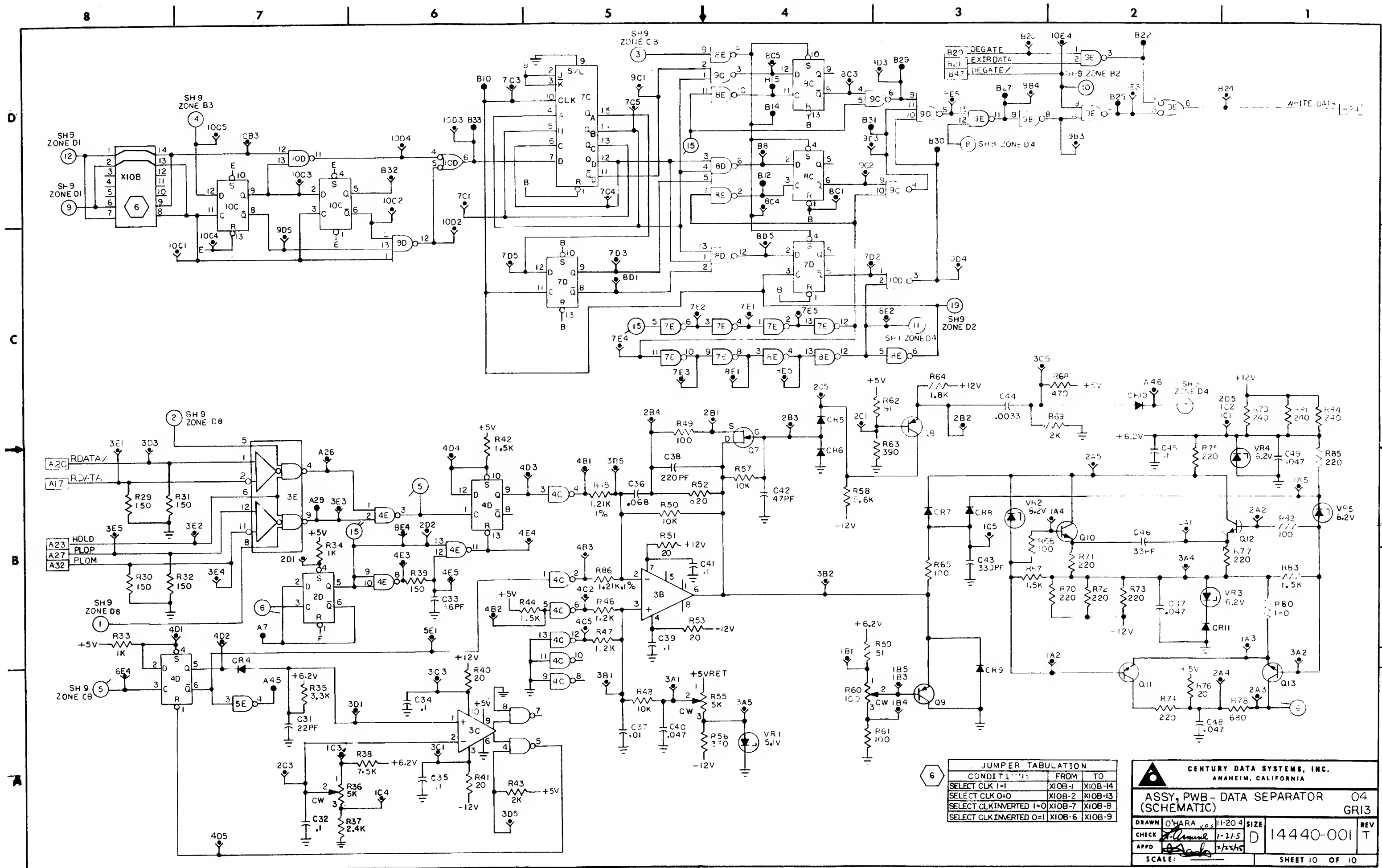
DRAWN OHARA JS 11/74

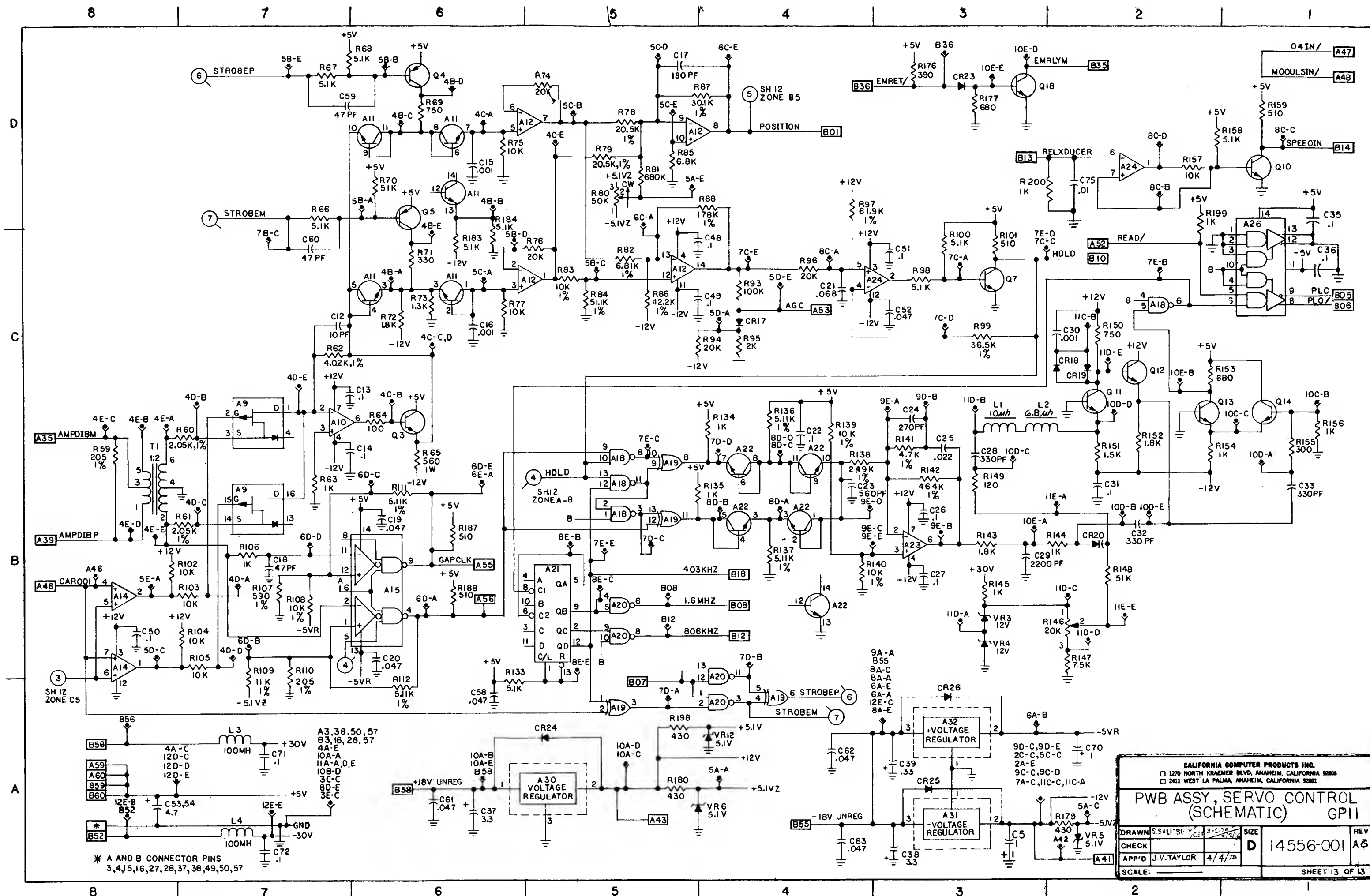
CHECK [Signature] 1-21-5

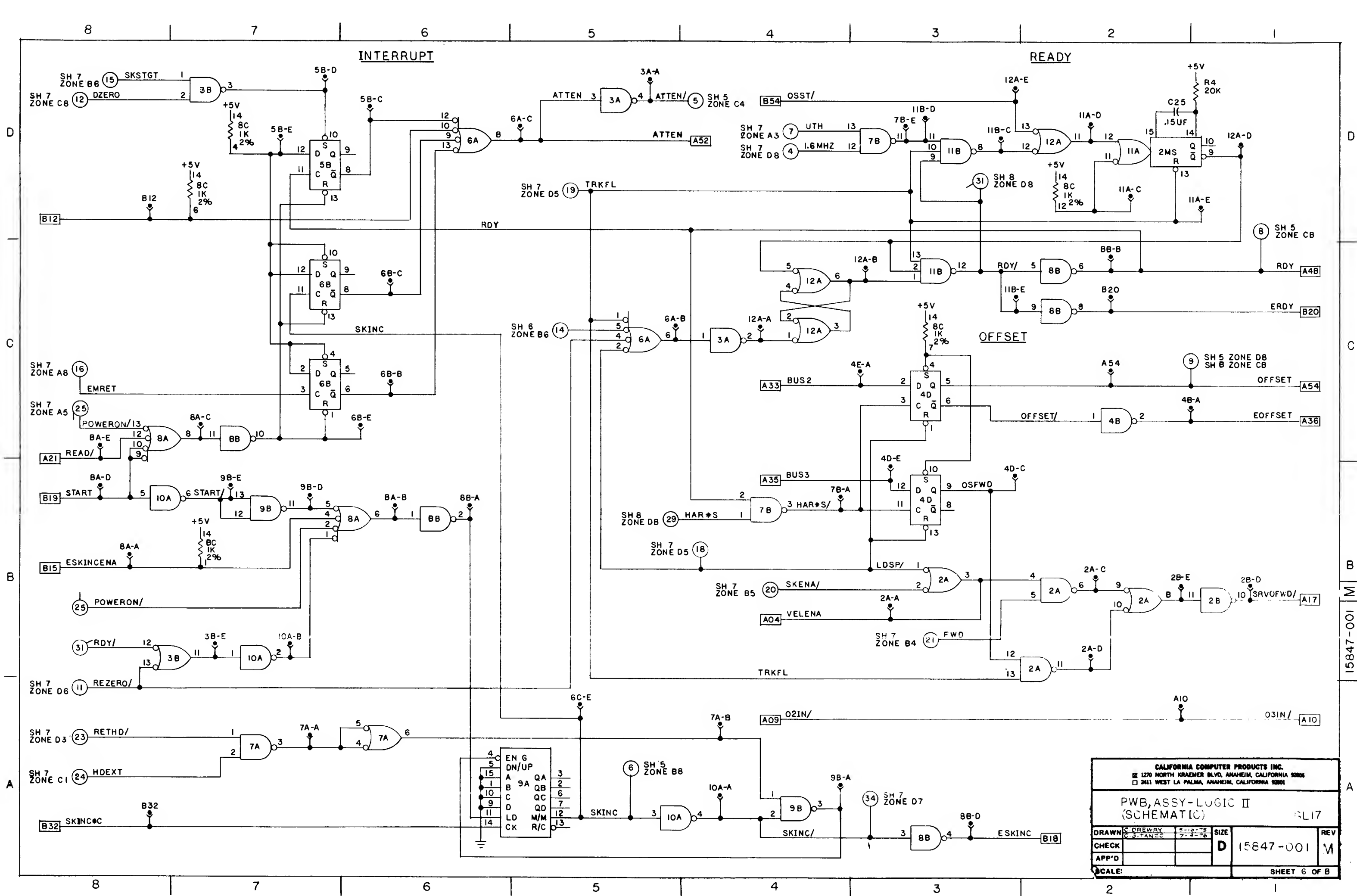
APPD [Signature] 1/25/75

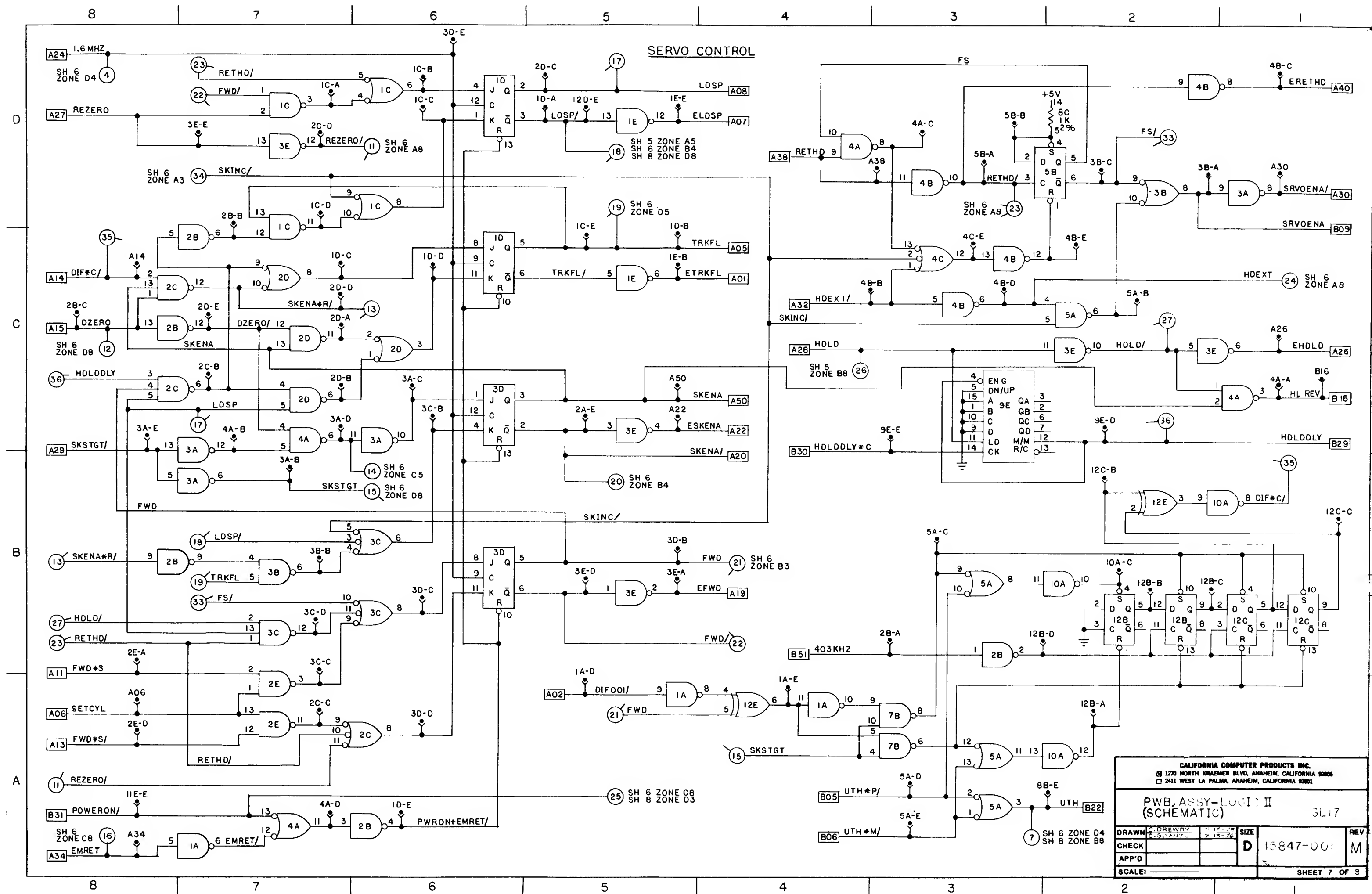
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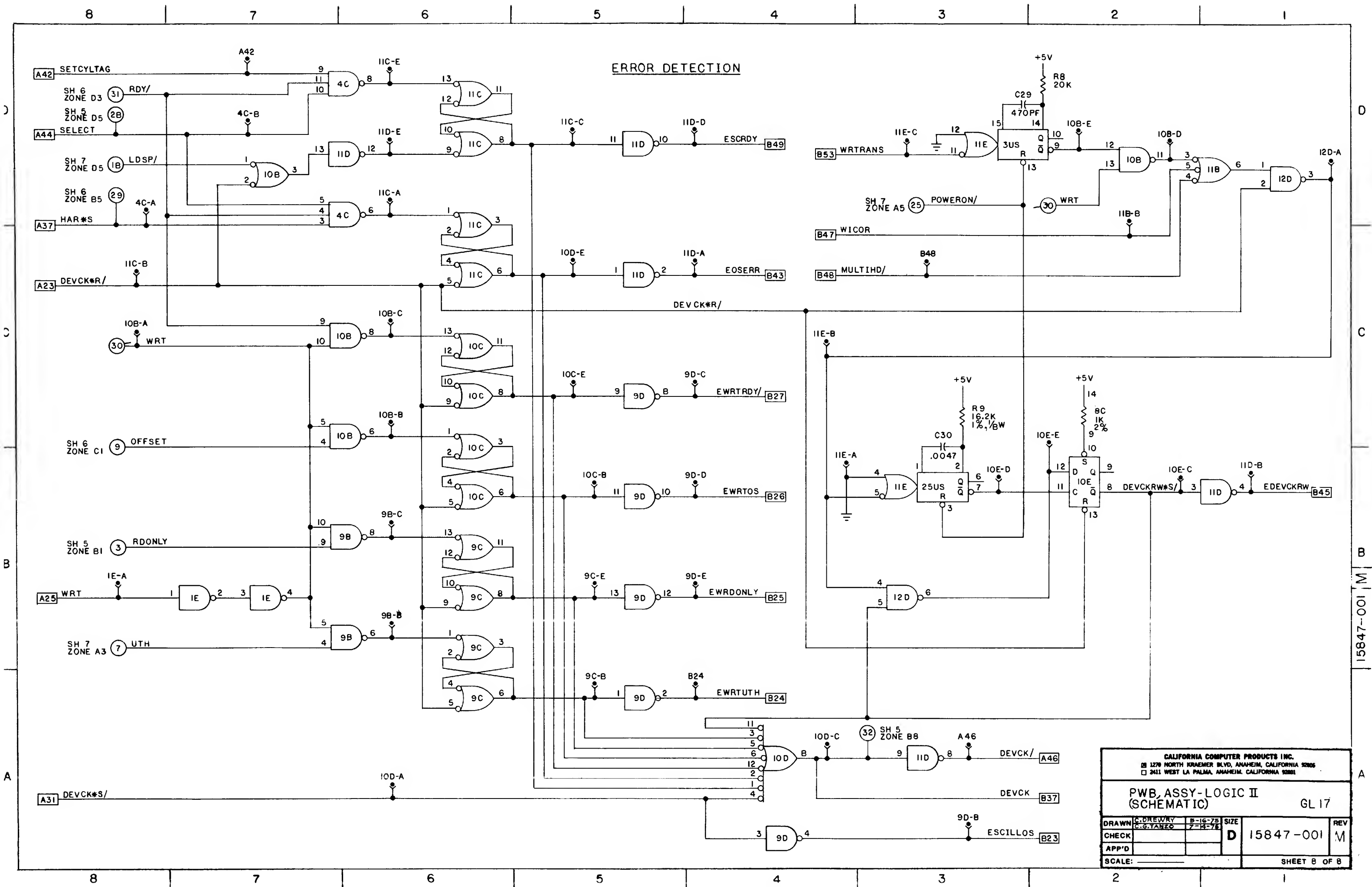
SHEET 9 OF 10

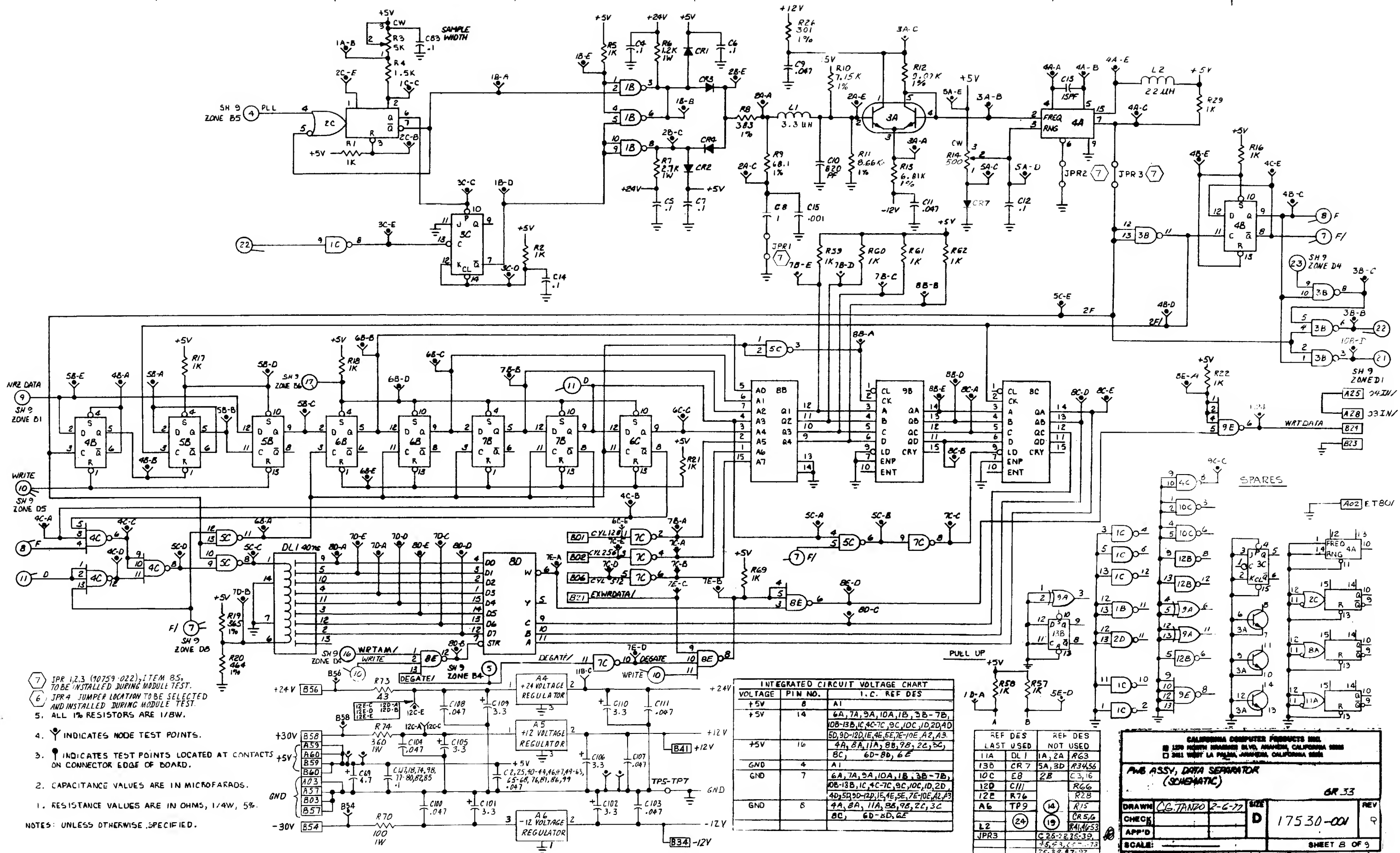


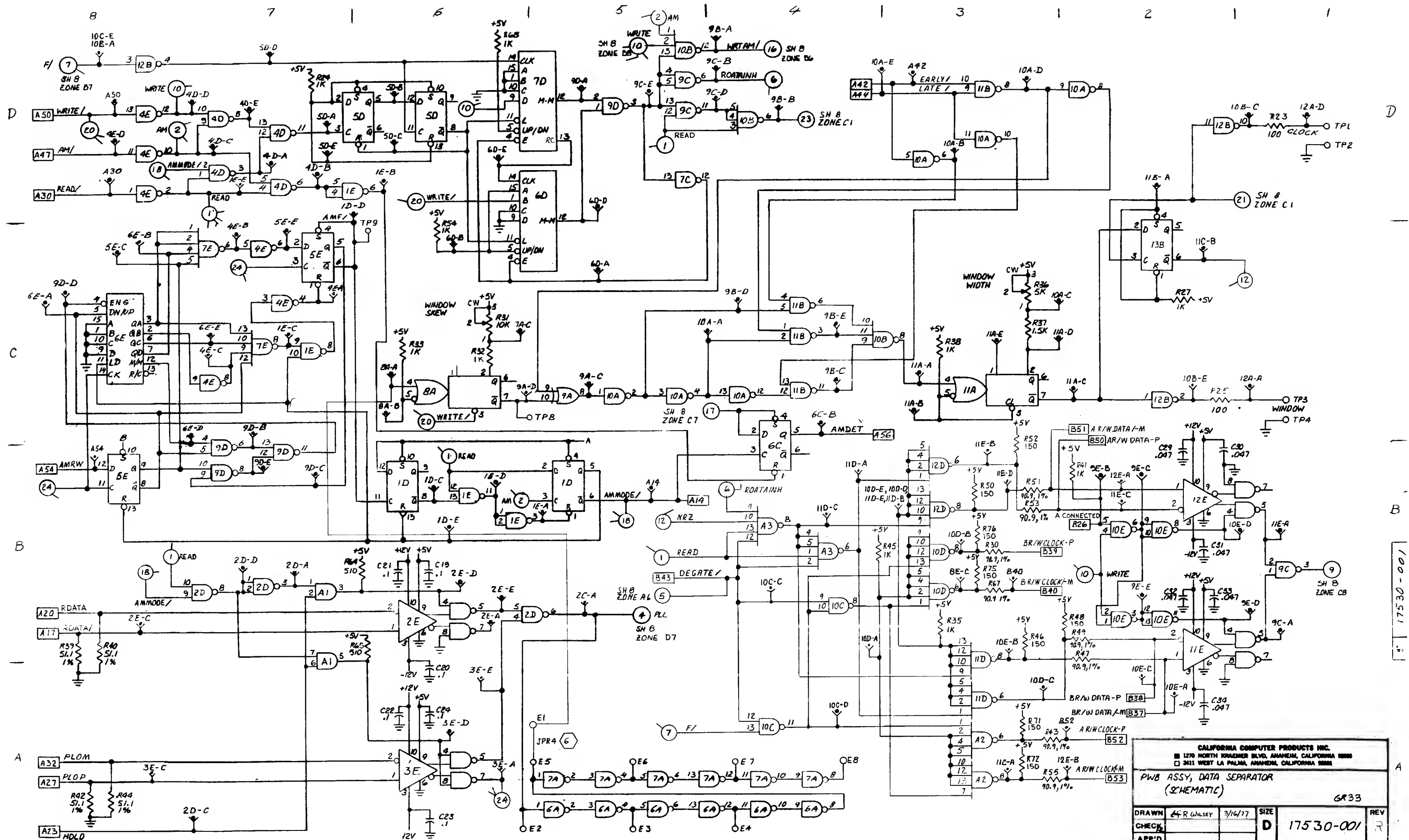




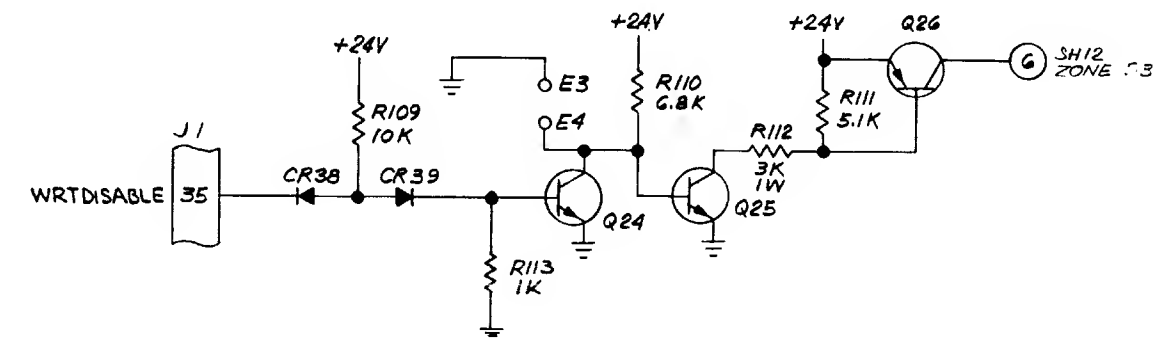
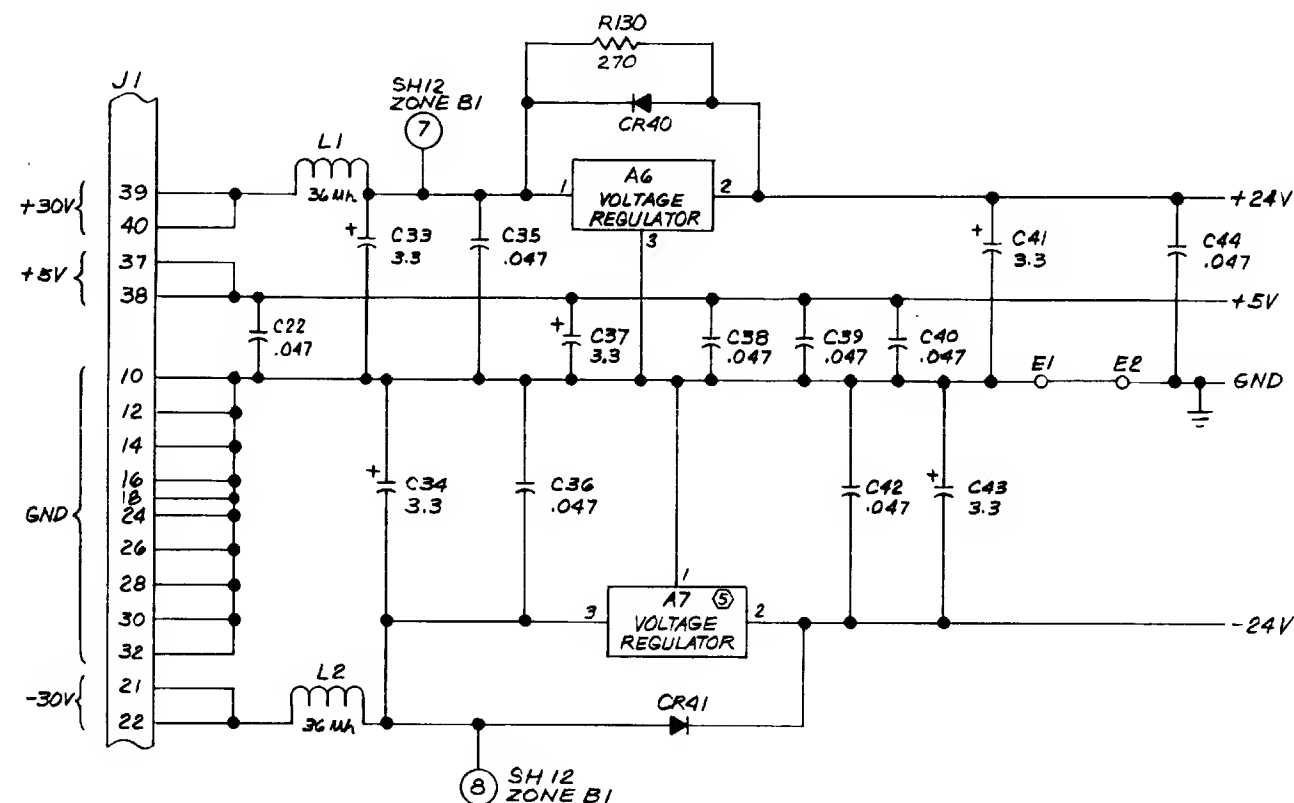




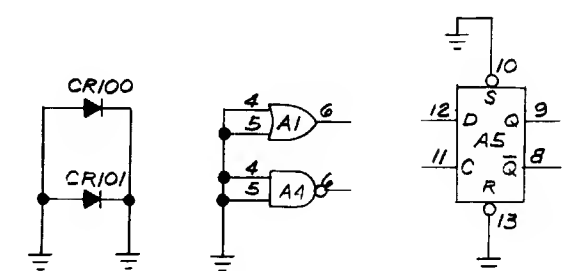




CALIFORNIA COMPUTER PRODUCTS INC. 1270 NORTH KRAEMER BLVD, ANAHEIM, CALIFORNIA 92805 3411 WEST LA PALMA, ANAHEIM, CALIFORNIA 92806			
PWB ASSY, DATA SEPARATOR (SCHEMATIC)			
GR33			
DRAWN	44-R WILSEY	3/16/77	SIZE
CHECK			D
APP'D			17530-001
SCALE: —			REV R
SHEET 9 OF 9			



SPARES



⑤ VALUE TO BE SELECTED IN TEST FROM 2.61K (90367-243), 3.65K (90367-292), 5.62K (90367-355), 11.5K (90367-125) OR OMIT. SELECT R129 FOR A HEAD WRITE CURRENT OF 84 MA. MIN., 892 MA. MAX. FLAT-TO-FLAT, AT CYL B14. IF A7 (ITEM 12) IS REPLACED, CHECK HEAD WRITE CURRENT AND RESELECT R129 IF REQUIRED.

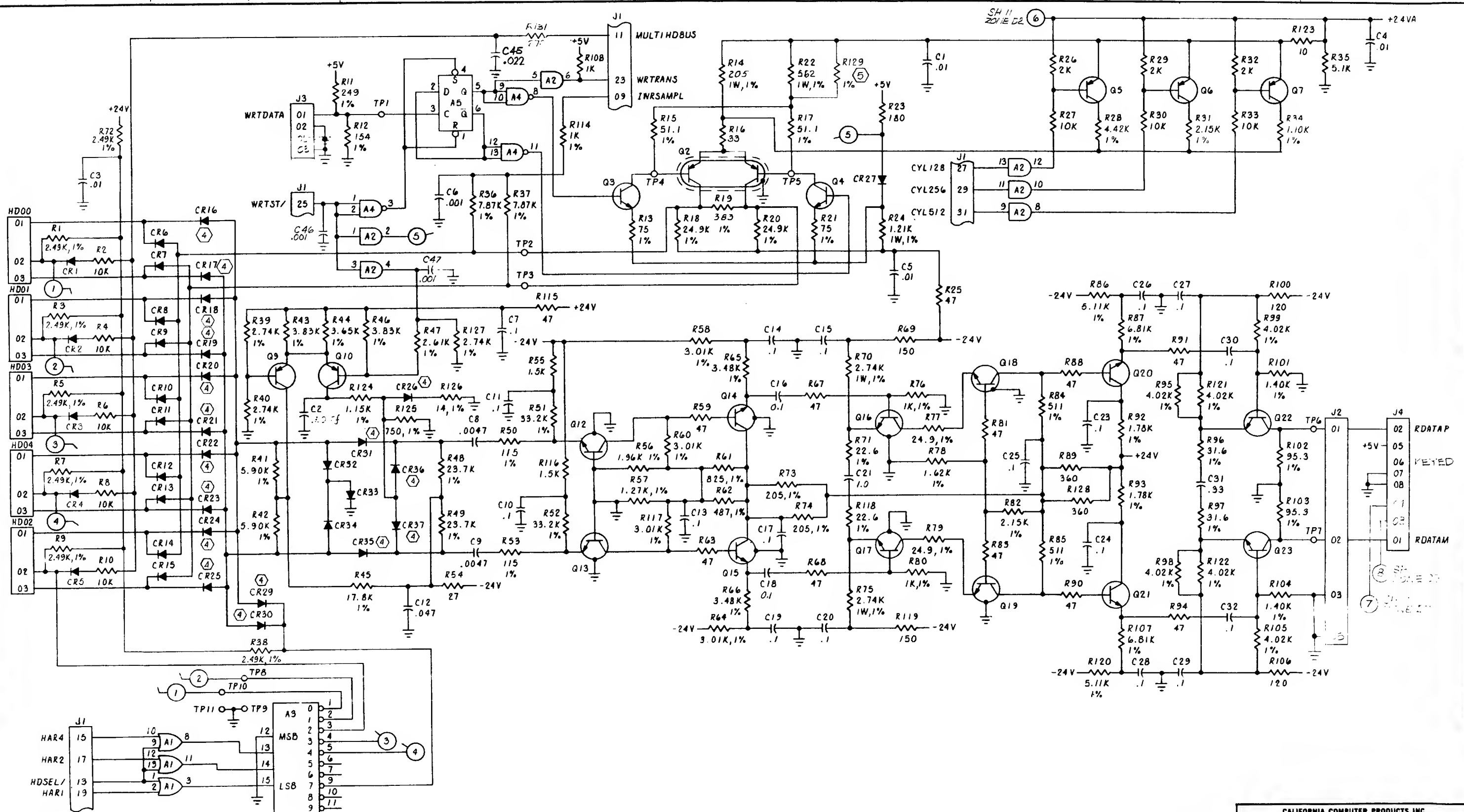
④ DIODES CR16-26, 29-31, 35-37 TO BE BATCH MATCHED, $\Delta V_f = 10\text{mV}$. FOR FIELD REPLACEMENT OF MATCHED DIODES, ITEMS 26 & 27, USE SPARE DIODES CR100 OR CR101, ITEM 29, FROM THE SAME PWB ASSY. WHEN CR100 AND CR101 ARE USED UP, FURTHER FIELD REPLACEMENT REQUIRES REPLACEMENT OF ALL DIODES, ITEMS 26, 27, 28 & 29 TO MAINTAIN MATCHING OF THE DIODES ON A GIVEN PWB ASSY.

3. ALL 1% RESISTOR ARE 1/8W.
2. CAPACITANCE VALUES ARE IN MICROFARADS.
1. RESISTANCE VALUES ARE IN OHMS, 1/4 W, 5%
NOTES: UNLESS OTHERWISE SPECIFIED.

INTEGRATED CIRCUIT VOLTAGE CHART		
VOLTAGE	PIN NO.	I.C. REF. DESIGNATOR
+5V	14	A1, A2, A4, A5
+5V	16	A3
GND	7	A1, A2, A4, A5
GND	8	A3

REF. DES. LAST USED	REF. DES. NOT USED
A7	R131
C47	TP11
CR101	HD04
E4	L2
J4	CR42-99
Q26	⑧

CALIFORNIA COMPUTER PRODUCTS INC. 1270 NORTH KRAEMER BLVD, ANAHEIM, CALIFORNIA 92806 2411 WEST LA PALMA, ANAHEIM, CALIFORNIA 92801			
PWB ASSY, READ/WRITE MATRIX (SCHEMATIC) VR89			
SCALE:	SIZE	M	
	D	17589-001	
C.G. TANZO 5/20/77 SHEET 11 of 12			

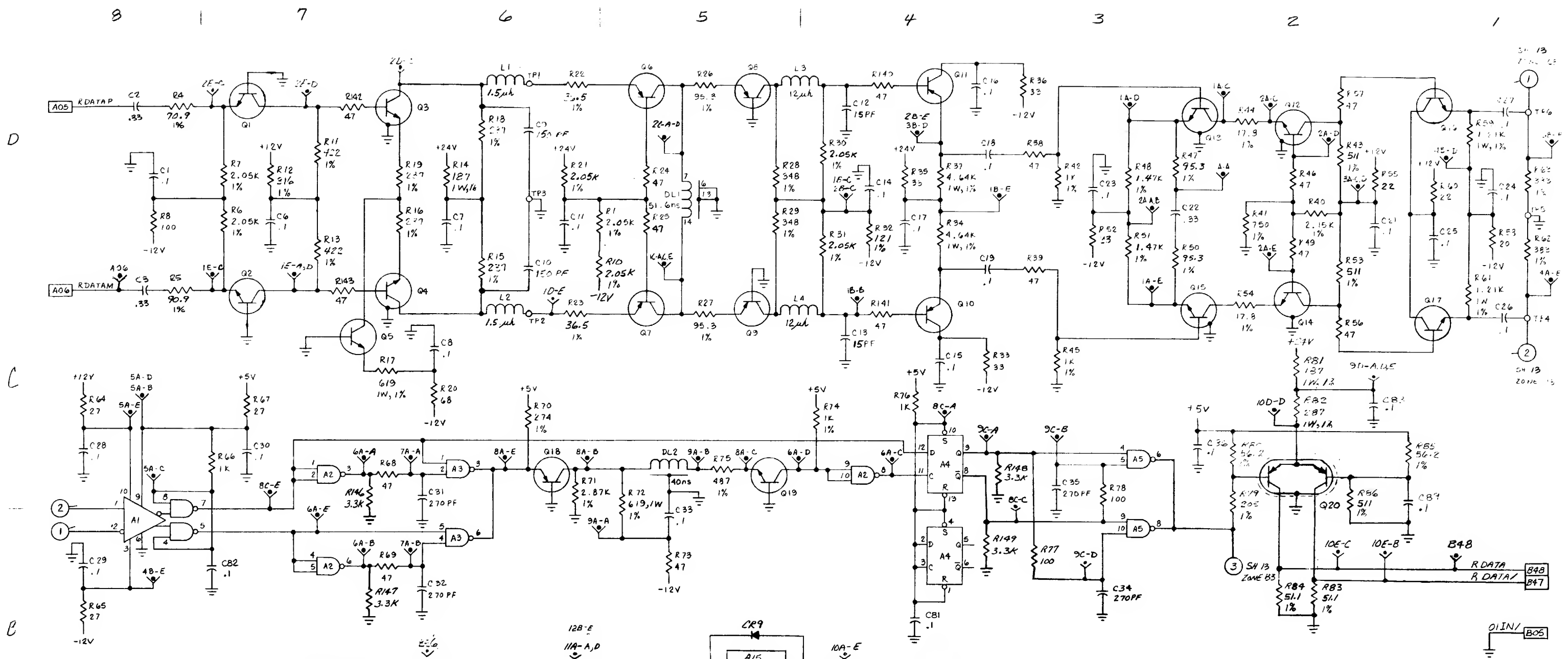


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 1270 NORTH KRAEMER BLVD, ANAHEIM, CALIFORNIA 92806
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PWB ASSY, READ/ WRITE
 MATRIX (SCHEMATIC) VR89

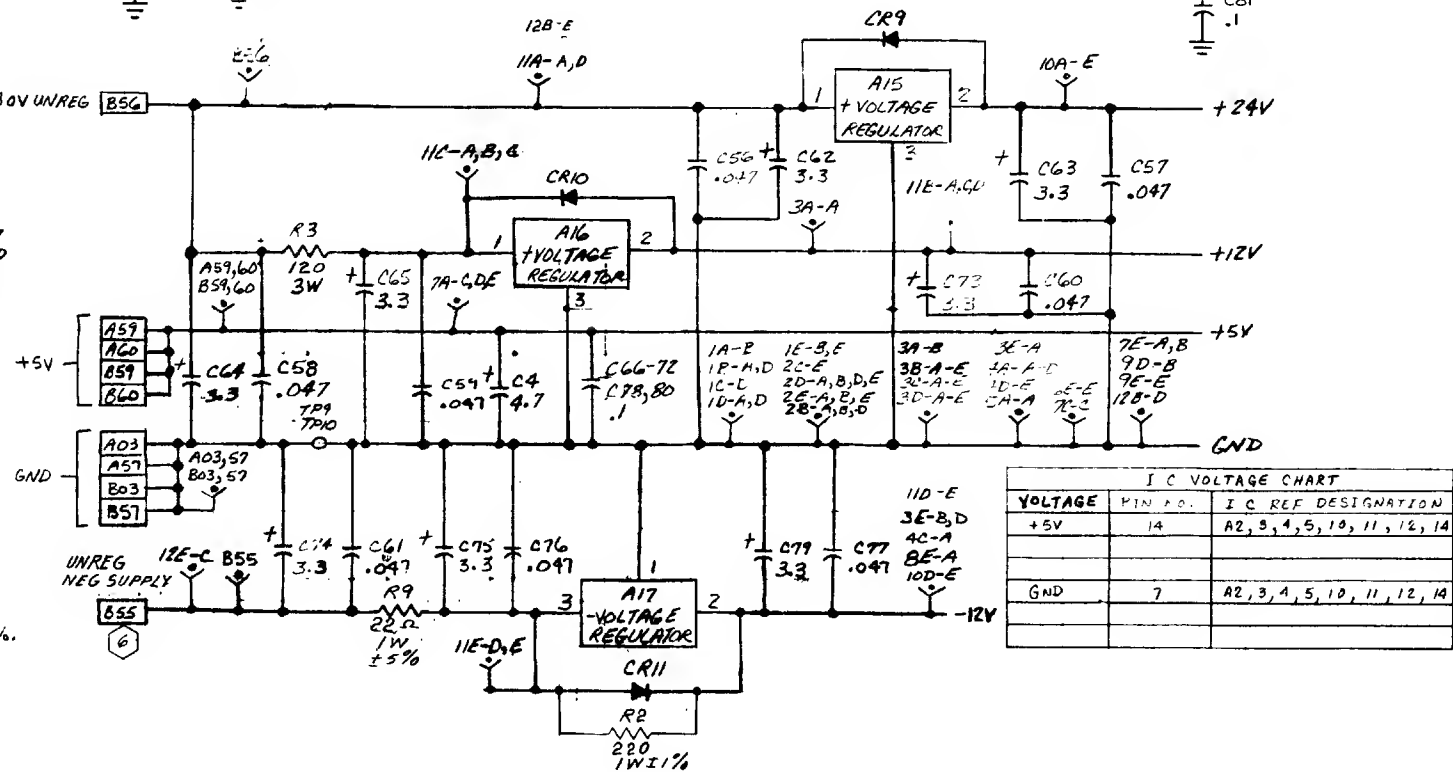
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SHEET 12 of 12



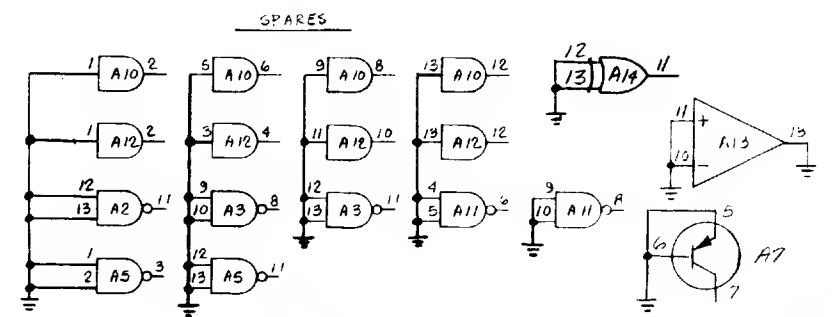
6. CONNECTED TO -30 VDC THRU DROPPING RESISTOR.
5. FOR RESISTOR SELECTION PROCESS SEE SPEC 19507 START SELECTION PROCESS WITH NO RESISTOR AND PROCEED THRU DECREASING RESISTANCE VALUES. IF A6 IS REPLACED ENTIRE SELECTION PROCESS MUST BE REPEATED. VALUE TO BE SELECTED IN TEST FROM 1K (90367-103), 562 (90367-354), 365 (90367-291), 274 (90367-249), 205 (90367-207), 169 (90367-179), 133 (90367-144), 110 (90367-116), 95.3 (90367-429) & 78.7 (90367-402).

4. Y INDICATES NODE TEST POINT.
3. ALL 1% RESISTORS ARE 1/8W.
2. CAPACITANCE VALUES ARE IN MICROFARADS.
1. RESISTANCE VALUES ARE IN OHMS, 1/4W, 5%.
- NOTE: UNLESS OTHERWISE SPECIFIED



I C VOLTAGE CHART		
VOLTAGE	PIN NO.	I C REF DESIGNATION
+5V	14	A2, 3, 4, 5, 10, 11, 12, 14
GND	7	A2, 3, 4, 5, 10, 11, 12, 14

REF DES	LAST USED	REF DES	NOT USED
C89	A153	C5, 20	C81
Q25	DL2	C50-51	R87
Q41	A17	CR8	R144
A6	Q2, 22	R145	
R11	(3)	CR5	R130



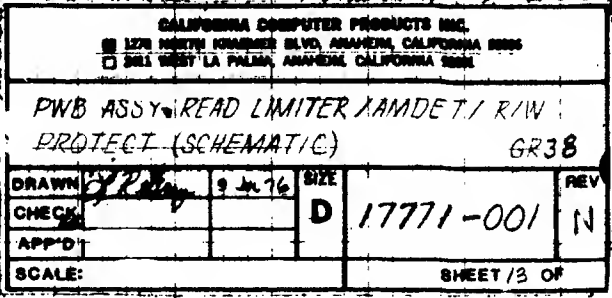
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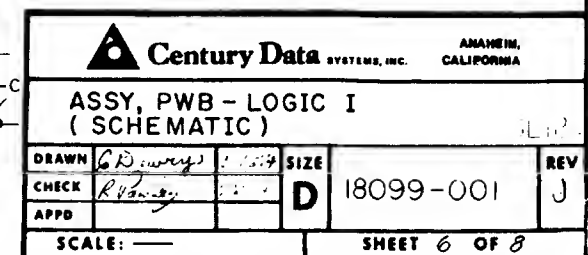
PWB ASSY, READ LIMITER / AMDET / R/W
PROTECT (SCHEMATIC) GR38

DRAWN	CHECK	APP'D	SCALE	SIZE	REV
				D	N

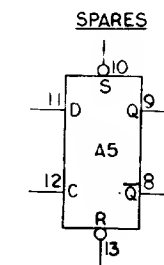
17771-001

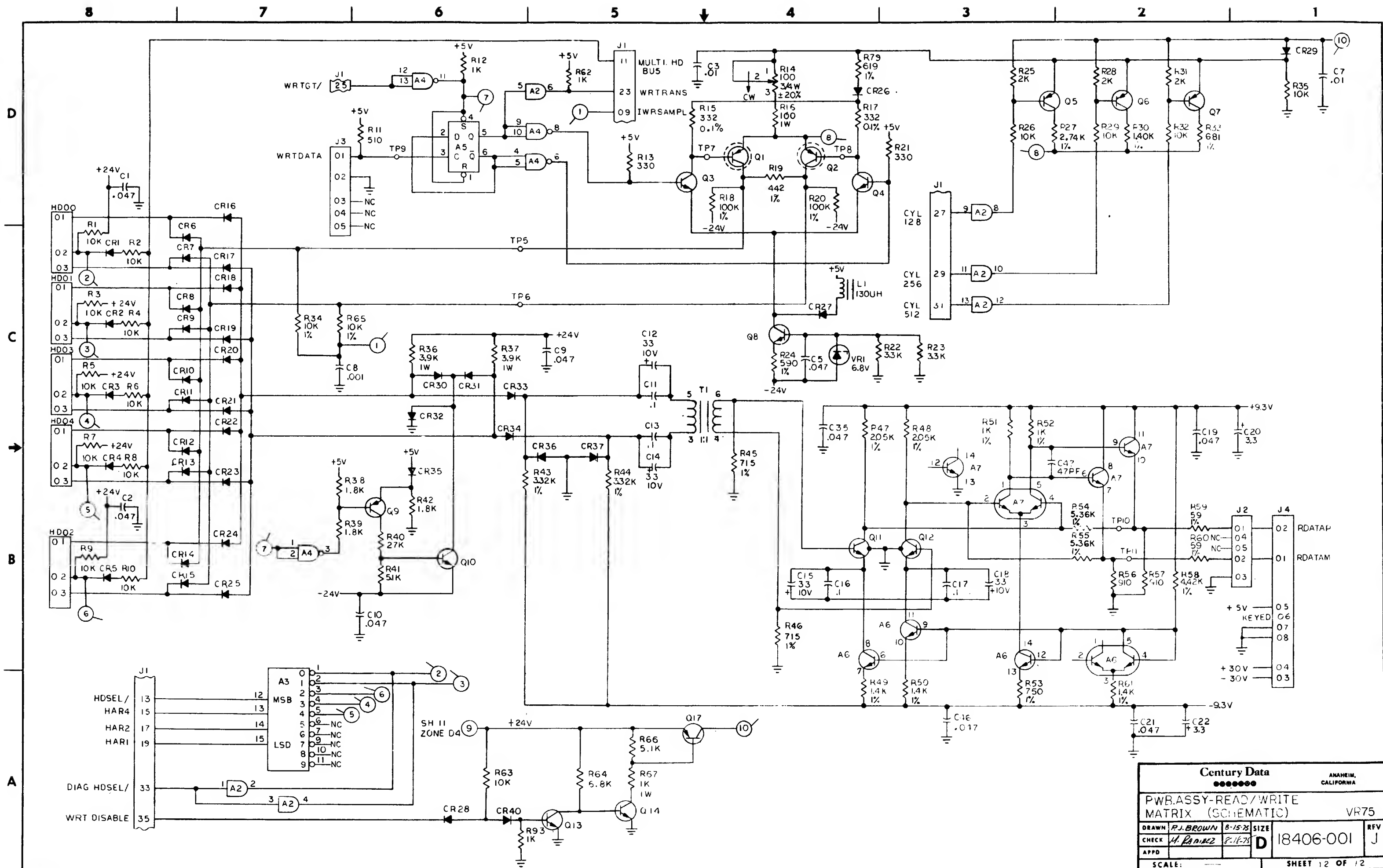
SHEET 12 OF

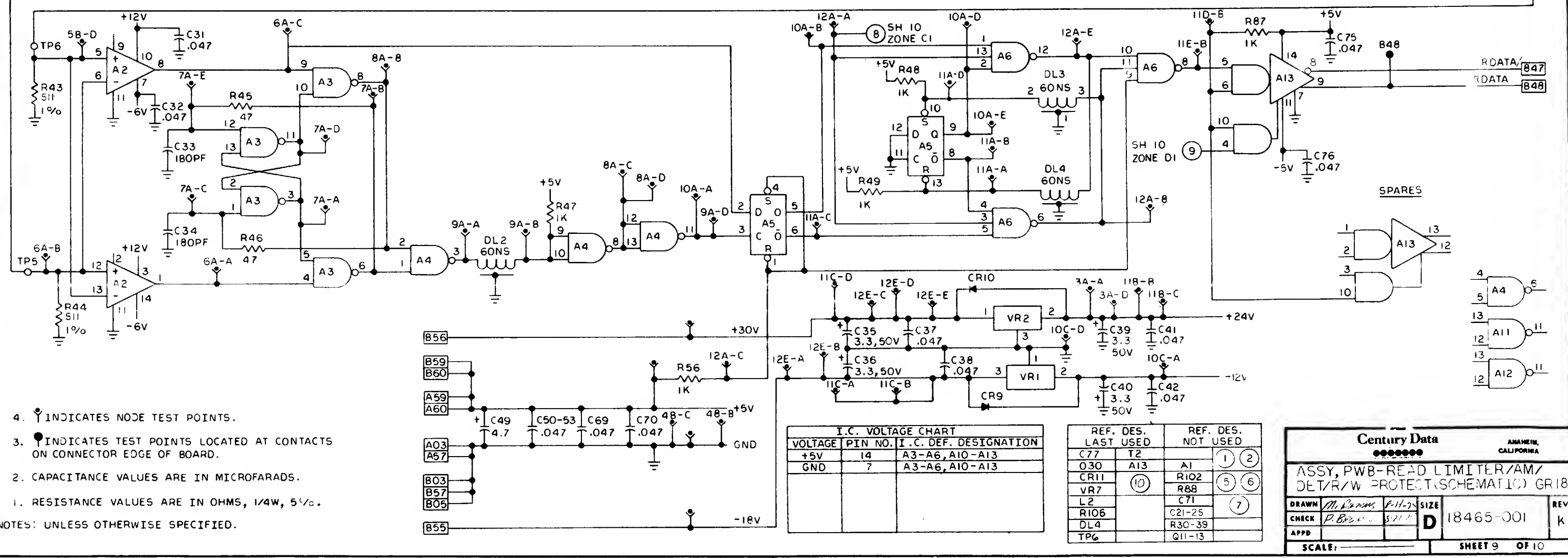
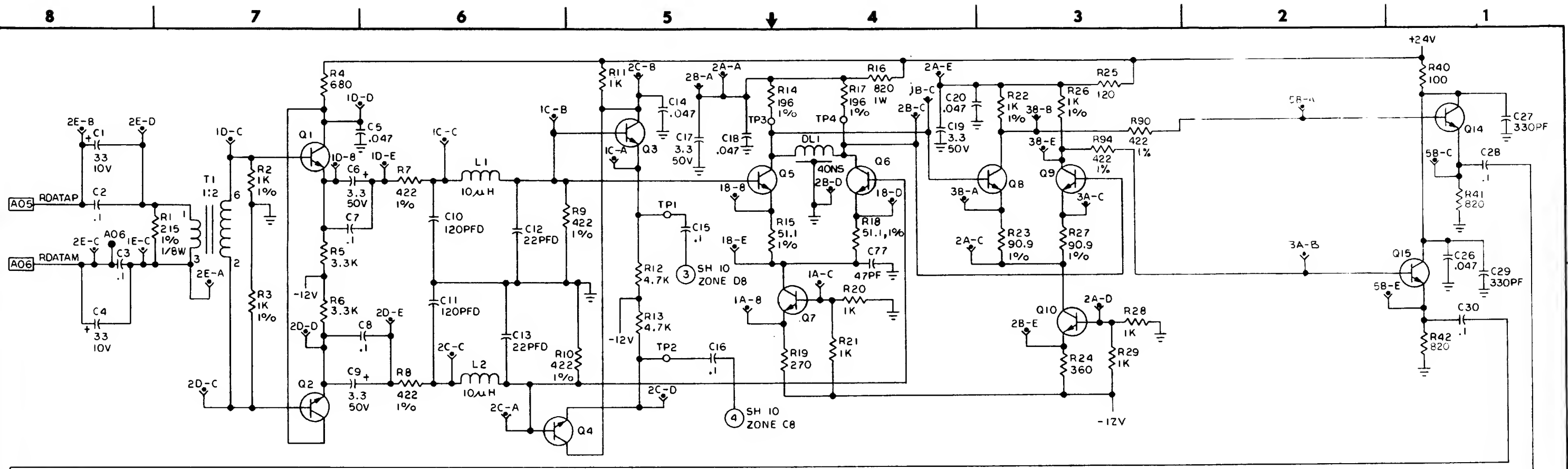












4. INDICATES NODE TEST POINTS.
3. INDICATES TEST POINTS LOCATED AT CONTACTS ON CONNECTOR EDGE OF BOARD.
2. CAPACITANCE VALUES ARE IN MICROFARADS.
1. RESISTANCE VALUES ARE IN OHMS, 1/4W, 5%.
- NOTES: UNLESS OTHERWISE SPECIFIED.

I.C. VOLTAGE CHART		
VOLTAGE	PIN NO.	I.C. DEF. DESIGNATION
+5V	14	A3-A6, A10-A13
GND	7	A3-A6, A10-A13

REF. DES.	DES.	REF. DES.
LAST USED		NOT USED
C77	T2	(1) (2)
O30	A13	A1
CR11	(10)	R102
VR7		R88
L2		C71
R106		C21-25
DL4		R30-39
TP6		Q11-13

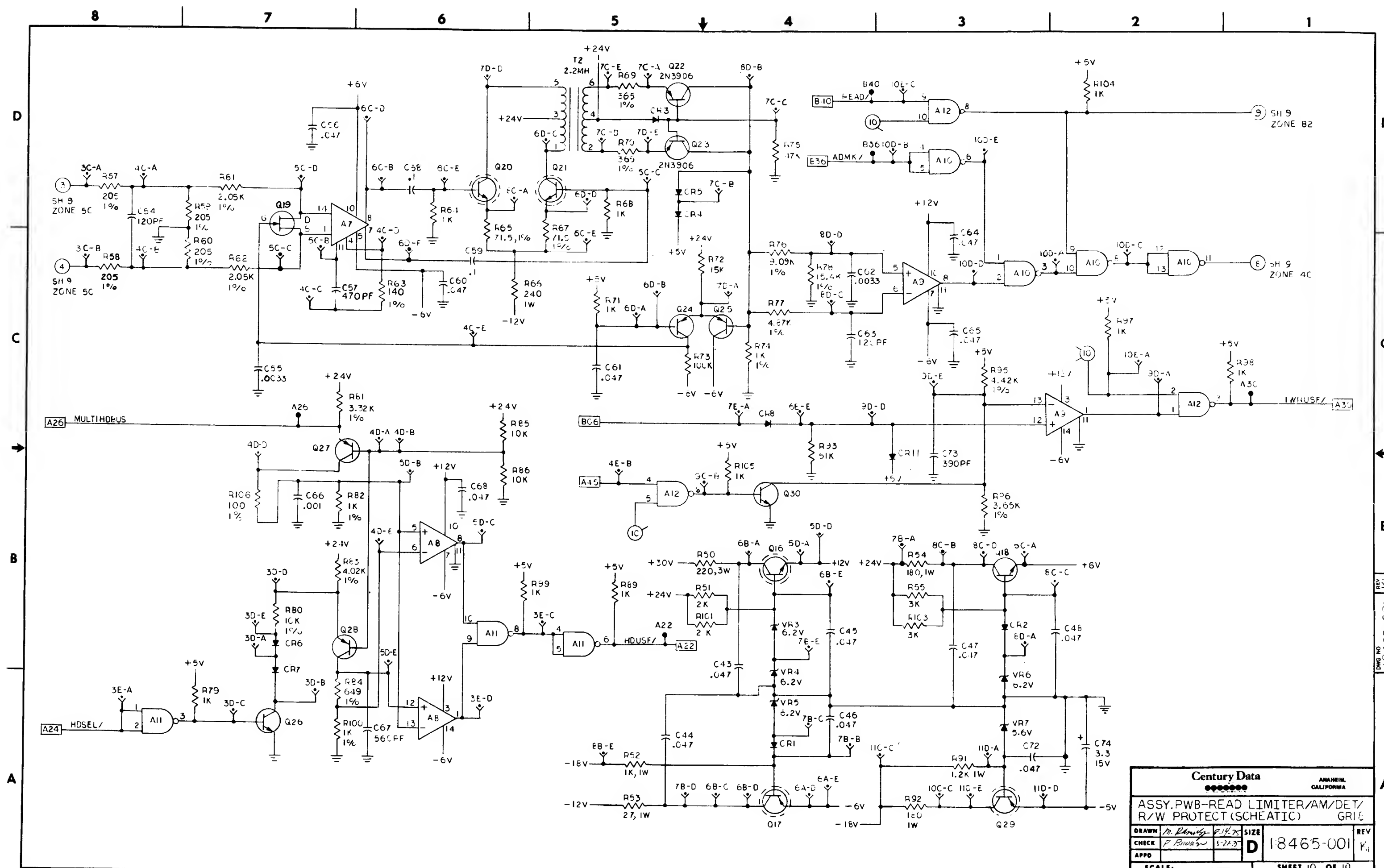
Century Data
ANAHEIM, CALIFORNIA

ASSY, PWB-READ LIMITER/AM/DET/R/W PROTECT (SCHEMATIC) GR18

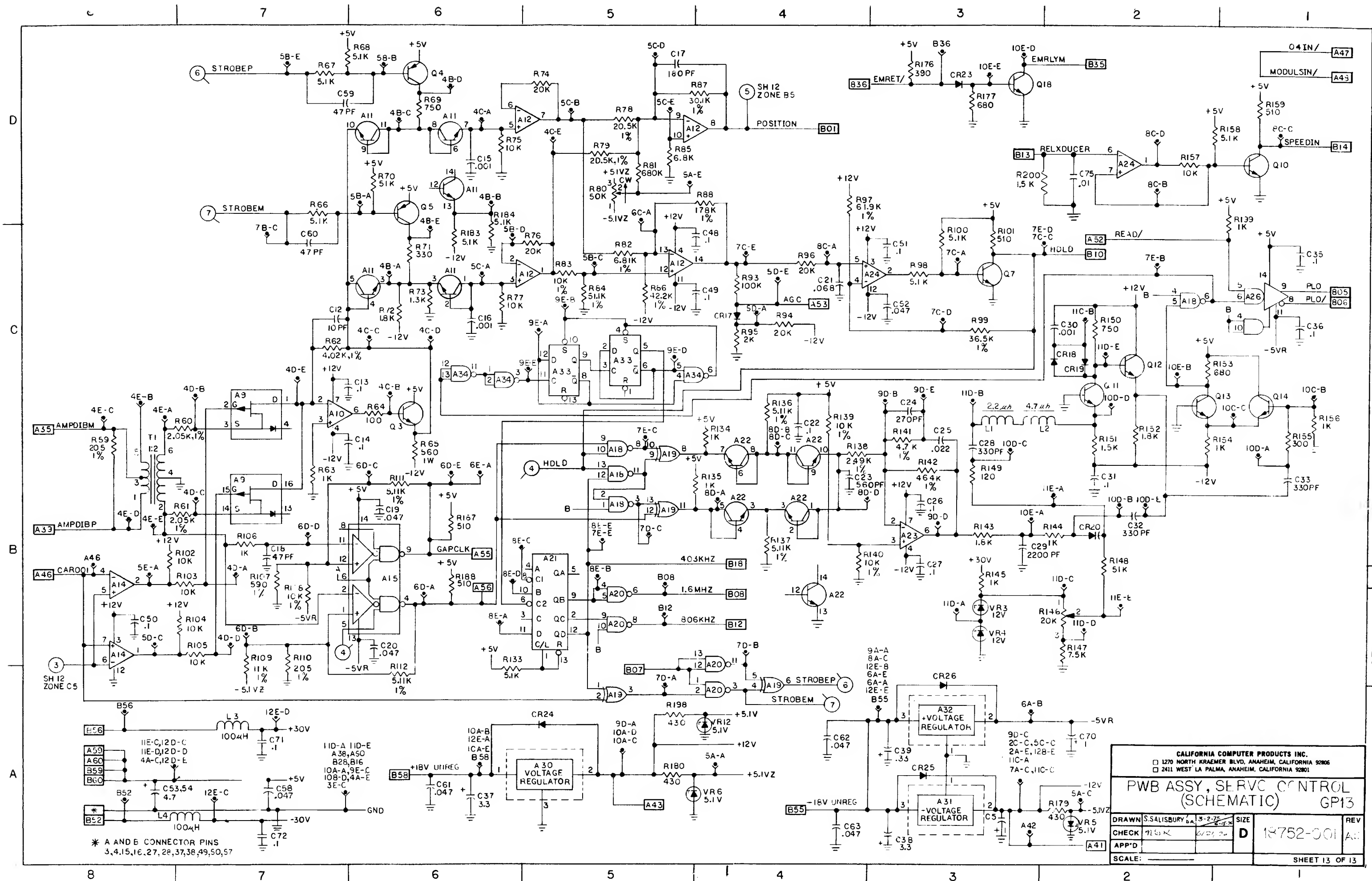
DRAWN: M. P. 11-78
CHECK: P. B. 11-78
APPD: D

SIZE: 18465-001
REV: K

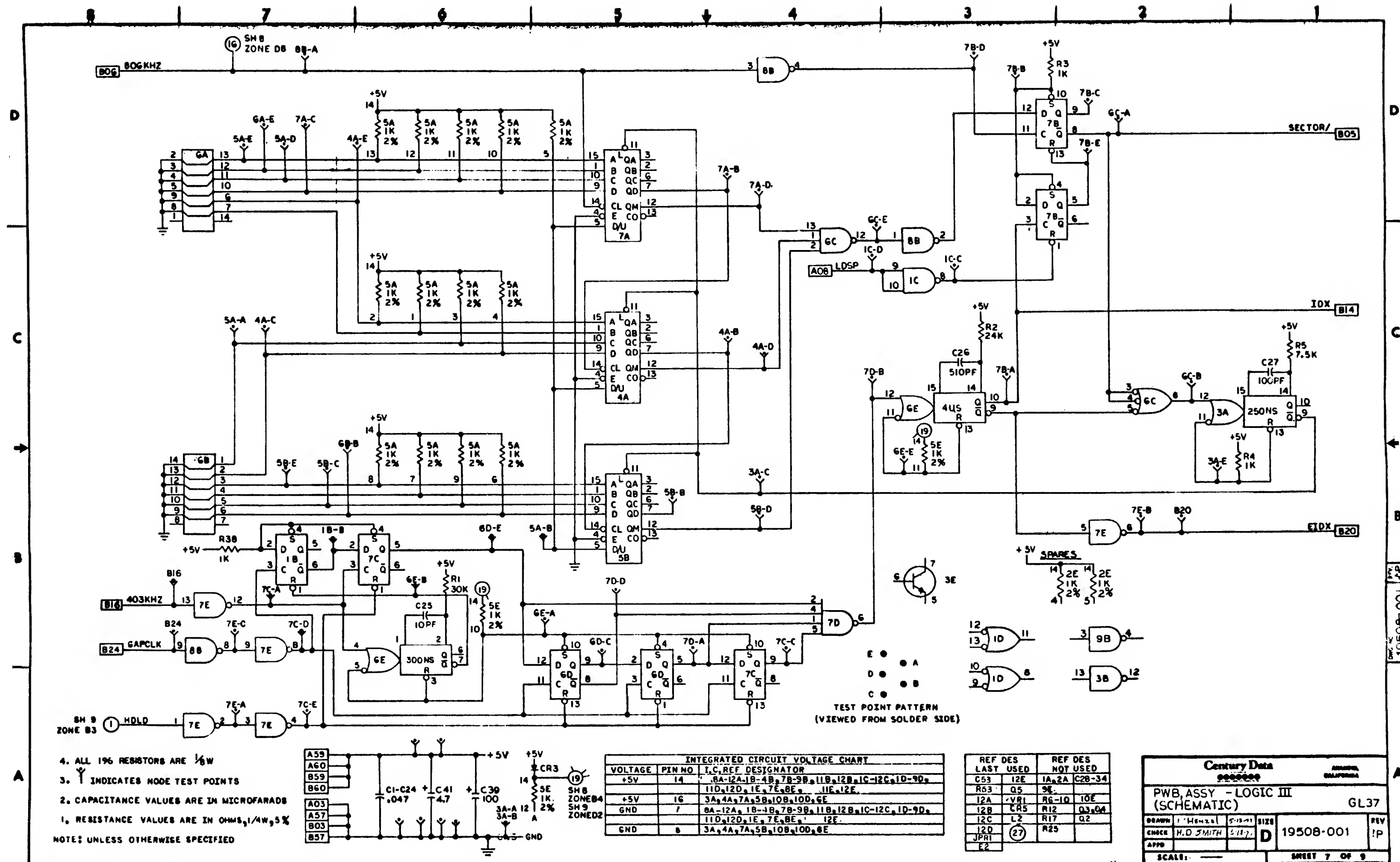
SCALE: SHEET 9 OF 10

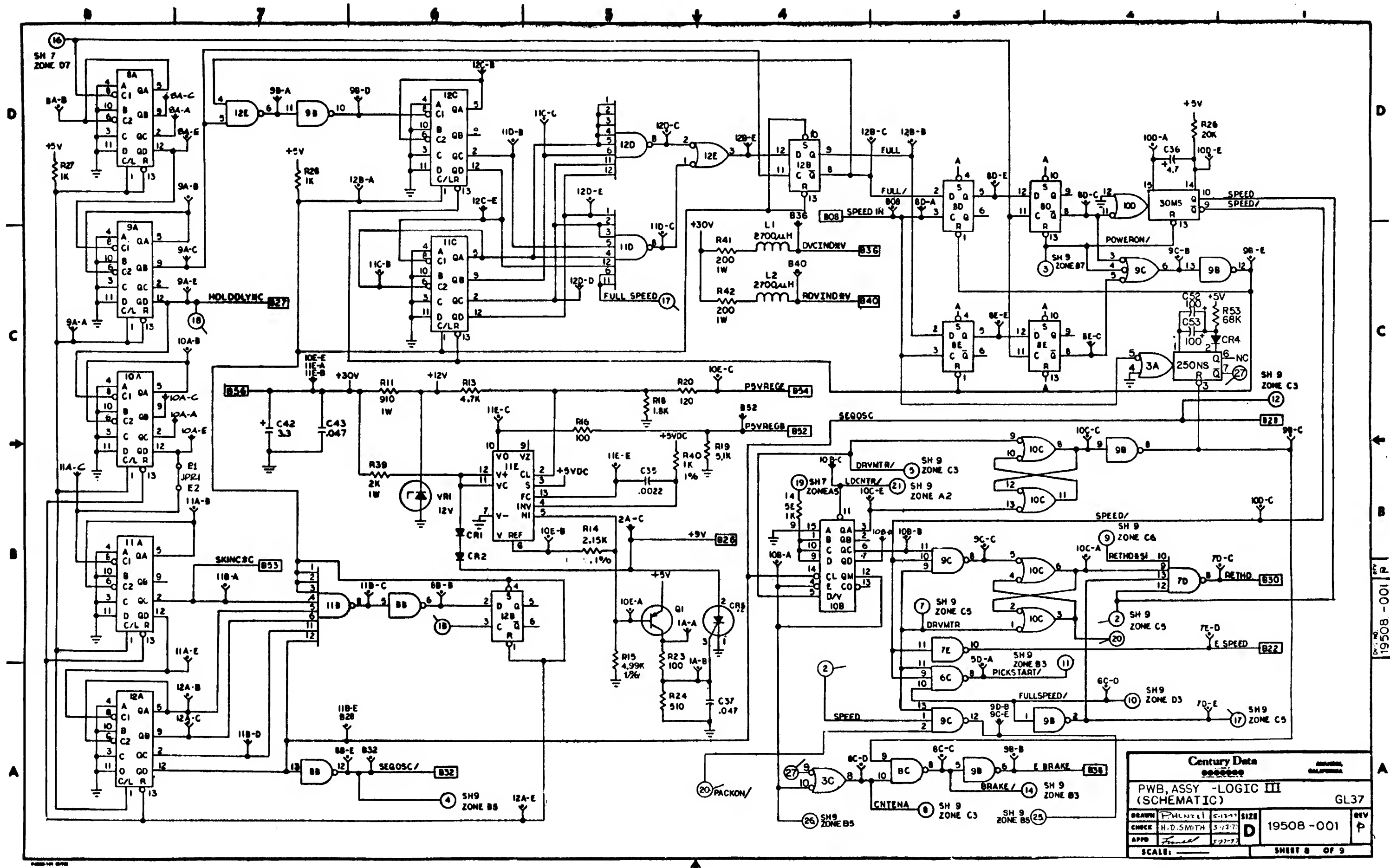


Century Data		ANAHEIM, CALIFORNIA	
ASSY. PWB-READ LIMITER/AM/DET/R/W PROTECT (SCHEMATIC) GRI 1			
DRAWN	M. Blawie	DATE	8-14-75
CHECK	P. Brown	DATE	5-21-75
APPD		DATE	
SCALE: 1:1		SHEET 10 OF 10	

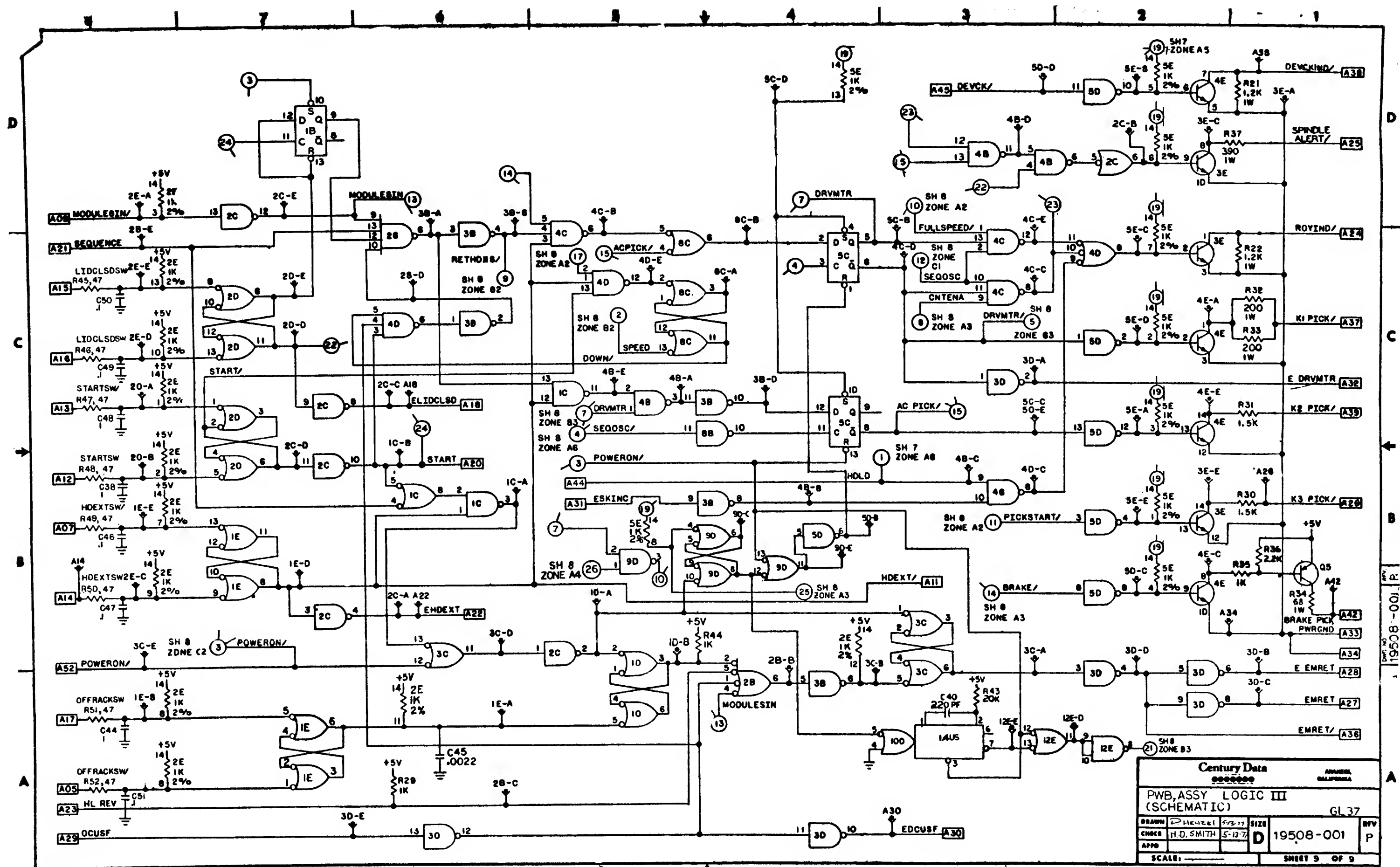


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PWB ASSY, SERVC CONTROL (SCHEMATIC) GP13			
DRAWN	S.SALISBURY	3-2-75	SIZE
CHECK	MSK	6/2/75	D
APP'D			
SCALE:			REV
			18752-001
			SHEET 13 OF 13

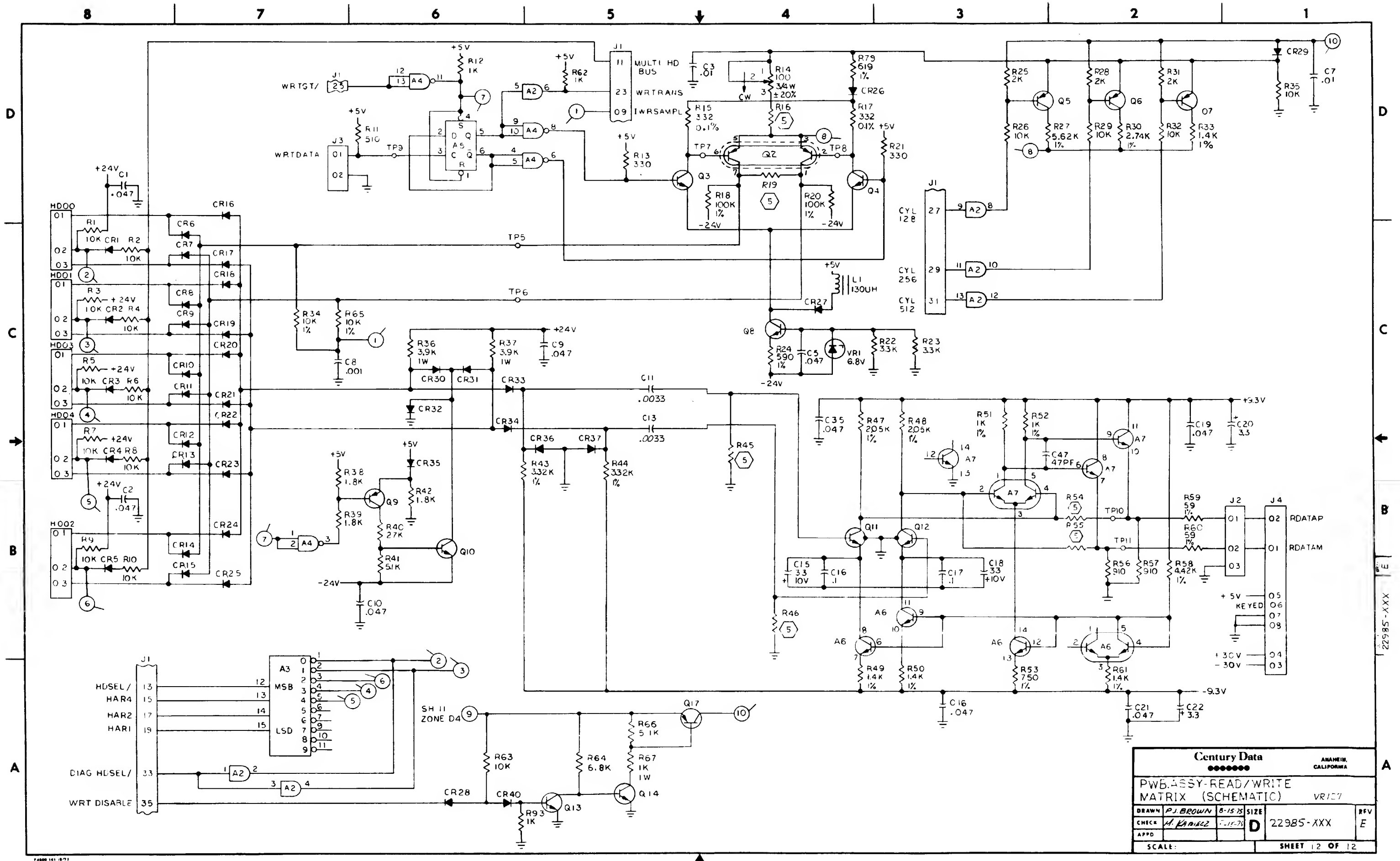




Century Data		ANALOG	
PWB, ASSY - LOGIC III		GL37	
(SCHEMATIC)		19508-001	
DRAWN	PHILZEL	5-13-77	SIZE
CHECK	H.D. SMITH	5-13-77	D
APPD	PHILZEL	5-13-77	REV
SCALE: 1" = 1"		SHEET 8 OF 9	



Century Data				ARABIAN, CALIFORNIA	
PWB, ASSY LOGIC III (SCHEMATIC)					
				GL 37	
DRAWN	PHENEL	5-17-77	SIZE	19508-001	REV
CHECK	H.D. SMITH	5-17-77	D		P
APPD					
SCALE:				SHEET 9 OF 9	



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